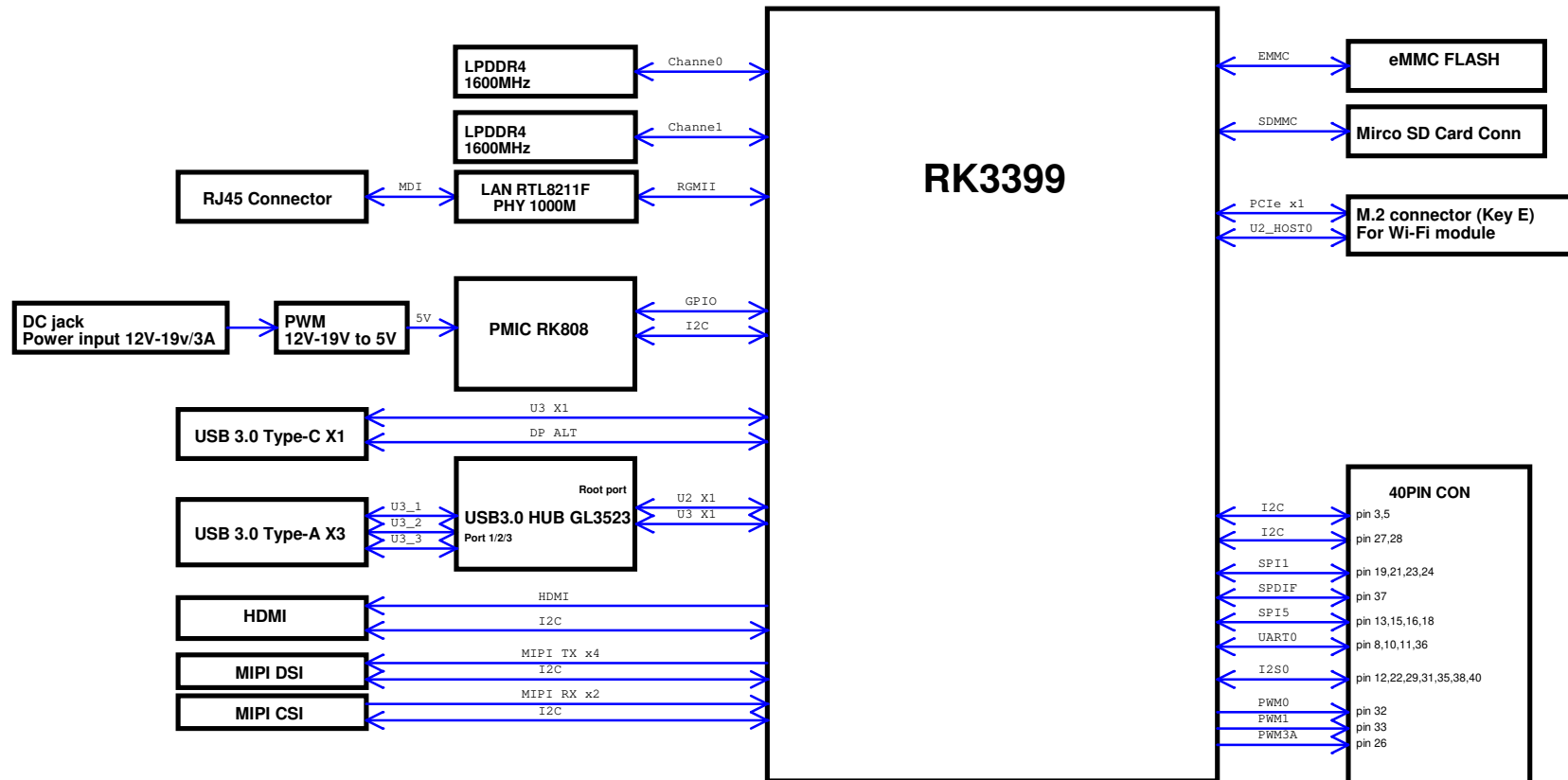


TINKER BOARD 2 Block Diagram



I2C Block Diagram

Port	Pin name	Domain	Bus name	Pull-up voltage	Slave Device	Slave Addr (MS 7Bits)	Note
I2C0	GPIO1_B7/SPI3_RXD/I2C0_SDA GPIO1_C0/SPI3_TXD/I2C0_SCL	PMUIO2	PMIC_I2C_SDA PMIC_I2C_SCL	VCC_3V0	Rockchip RK808-D	0x1b	PMIC
					FAN53555BUC23X	0xC0	DC-DC BUCK
I2C1	GPIO4_A1/I2C1_SDA GPIO4_A2/I2C1_SCL	APIO5	CSI_I2C_SDA CSI_I2C_SCL	VCC_3V0	MIPI_CSI		Camera
I2C2	GPIO2_A0/VOP_D0/CIF_D0/I2C2_SDA GPIO2_A1/VOP_D1/CIF_D1/I2C2_SCL	APIO2	RESERVE	VCC_3V0			
I2C3	GPIO4_C0/I2C3_SDA/UART2B_RX GPIO4_C1/I2C3_SCL/UART2B_TX	APIO4	HDMI_I2C_SDA HMDI_I2C_SCL	VCC_3V0			HDMI conn
I2C4	GPIO1_B3/I2C4_SDA GPIO1_B4/I2C4_SCL	PMUIO2	CC_I2C_SDA CC_I2C_SCL	VCC_3V0	Fairchild FUSB302B	0x44,0x46	USB-TypeC Mux
					FAN53555BUC23X	0xC0	DC-DC BUCK
I2C5	GPIO3_B3/MAC_CLK/I2C5_SCL GPIO3_B2/MAC_RXER/I2C5_SDA	APIO1	RESERVE	+3V_DUAL			
I2C6	GPIO2_B1/SPI2_RXD/CIF_HREF/I2C6_SDA GPIO2_B2/SPI2_TXD/CIF_CLKIN/I2C6_SCL	APIO2	40_I2C6_SDA 40_I2C6_SCL	VCC_3V0			GPIO 40 PIN
I2C7	GPIO2_A7/VOP_D7/CIF_D7/I2C7_SDA GPIO2_B0/VOP_CLK/CIF_VSYNC/I2C7_SCL	APIO2	40_I2C7_SDA 40_I2C7_SCL	VCC_3V0			GPIO 40 PIN
I2C8	GPIO1_C4/I2C8_SDA GPIO1_C5/I2C8_SCL	PMUIO2	EE_I2C_SDA EE_I2C_SCL	VCC_3V0	MIPI_DSI		Panel
					EEPROM		EEPROM

<Variant Name>

		Title :	
ASUSTeK COMPUTER INC		Engineer:	
Size	Project Name	Rev	
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Tinker Board 2

[illegible][illegible]

CSI conn

The diagram illustrates the CSI connection between the processor and the FPC_CON_15P connector. The processor pins are labeled as follows:

- MIP1_RX0, D0_N
- MIP1_RX0, D0_P
- MIP1_RX0, D1_N
- MIP1_RX0, D1_P
- MIP1_RX0_CLK_N
- MIP1_RX0_CLK_P
- CSIO_GPIO
- CSIO_SCL
- CSIO_SDA

The FPC_CON_15P connector pins are labeled as follows:

- 15
- 14
- 13
- 12
- 11
- 10
- 9
- 8
- 7
- 6
- 5
- 4
- 3
- 2
- 1

The diagram also shows the internal components of the processor, including the CSIO block, the CSIO_GPIO pin, the CSIO_SCL pin, the CSIO_SDA pin, and the CSIO block. The diagram is a detailed schematic showing the electrical connection between the processor and the FPC_CON_15P connector.

The diagram illustrates the DSI connection between the DSI controller and the display panel. The DSI controller is connected to the display panel via the DSI IC_SDA and DSI IC_SCL signals. The power supply VCC_3V3_DSI is connected to the DSI controller and the display panel. The DSI controller is also connected to the display panel via the MIPI TX0 signals (D0_P, D1_N, CLK_P, D2_N, D3_P). The DSI controller is connected to the display panel via the DSI IC_SDA and DSI IC_SCL signals. The power supply VCC_3V3_DSI is connected to the DSI controller and the display panel. The DSI controller is also connected to the display panel via the MIPI TX0 signals (D0_P, D1_N, CLK_P, D2_N, D3_P).

SD card slot

VCC3V3_SD

F2 1.1A/BV

VCC3V3 SD S0 CON

C203 10UF/6.3V

C204 0.1UF/16V

GND

SDMMC0_D2

SDMMC0_D3

SDMMC0_CMD

SDMMC0_CLKO

SDMMC0_D0

SDMMC0_D1

SDMMC0_DET#

C_S0MMC0_D2

C_S0MMC0_D3

C_S0MMC0_CMD

C_S0MMC0_D0

C_S0MMC0_D1

C_S0MMC0_DET#

SD_CARD

SD_SOCKET_3P

GND

<Variant Name>

ASUS Title :

ASUSTek COMPUTER INC Engineer:

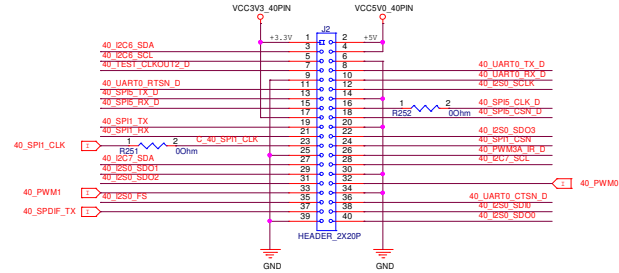
Size Project Name

Tinker Board 2

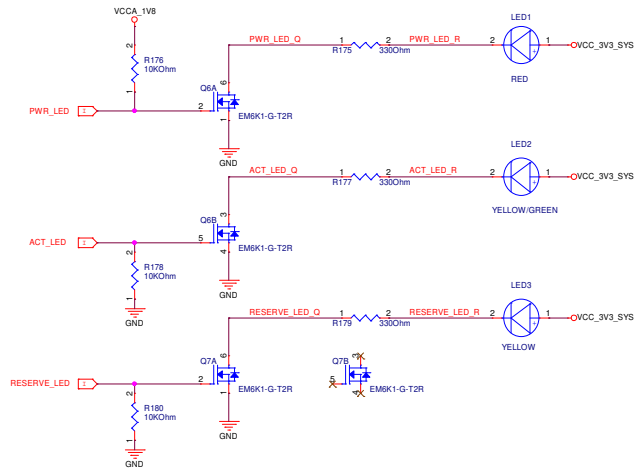
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Rev

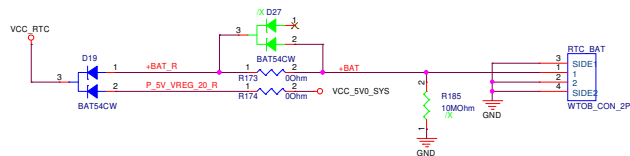
40 pin header



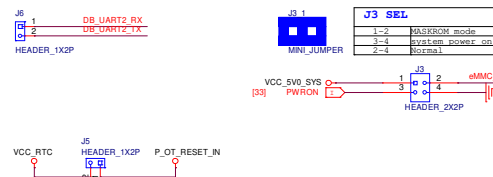
LED



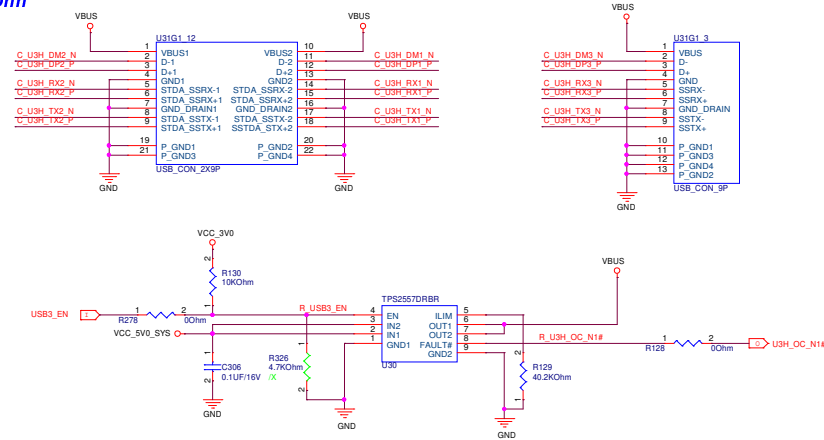
Battery conn



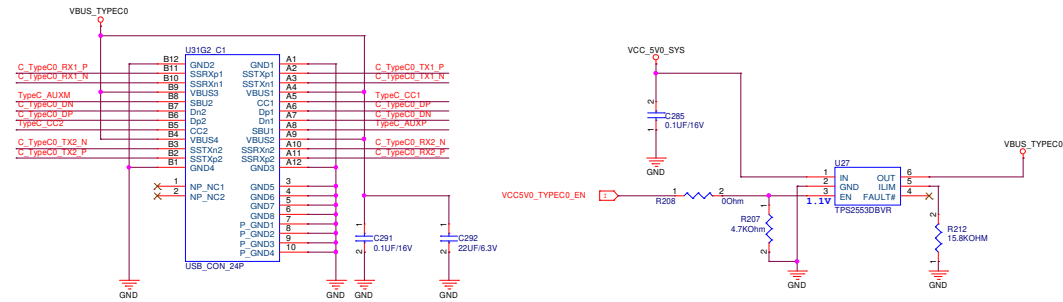
Jumper



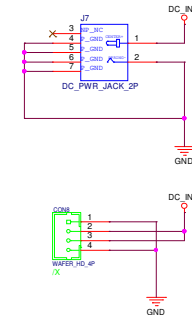
USB3 type A conn



USB3 type C conn



DC input



FAN

