



Radxa ROCK S0

schematic

V1.2

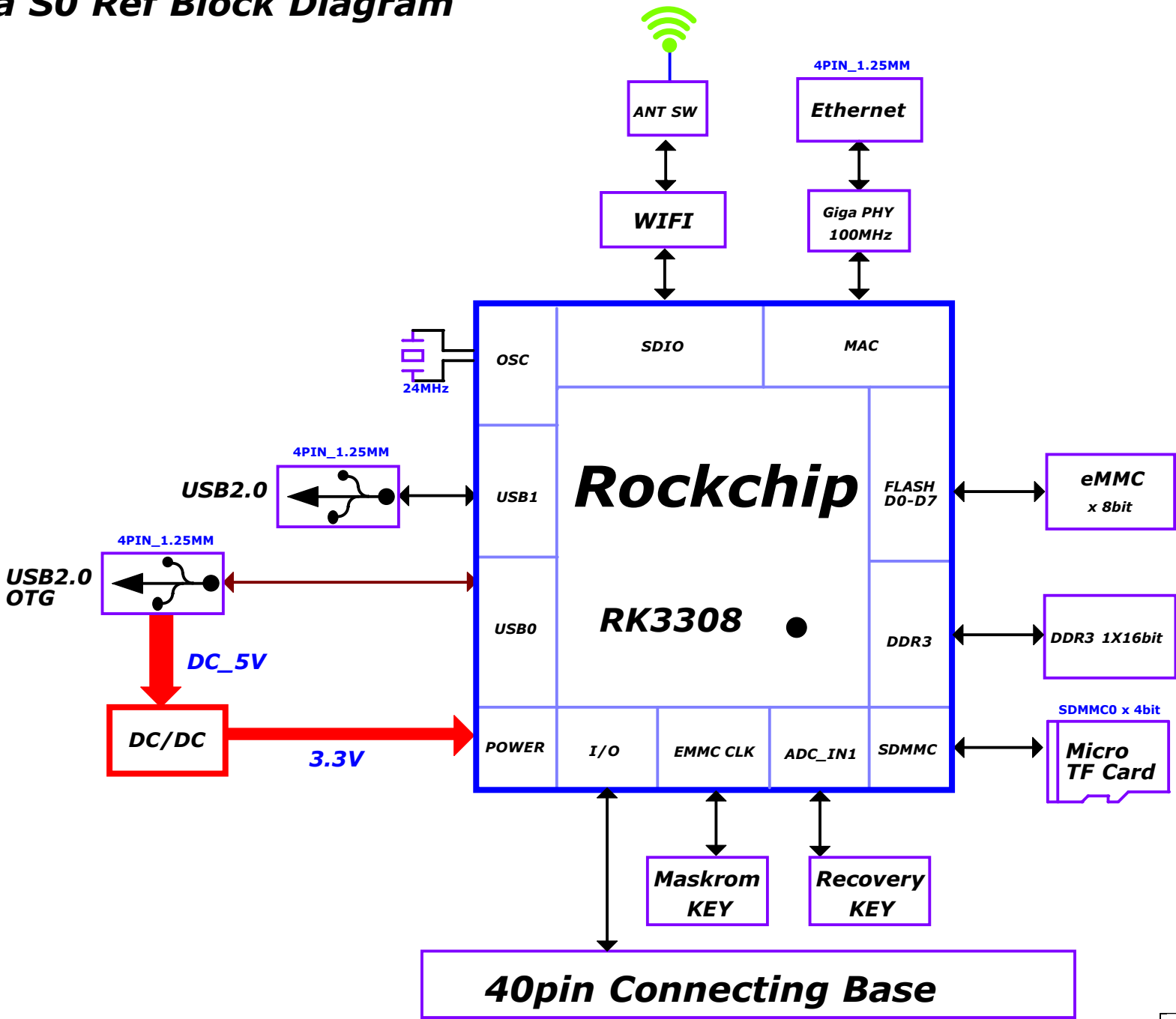
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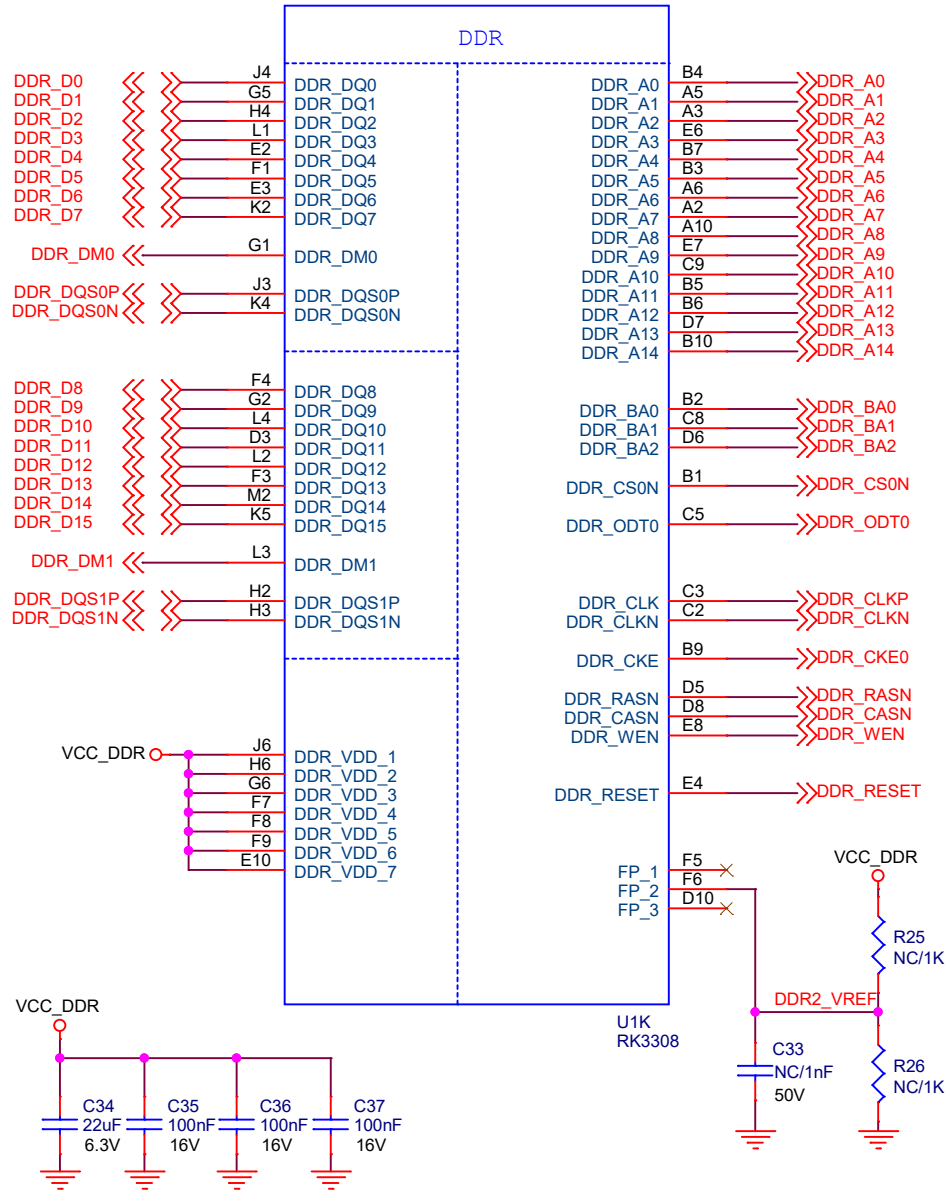
radxa

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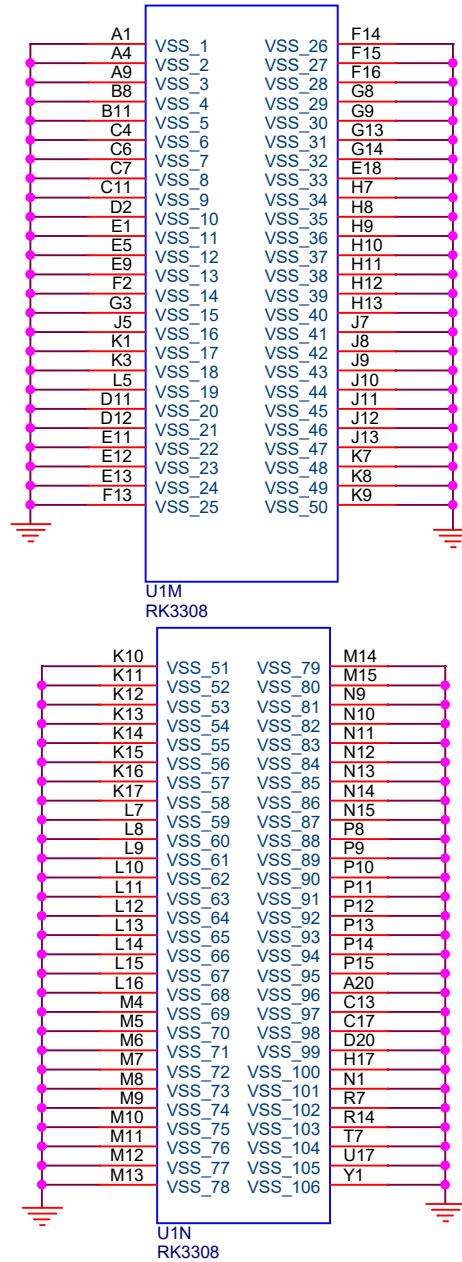
Radxa S0 Ref Block Diagram



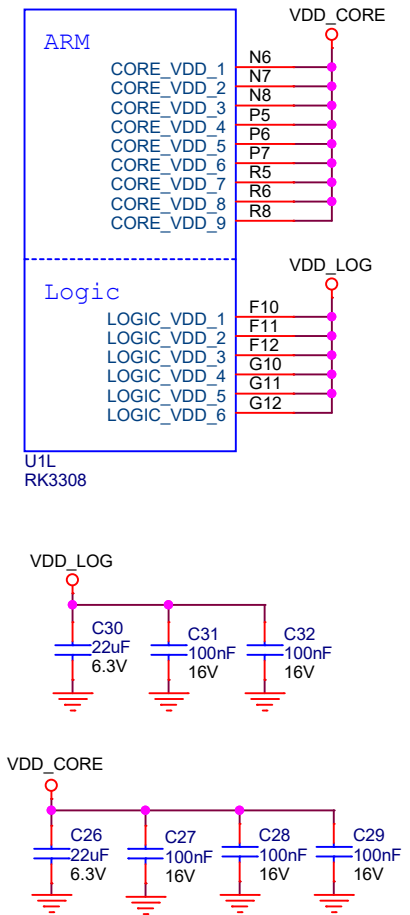
RK3308 Part-K



RK3308 Part-N



RK3308 Part-L



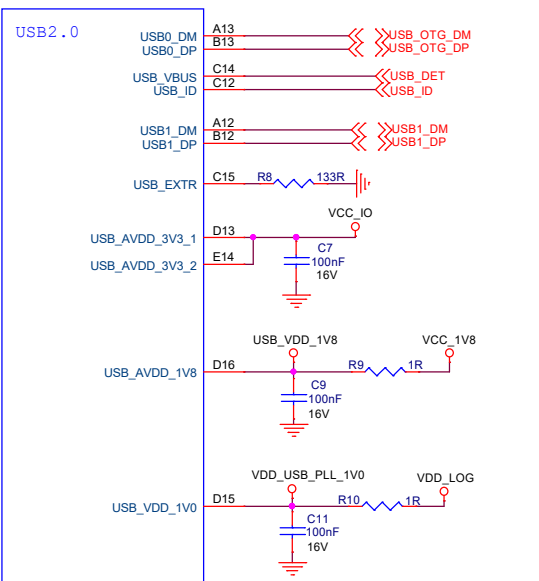
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LCDC/I2S1_8CH/PDM_8CH/MAC
GPIO1_A0/LCDC_DCLK_d
GPIO1_A1/LCDC_HSYNC_d
GPIO1_A2/LCDC_VSYNC/I2S1_8CH_MCLK_M0_d
GPIO1_A3/LCDC_DEN/I2S1_8CH_SCLK_TX_M0_d
GPIO1_A4/LCDC_D0/I2S1_8CH_SCLK_RX_M0/PDM_8CH_CLK_M0_d
GPIO1_A5/LCDC_D1/I2S1_8CH_LRCK_TX_M0_d
GPIO1_A6/LCDC_D2/I2S1_8CH_LRCK_RX_M0_d
GPIO1_A7/LCDC_D3/I2S1_8CH_SDOO_M0_d
GPIO1_B0/LCDC_D4/I2S1_8CH_SDO1_SDI2_M0/PDM_8CH_SDI3_M0_d
GPIO1_B1/LCDC_D5/I2S1_8CH_SDO2_SDI2_M0/PDM_8CH_SDI2_M0_d
GPIO1_B2/LCDC_D6/I2S1_8CH_SDO3_SDI1_M0/PDM_8CH_SDI1_M0_d
GPIO1_B3/LCDC_D7/I2S1_8CH_SDI0_M0/PDM_8CH_SDI0_M0_d
GPIO1_B4/LCDC_D8/I2S1_8CH_MCLK_M1/MAC_CLK_d
GPIO1_B5/LCDC_D9/I2S1_8CH_SCLK_TX_M1/MAC_MDC_d
GPIO1_B6/LCDC_D10/I2S1_8CH_SCLK_RX_M1/PDM_8CH_CLK_M1/MAC_MDIO_d
GPIO1_B7/LCDC_D11/I2S1_8CH_LRCK_TX_M1/MAC_RXER_d
GPIO1_C0/LCDC_D12/I2S1_8CH_SDOO_M1/MAC_RXDV_d
GPIO1_C1/LCDC_D13/I2S1_8CH_SDOO_M1/MAC_TXEN_d
GPIO1_C2/LCDC_D14/I2S1_8CH_SDO1_SDI3_M1/PDM_8CH_SDI3_M1/MAC_TXD0_d
GPIO1_C3/LCDC_D15/I2S1_8CH_SDO2_SDI2_M1/PDM_8CH_SDI2_M1/MAC_TXD1_d
GPIO1_C4/LCDC_D16/I2S1_8CH_SDO3_SDI1_M1/PDM_8CH_SDI1_M1/MAC_RXD0_d
GPIO1_C5/LCDC_D17/I2S1_8CH_SDI0_M1/PDM_8CH_SDI0_M1/MAC_RXD1_d
-----
UART/SPI2/JTAG/T2C0
GPIO1_C6/UART1_CTSN/UART2_RX_M0/SPI2_MISO/JTAG_TCK/OWIRE_M1/LCDC_D18_u
GPIO1_C7/UART1_RTSN/UART2_TX_M0/SPI2_MOSI/JTAG_TMS/LCDC_D19_u
GPIO1_D0/UART1_RX/I2C0_SDA/SPI2_CLK_u
GPIO1_D1/UART1_TX/I2C0_SCL/SPI2_CSN0_u
VCCIO1

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RK3308 Part-I



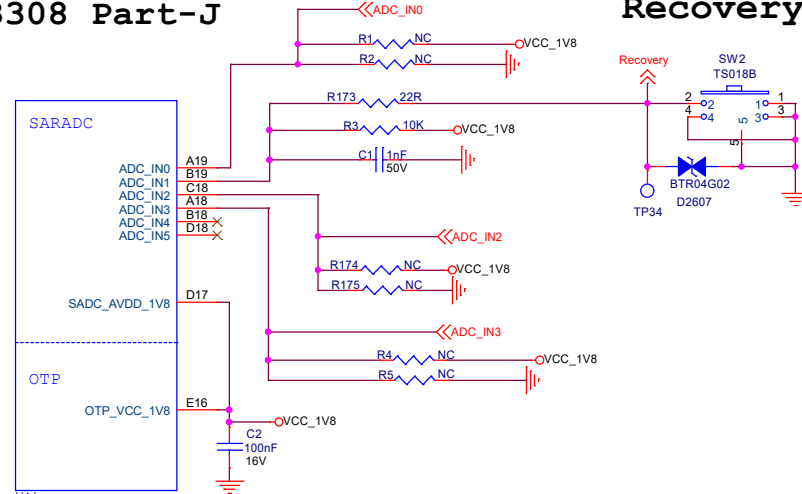
SARADC

ADC_IN0
ADC_IN1
ADC_IN2
ADC_IN3
ADC_IN4
ADC_IN5

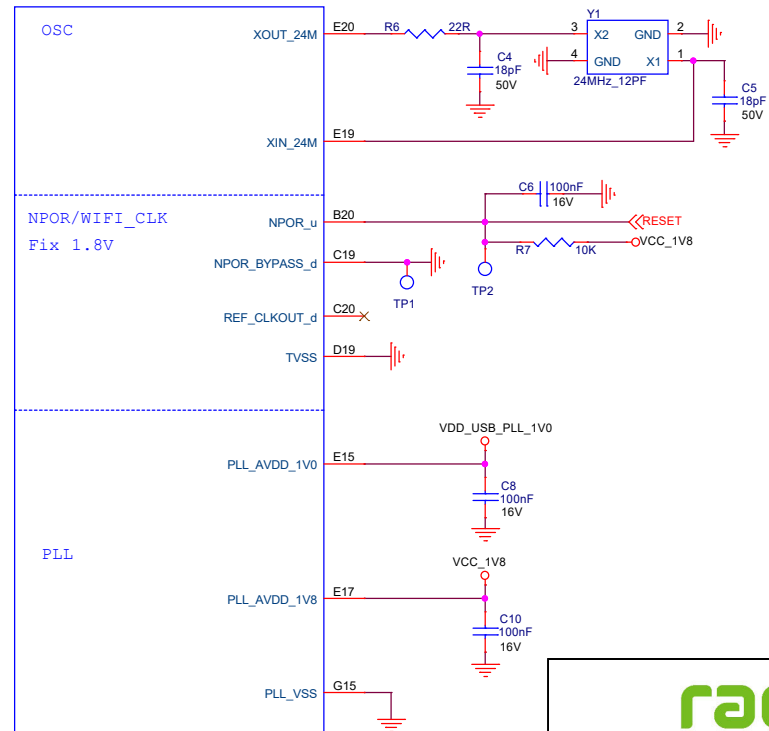
SADC_AVDD_1V8

OTP

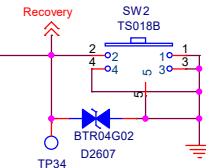
OTP_VCC_1V8



OSC	XOUT_24M
	XIN_24M
NPOR/WIFI_CLK Fix 1.8V	NPOR_u
	NPOR_BYPASS_d
	REF_CLKOUT_d
	TVSS
	PLL_AVDD_1V0
PLL	PLL_AVDD_1V8
	PLL_VSS



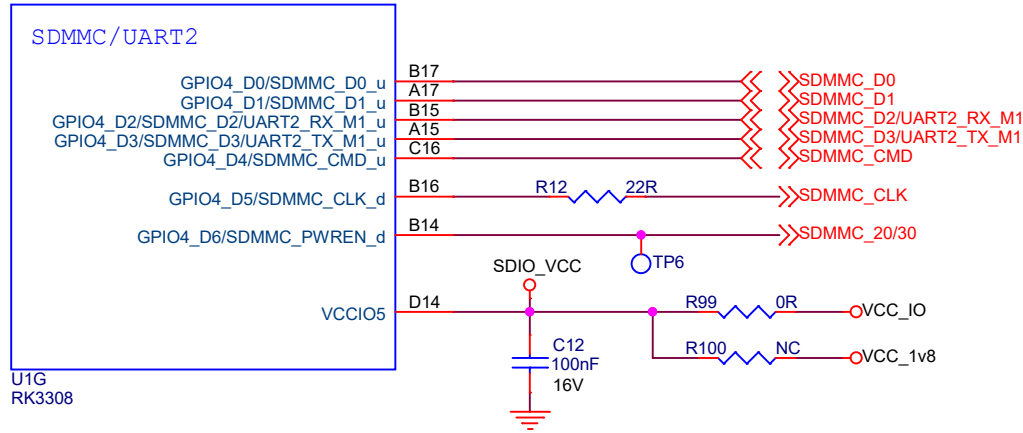
Recovery



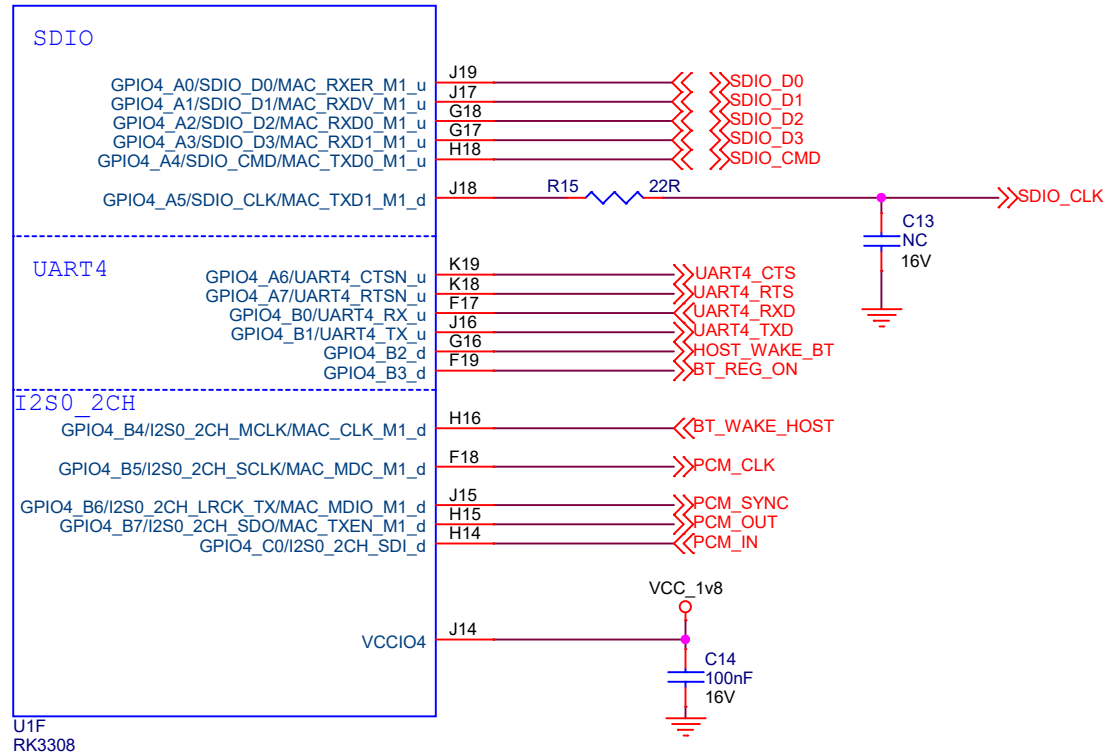
radxa

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RK3308 Part-G



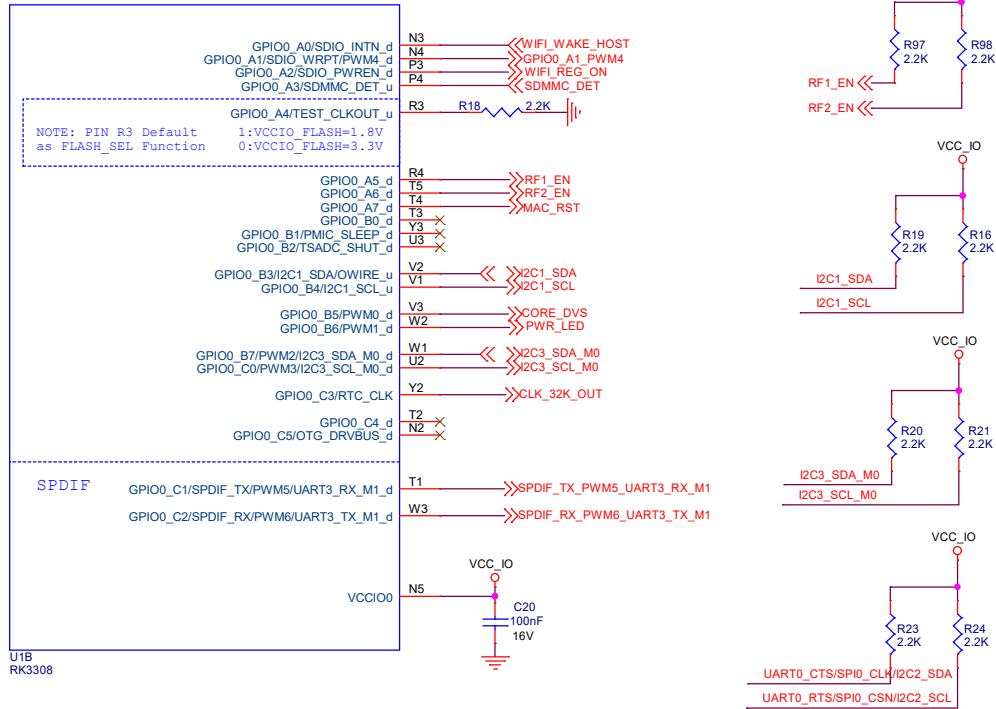
RK3308 Part-F



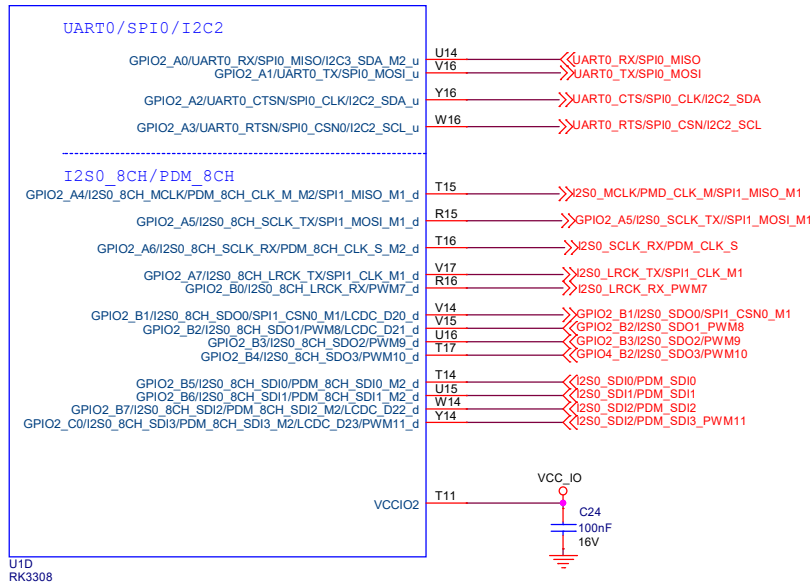
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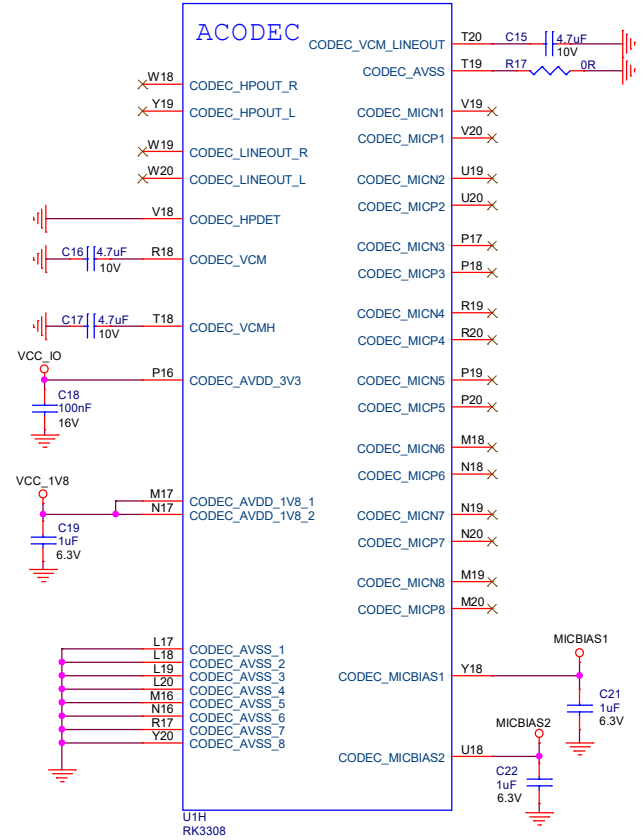
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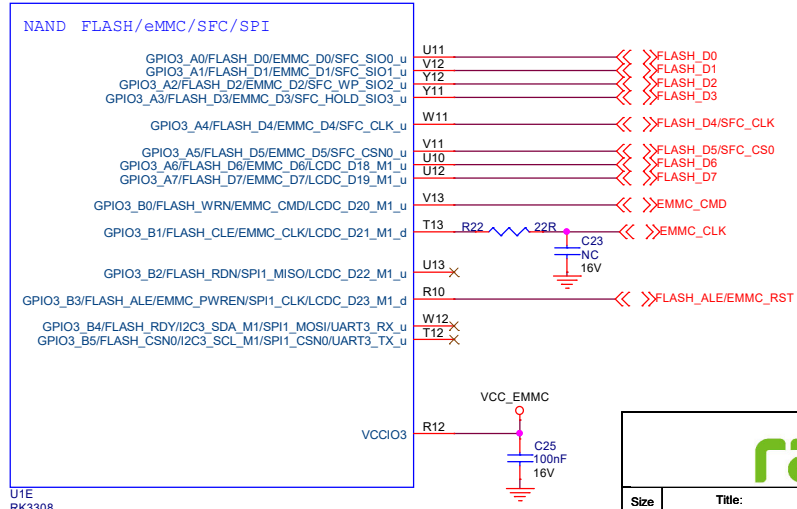
RK3308 Part-D



RK3308 Part-H



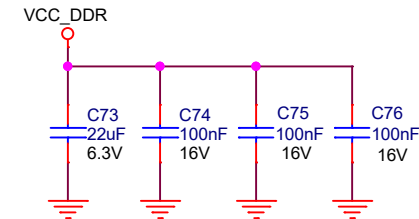
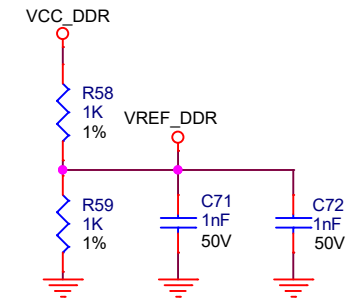
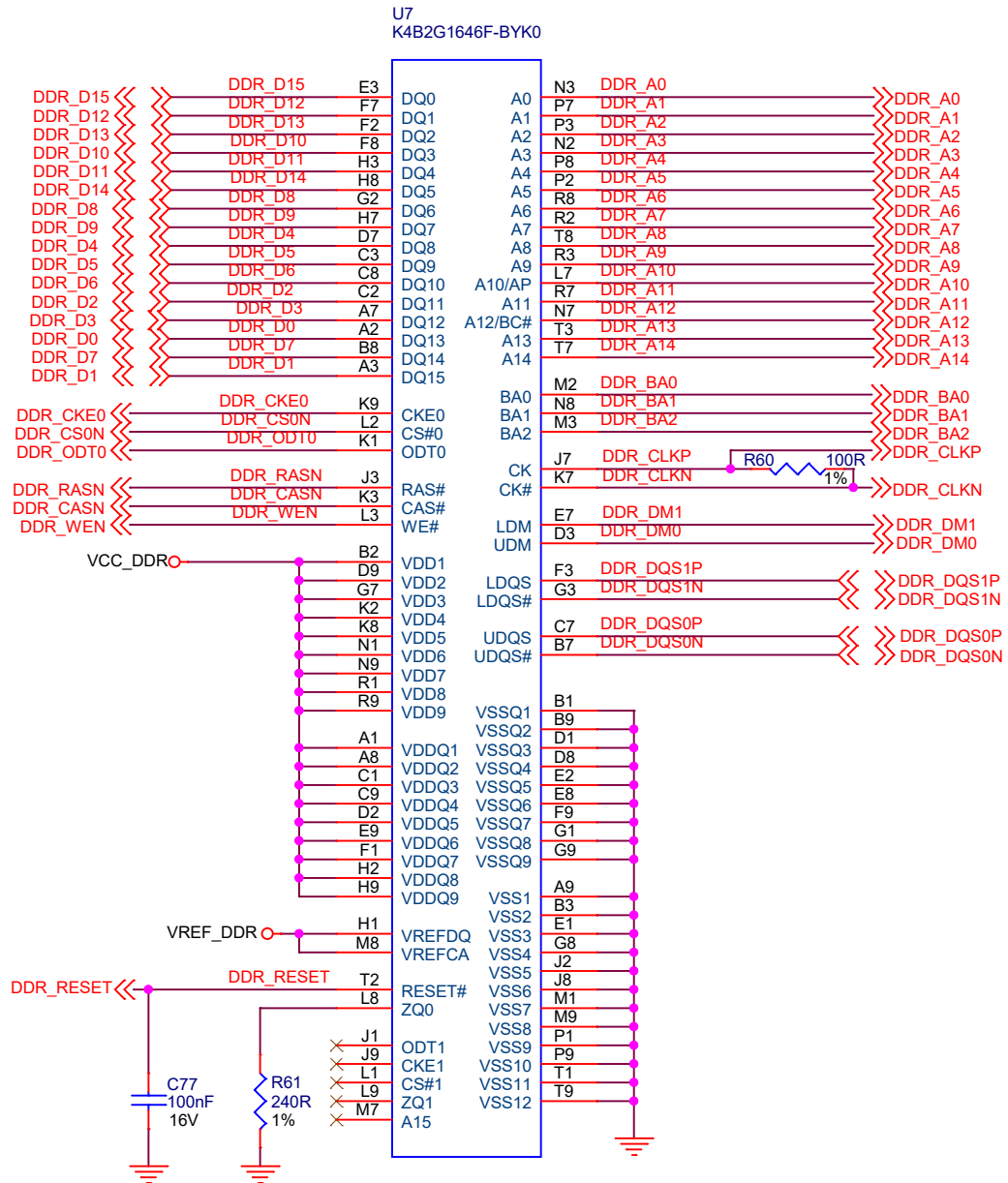
RK3308 Part-E



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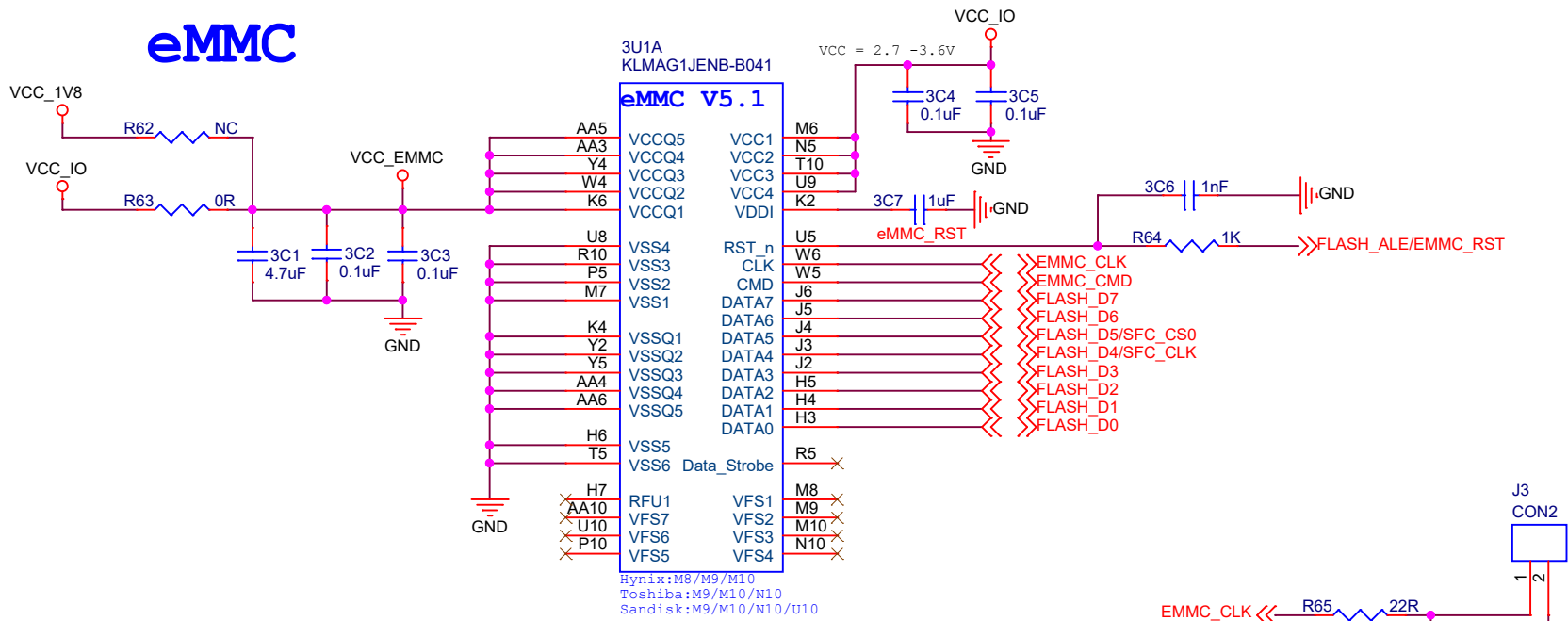
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DDR3 1x16bit



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eMMC



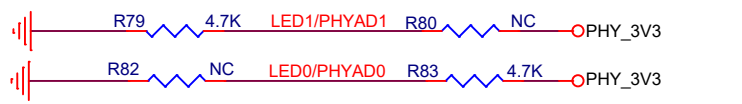
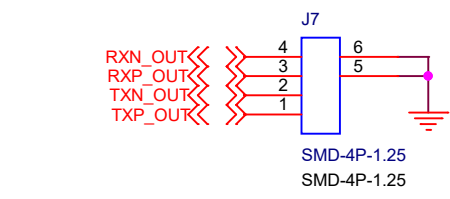
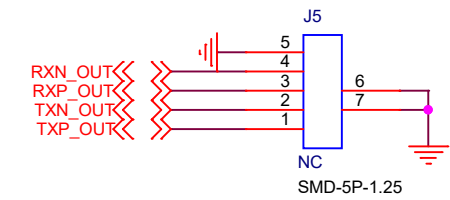
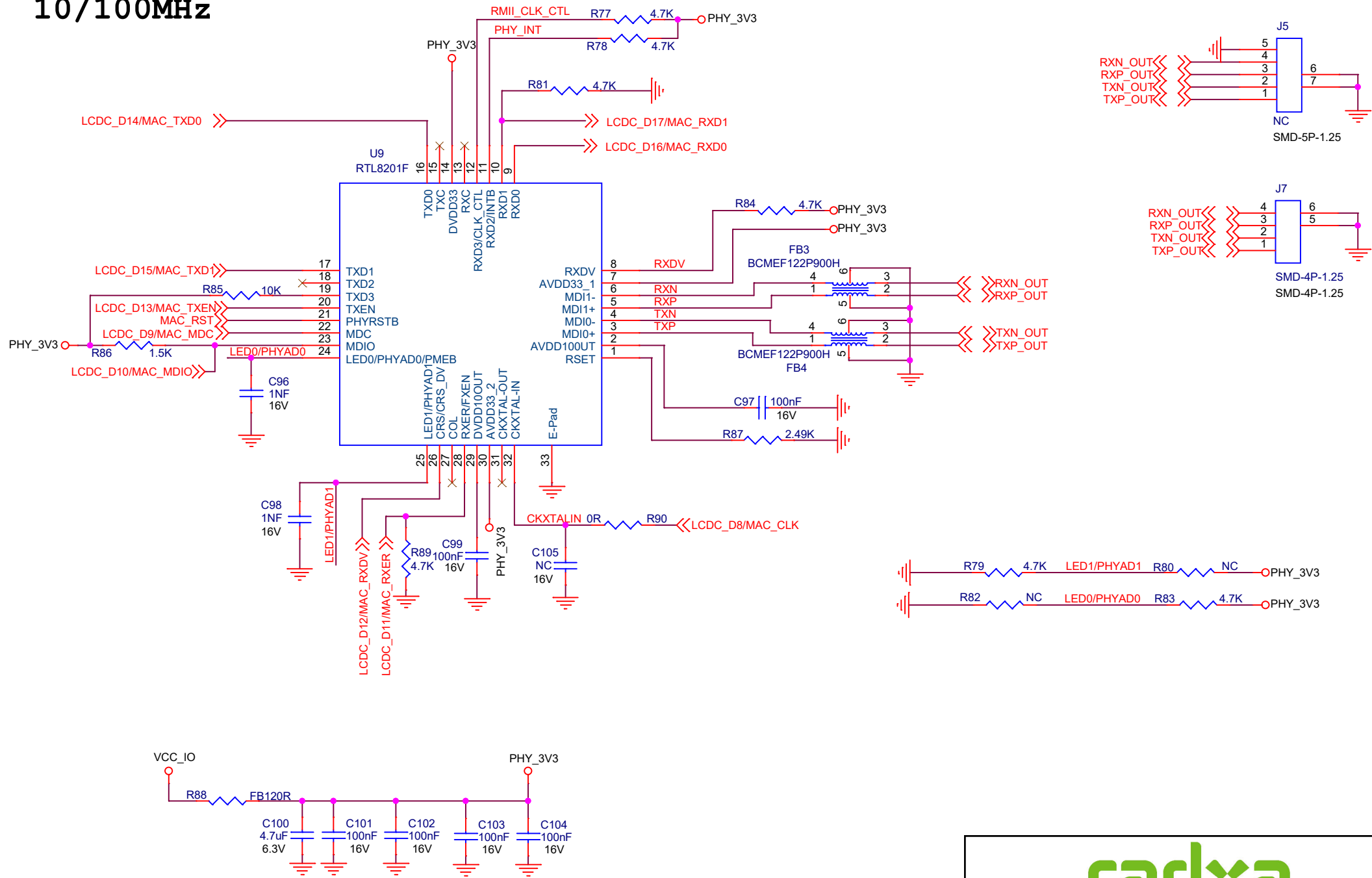
eMMC V5.1

eMMC NC PIN FOR ALLEGRO

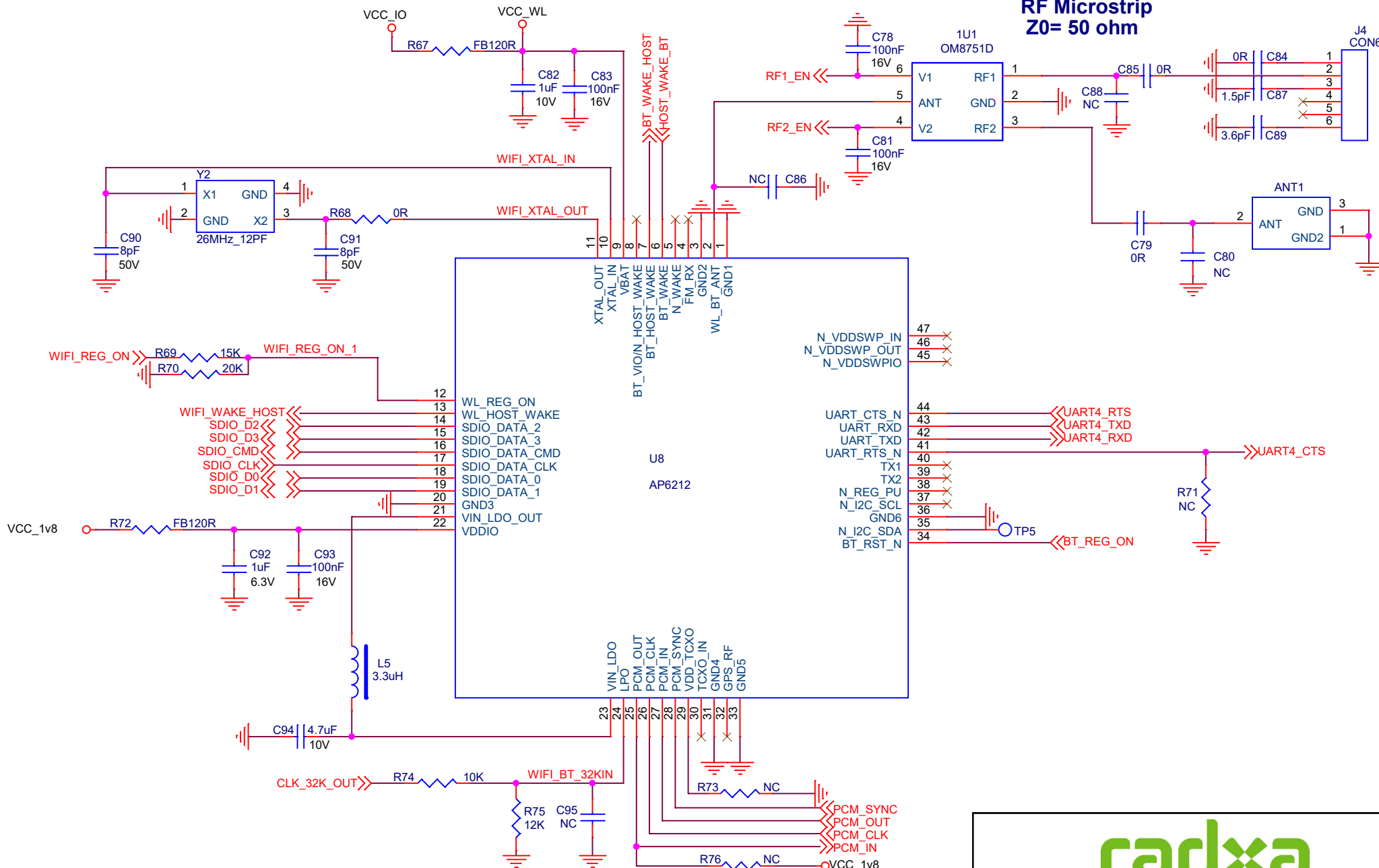
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10/100MHz

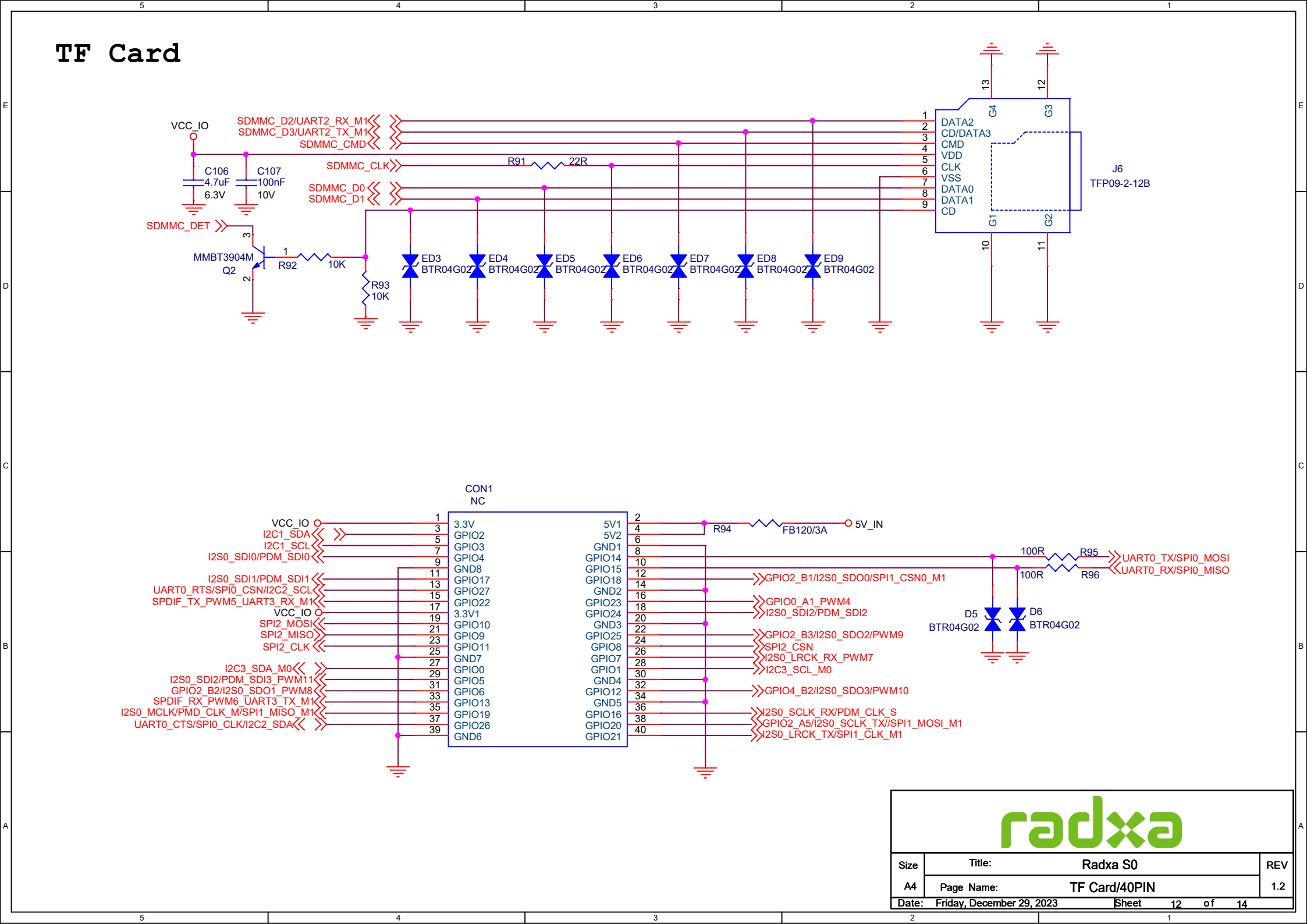
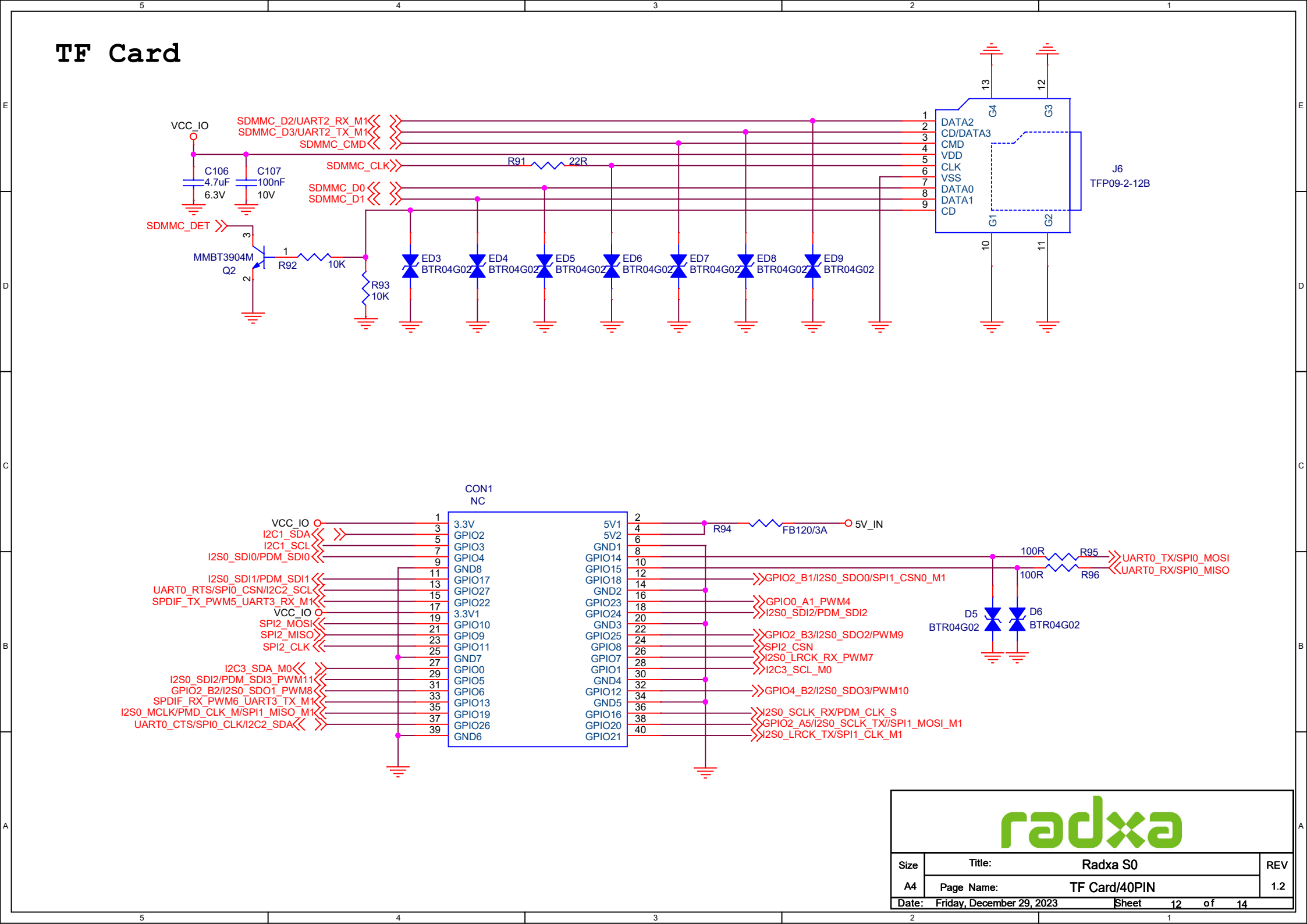


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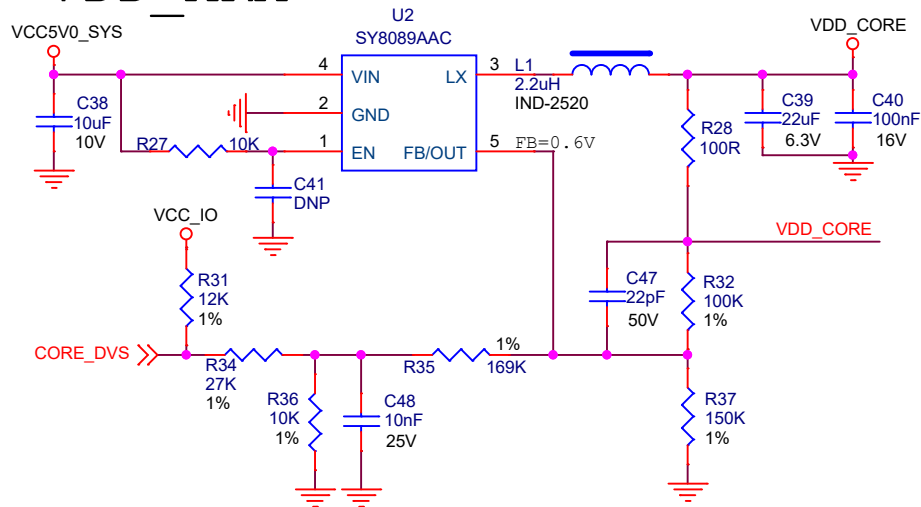


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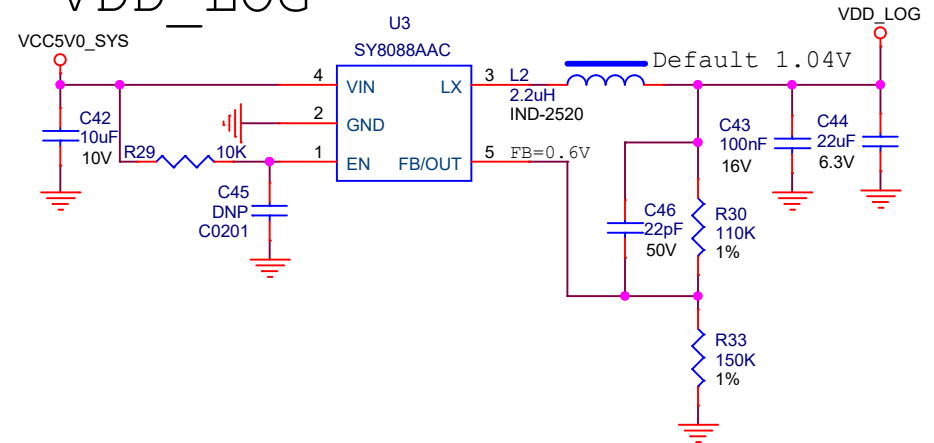
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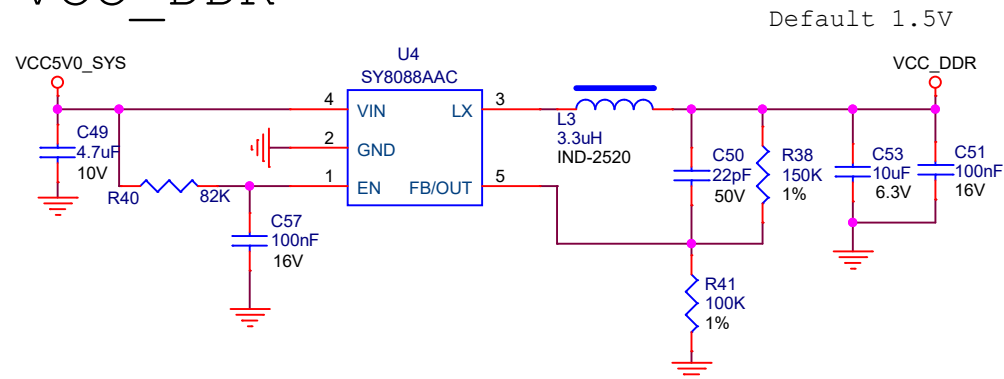
VDD_ARM



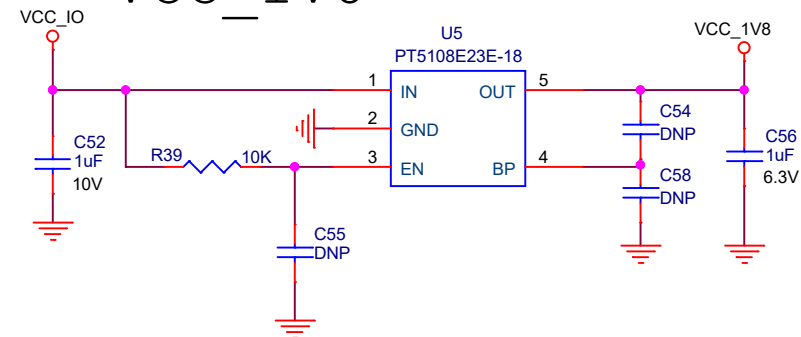
VDD_LOG



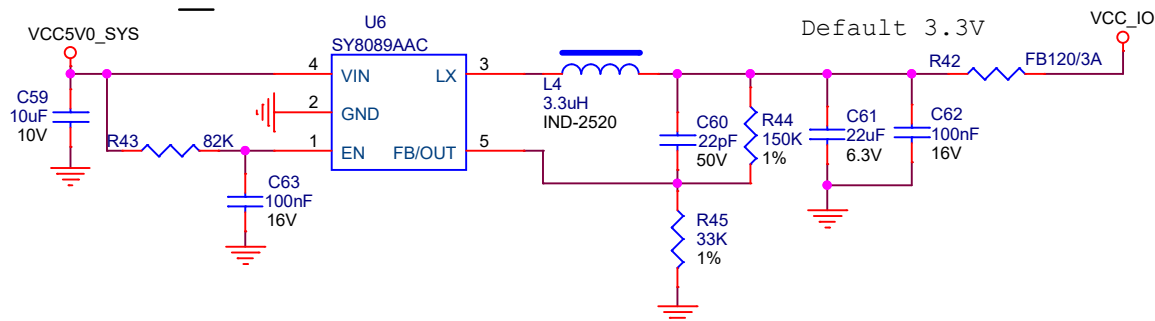
VCC_DDR



VCC_1V8



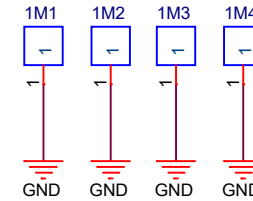
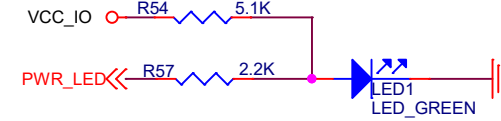
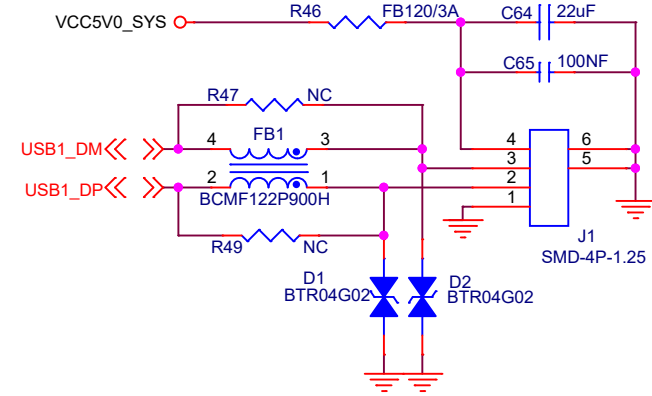
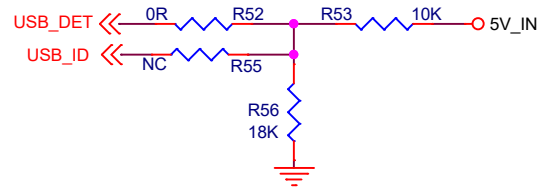
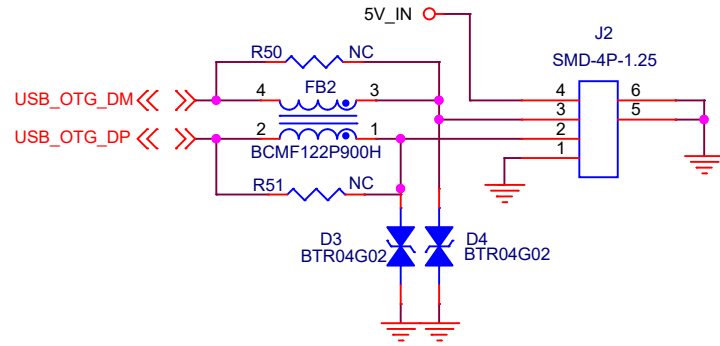
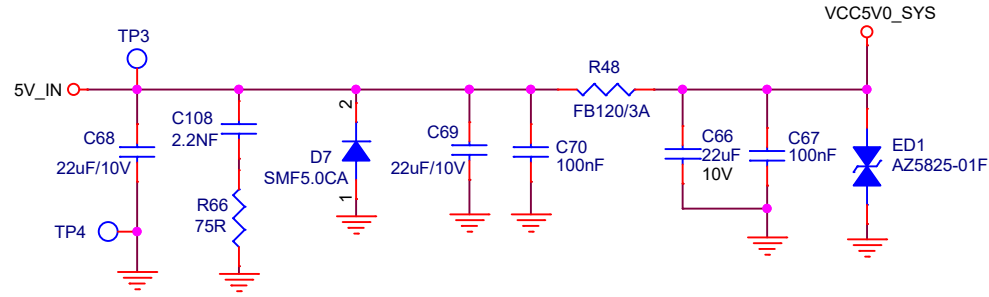
VCC_IO



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USB OTG



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