



Radxa Cubie A5E

Schematic

V1.2

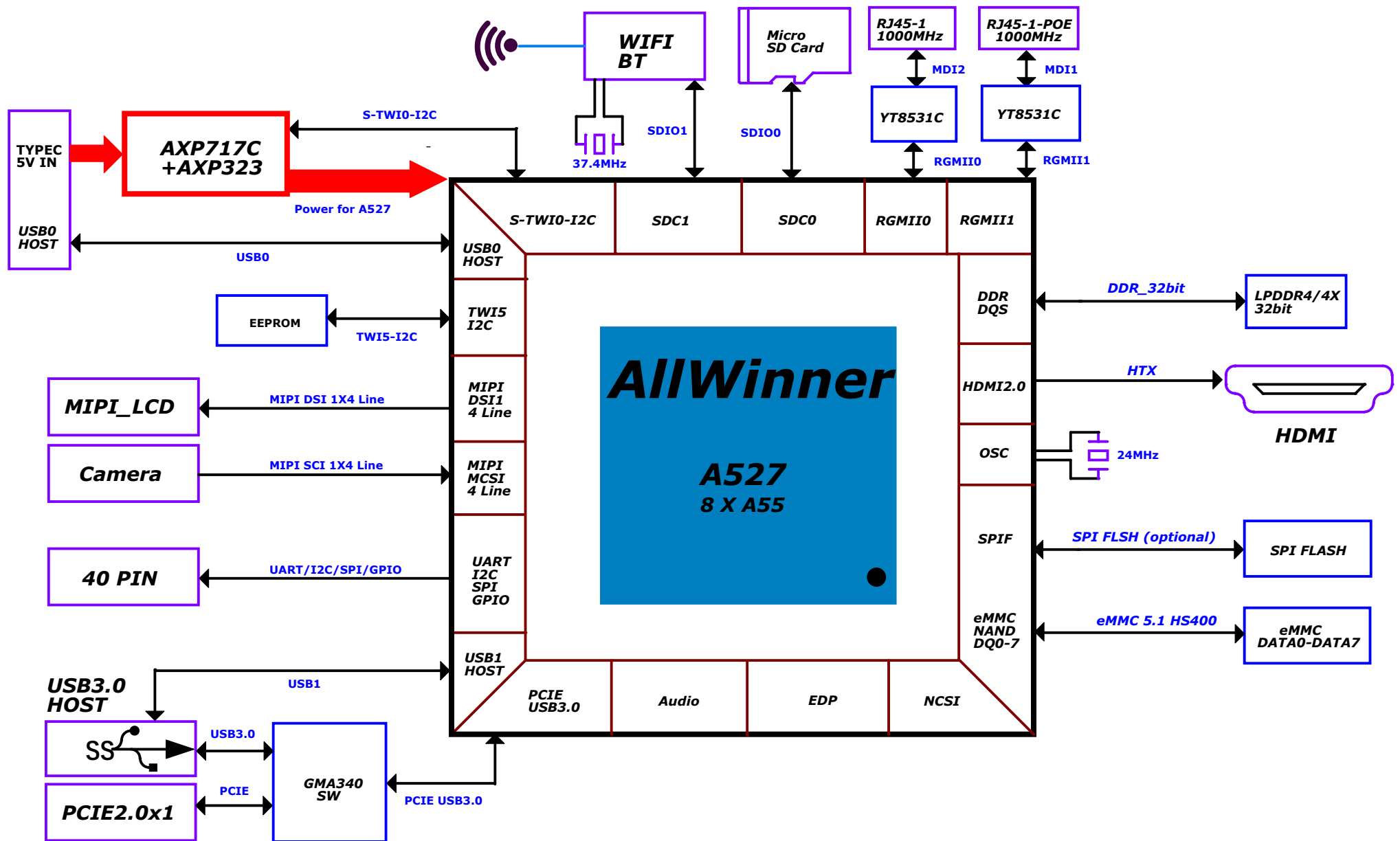
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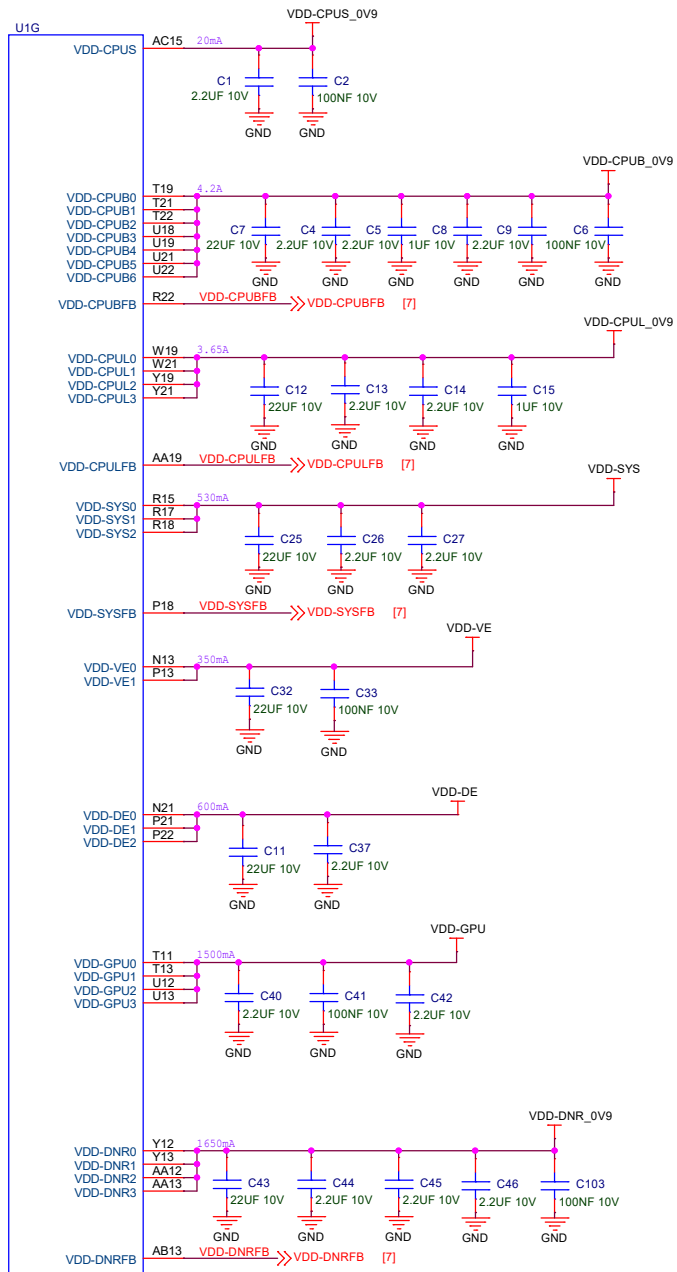


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A4	Page Name: Index and Notes	1.2
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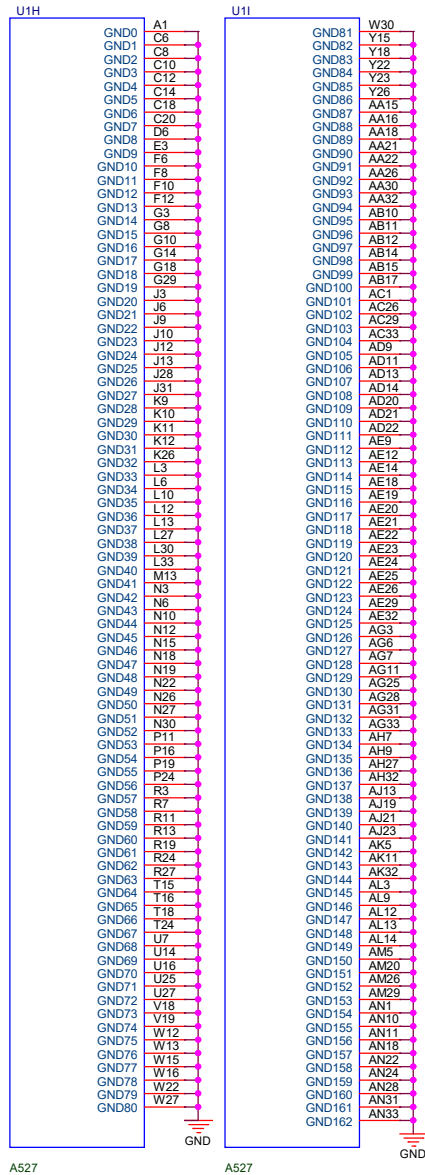
Block Diagram



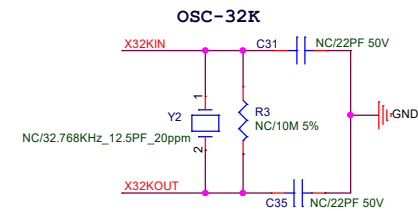
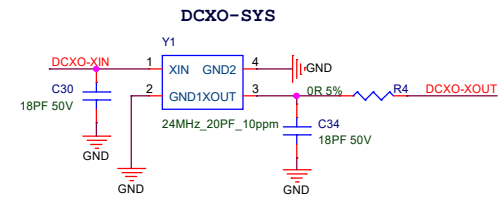
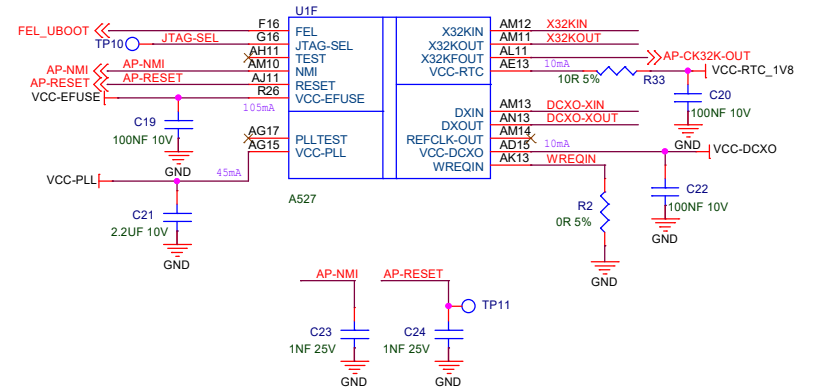
CORE POWER



A527

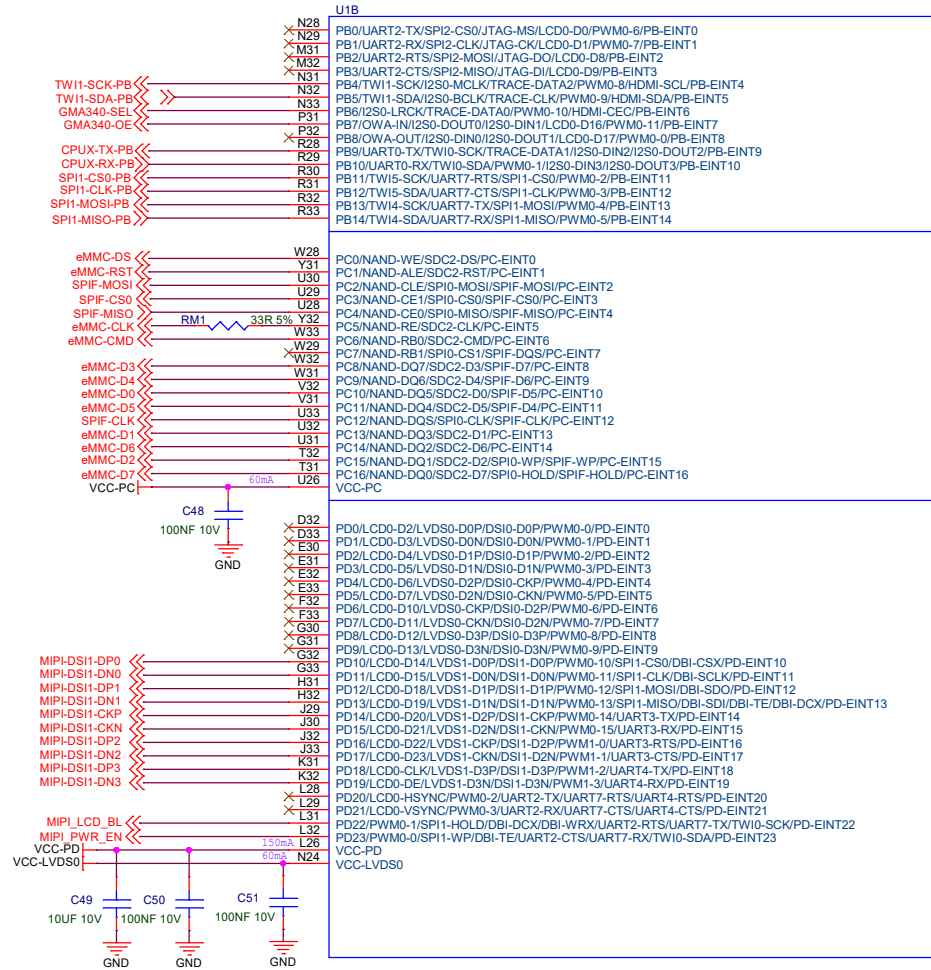


SYS



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GPIO1



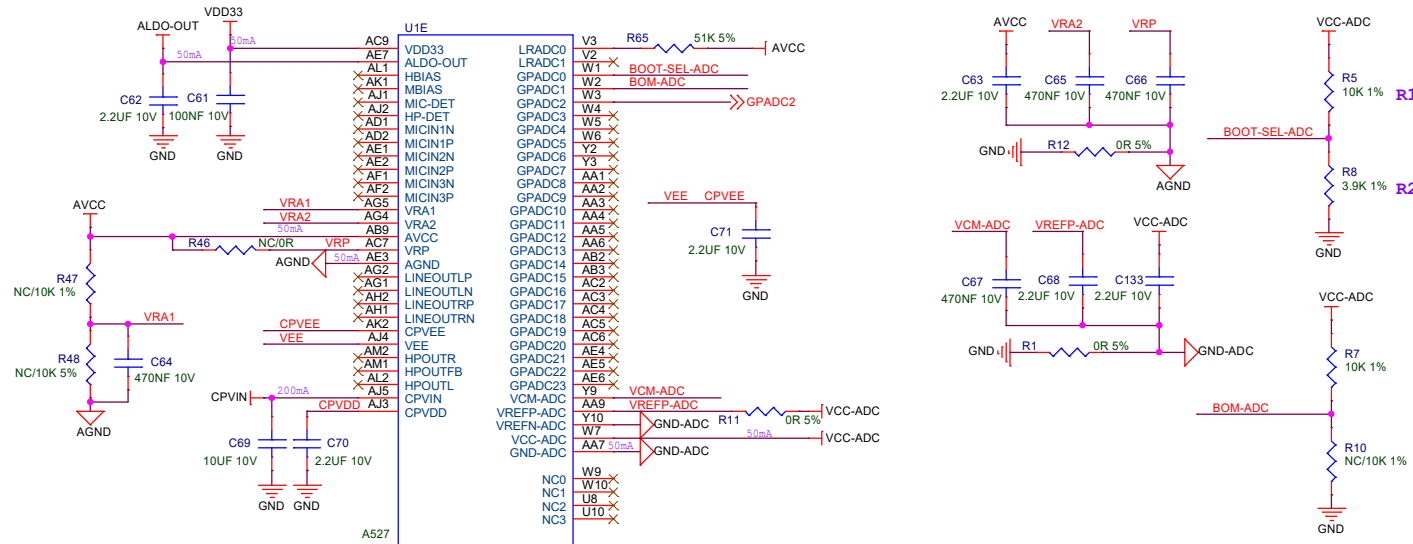
GPIO2



radxa

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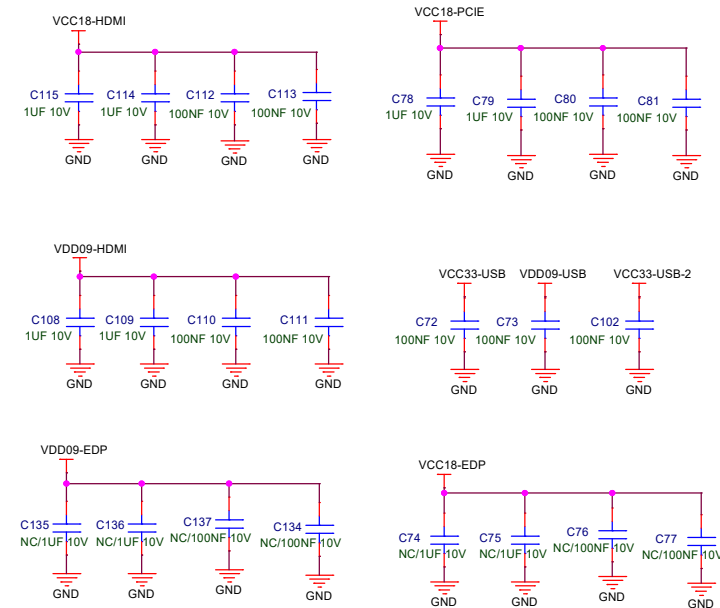
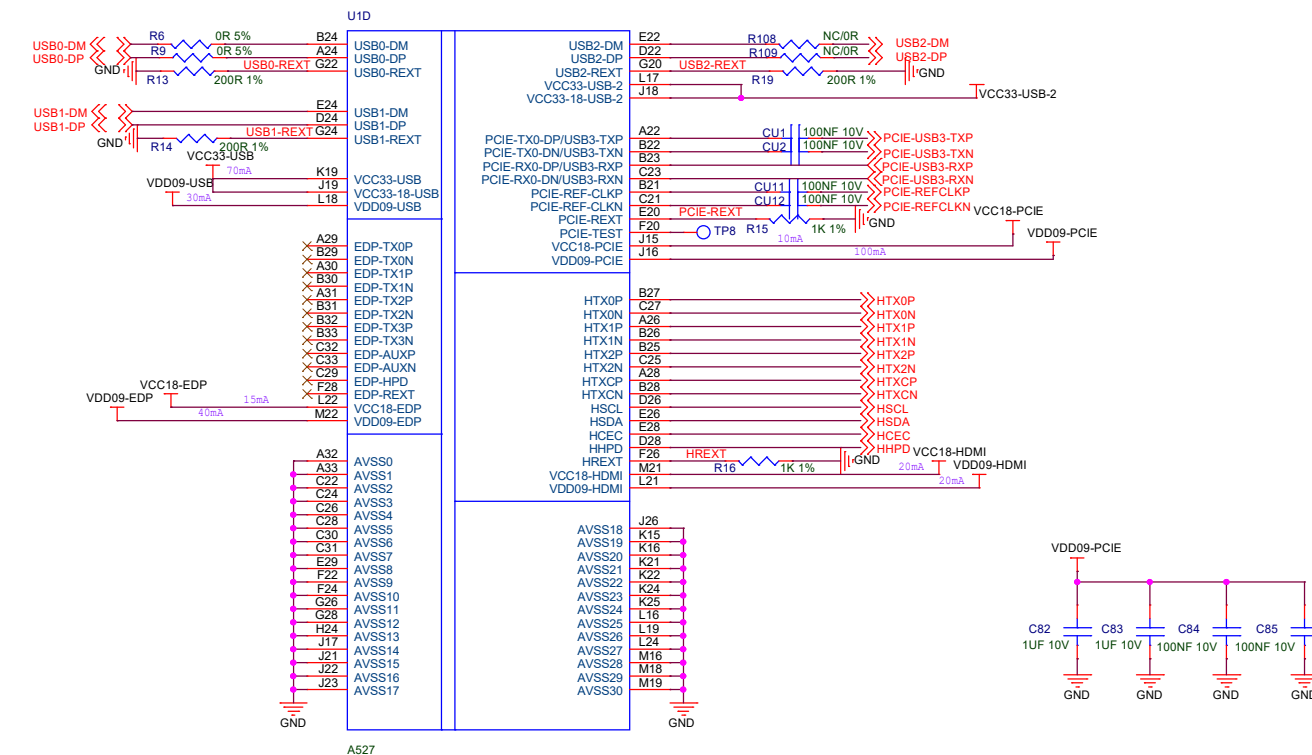
Audio&ADC



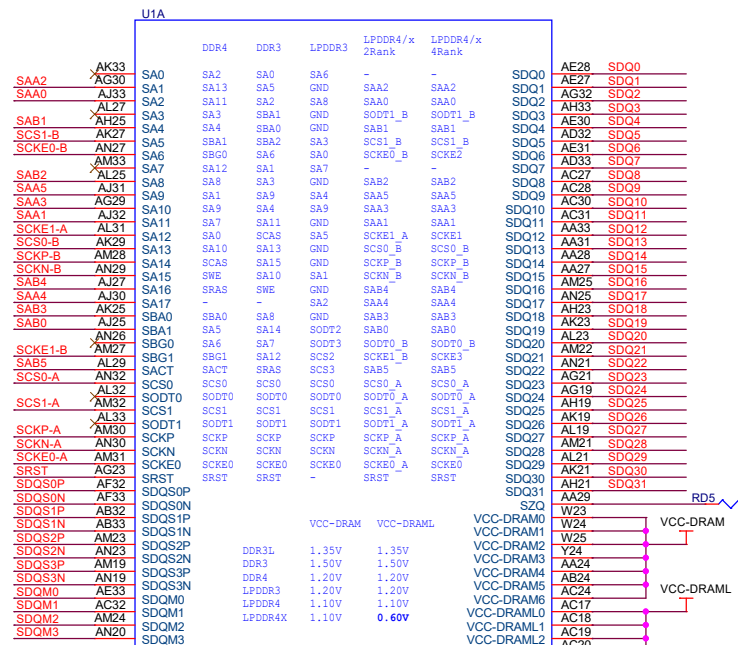
NO.	R1	R2	Boot Select type
1	NC	10K	SMHC0->MLC NAND->SLC NAND->try(except SPI in PJ)
2	10K	1K	SMHC0->SLC NAND->MLC NAND->try(except SPI in PJ)
3	10K	2K2	SMHC0->EMMC_USER->EMMC_BOOT->try(except SPI in PJ)
4	10K	3K9	SMHC0->EMMC_BOOT->EMMC_USER->try(except SPI in PJ)
5	6K8	4K7	SMHC0->SPI NOR->try(except SPI in PJ)
6	6K8	6K8	SMHC0->SPI NAND->try(except SPI in PJ)
7	4K3	6K8	SMHC0->SPI NOR in PJ->try
8	2K2	10K	SMHC0->SPI NAND in PJ->try
9	10K	NC	Reserved

```
try default order:(selected fail media will not try)
EMMC_USER->EMMC_BOOT->SLC NAND->MLC NAND->SPI NOR->SPI NAND
```

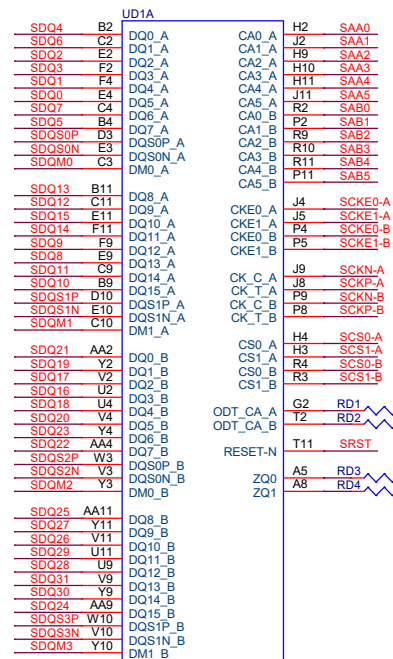
USB&EDP&PCIE



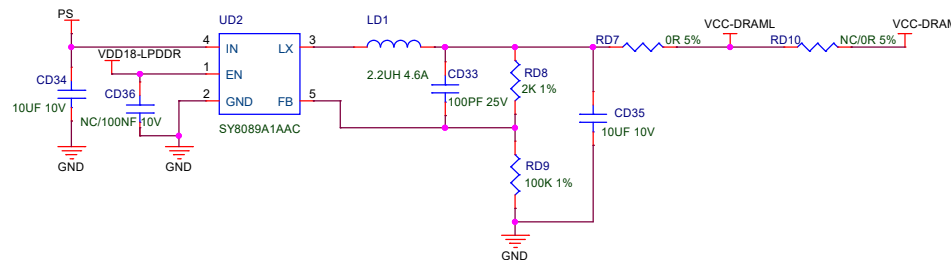
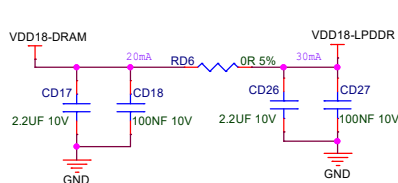
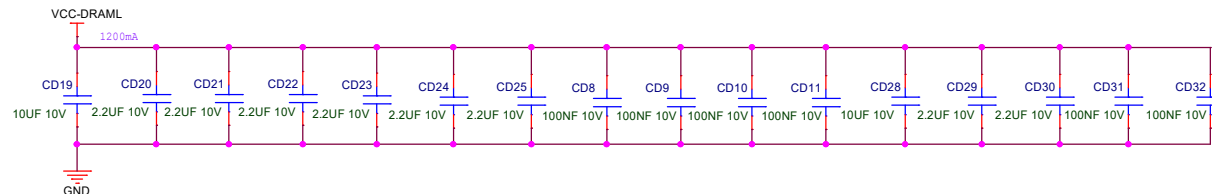
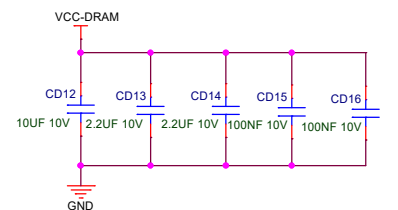
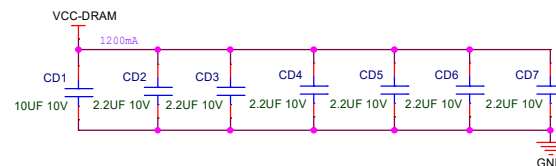
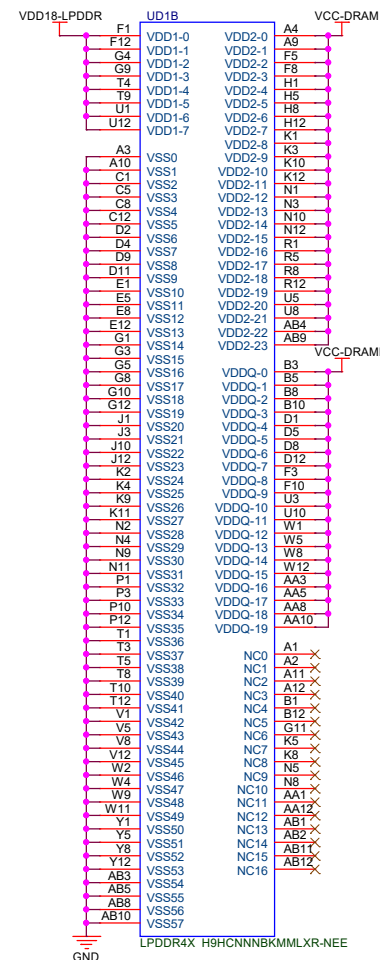
LPDDR4-4X



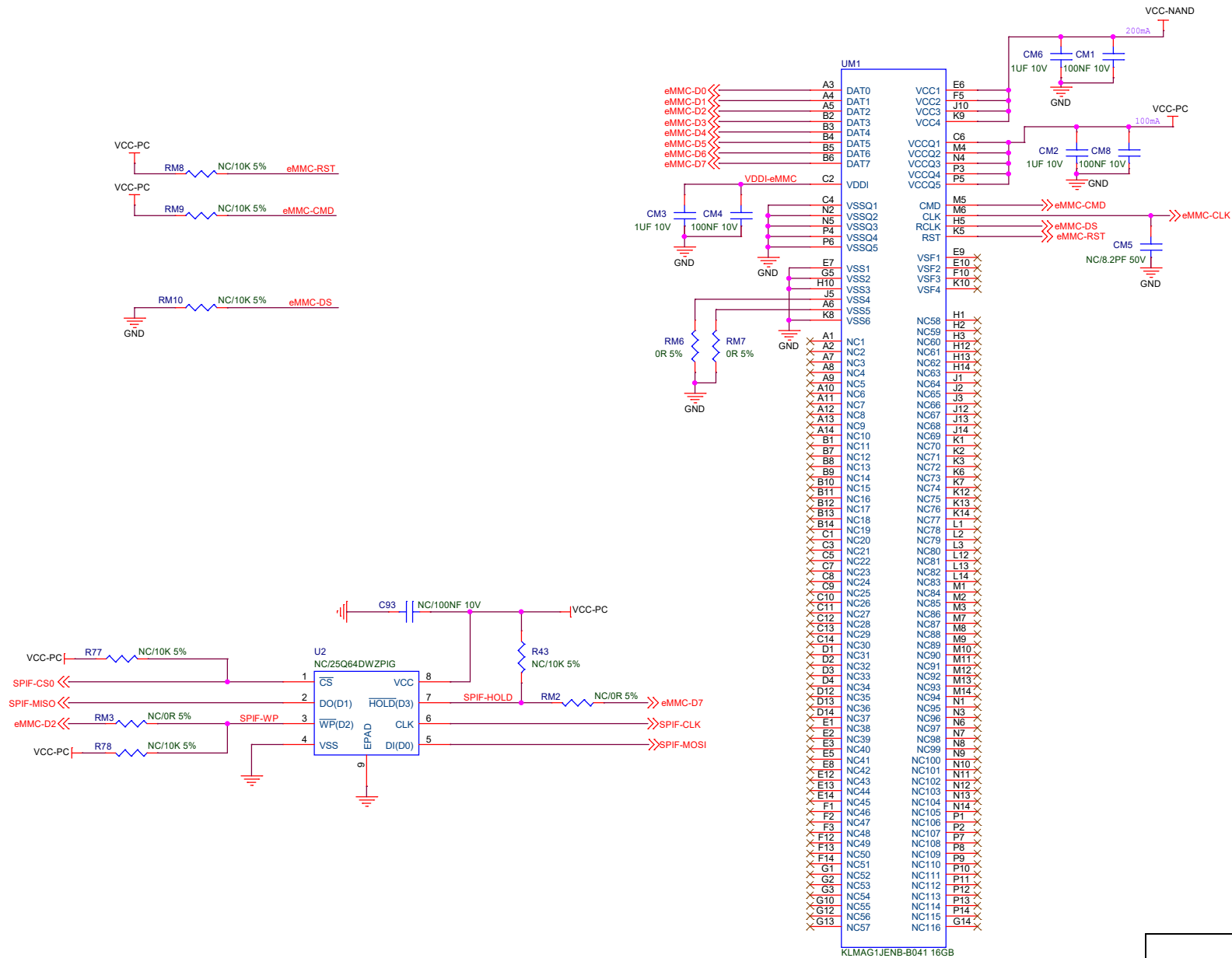
A527



LPDDR4X H9HCNNNBKMMLXR-NEE

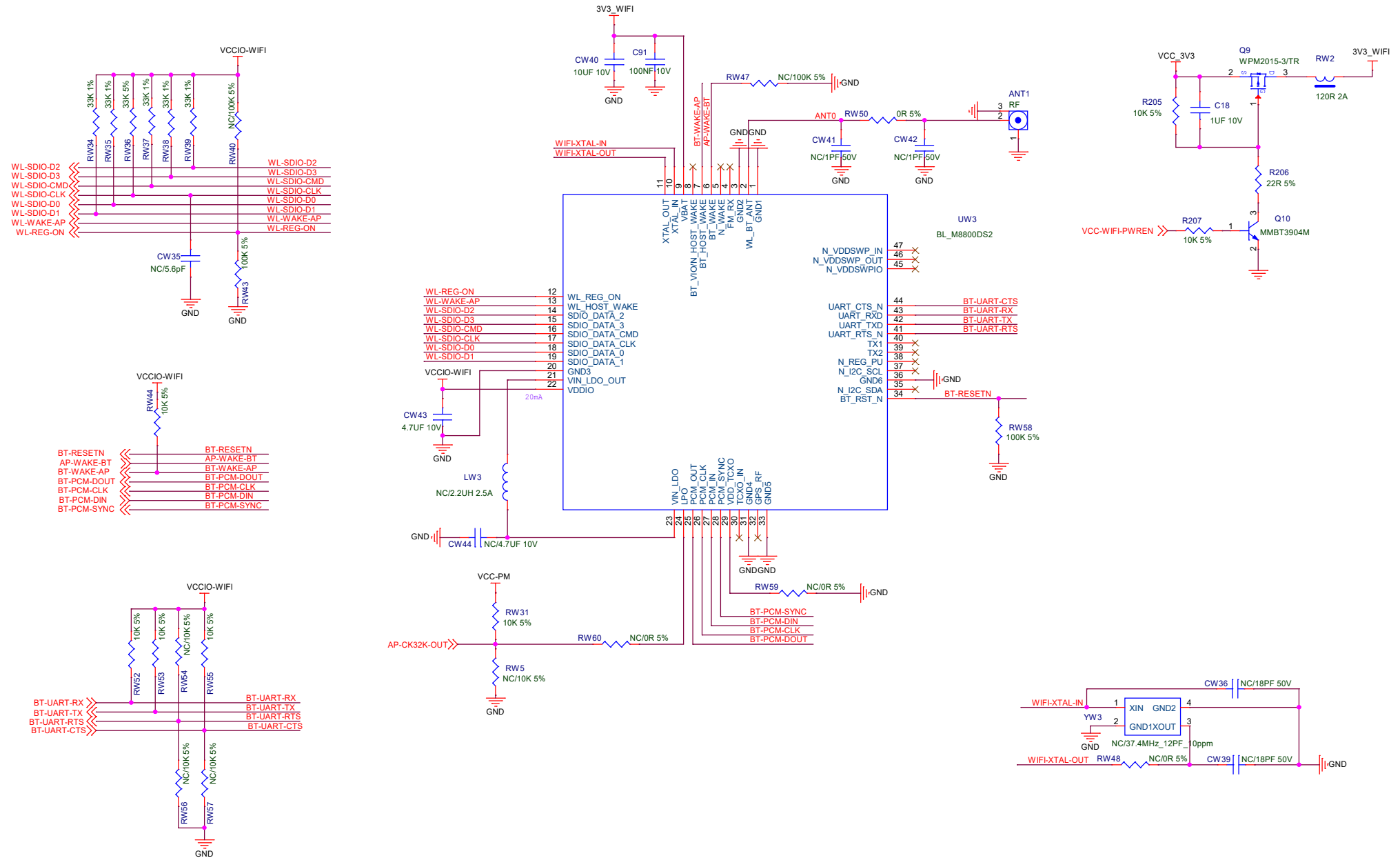


EMMC



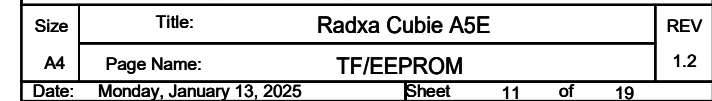
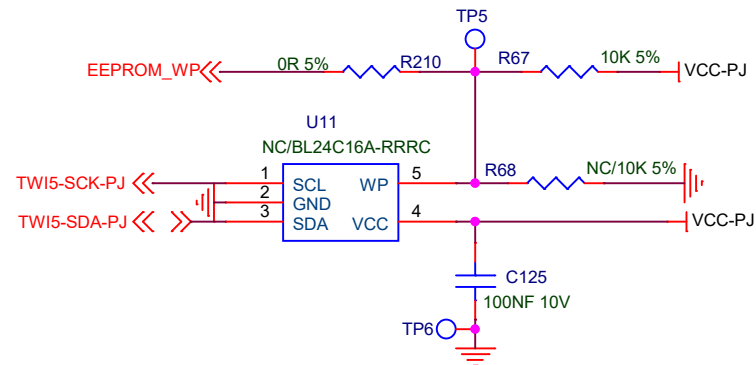
radxa

WIFI/BT

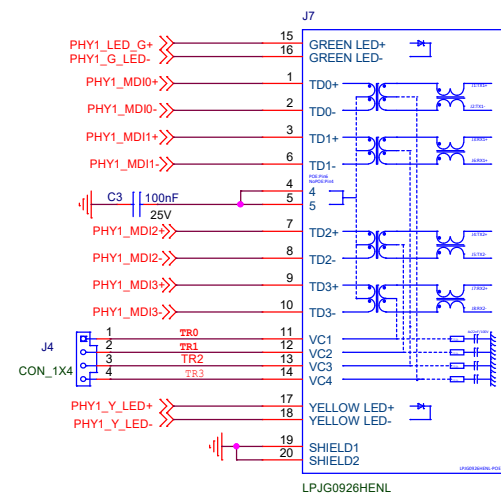
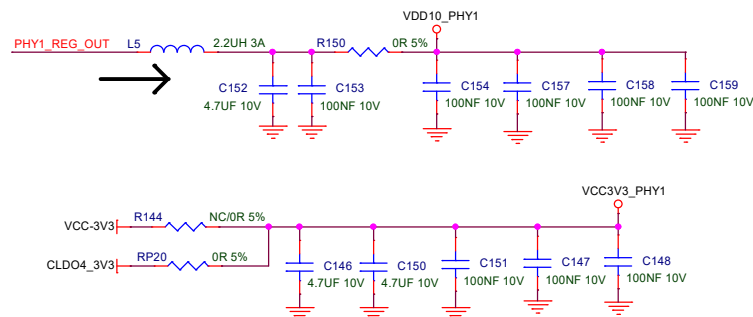
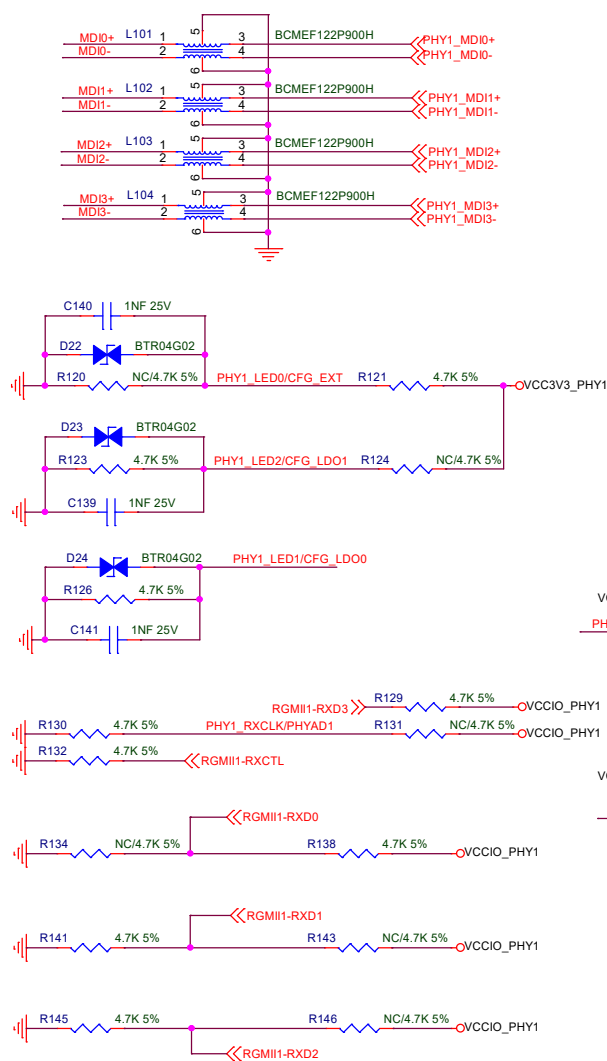
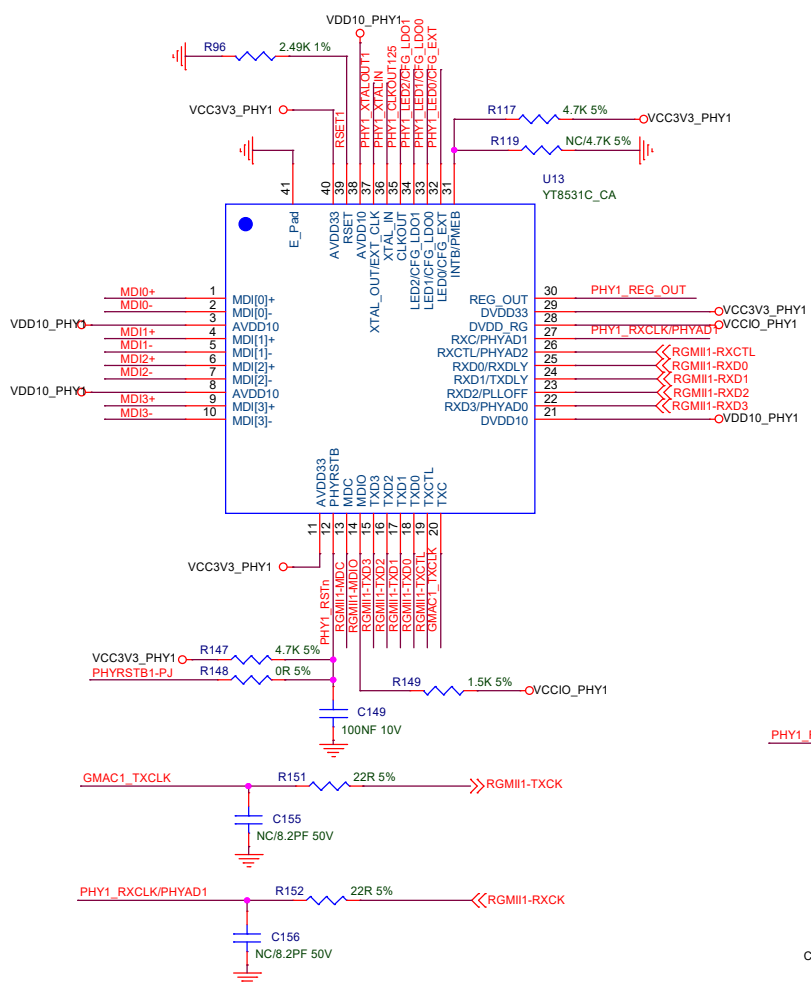
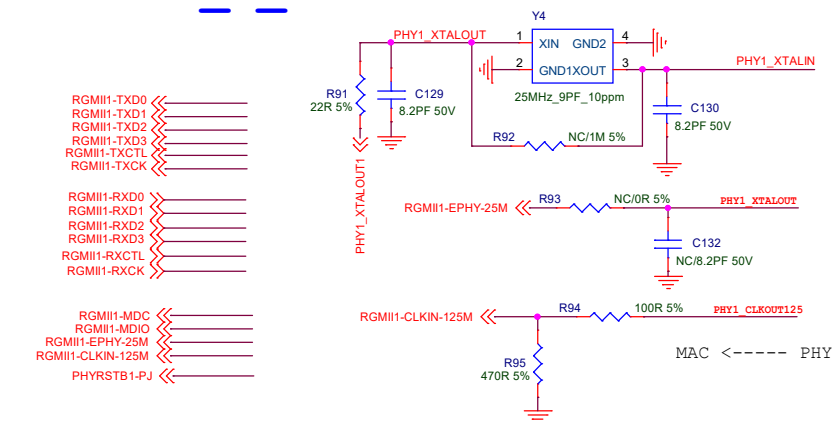


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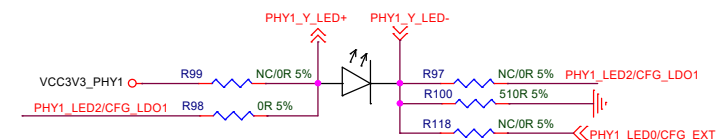
EEPROM



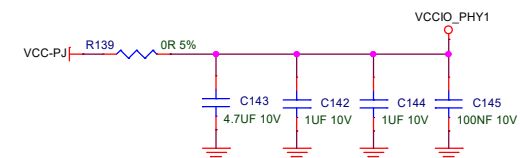
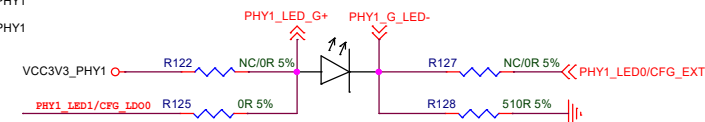
ETHERNET 1 POE



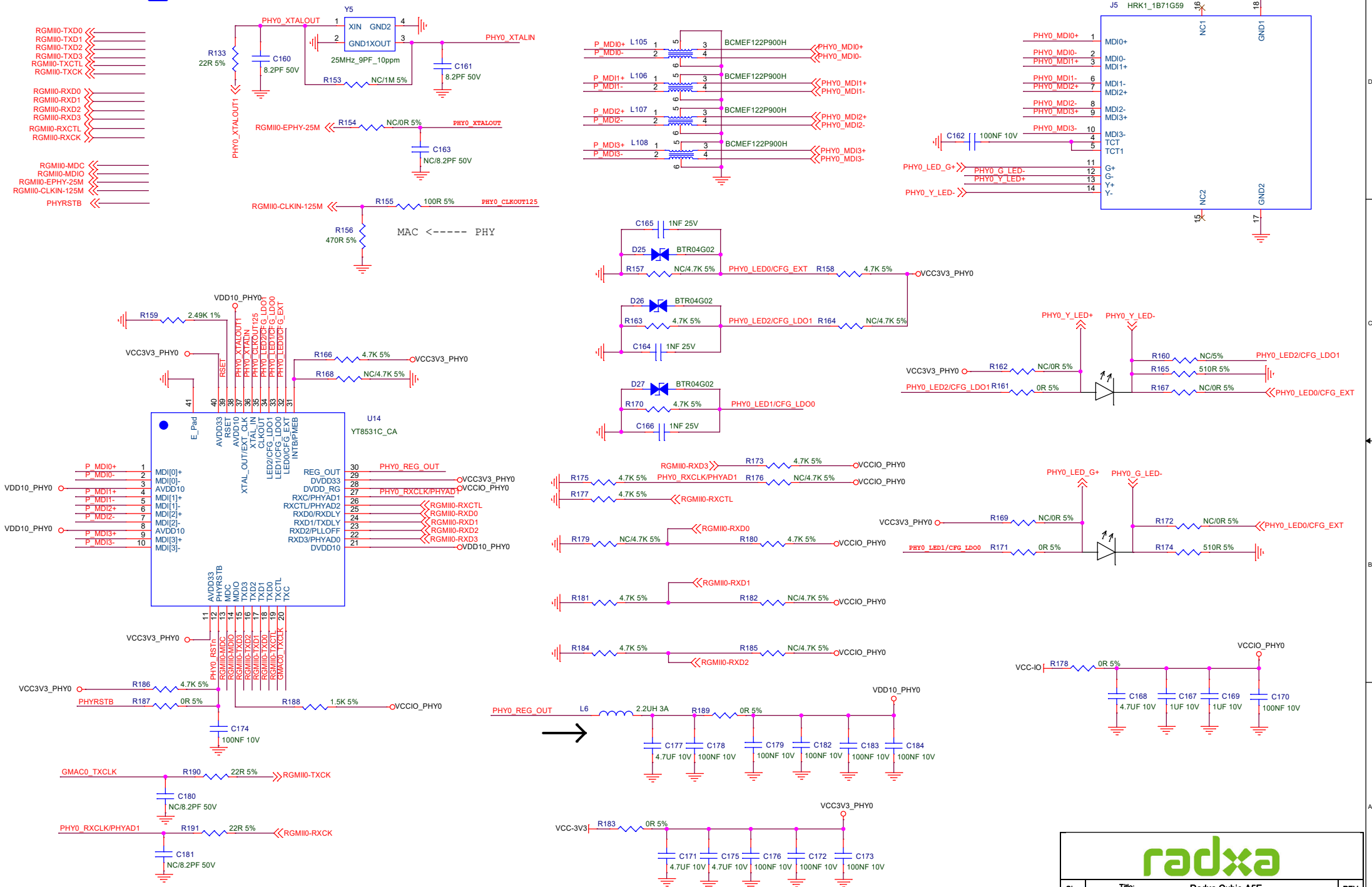
Yellow LED



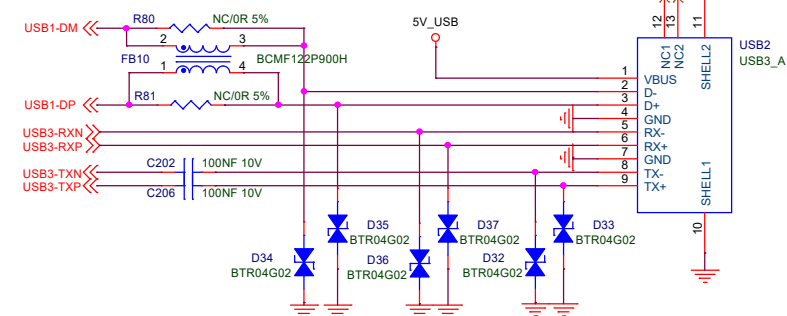
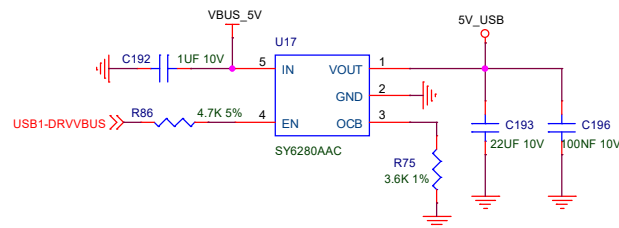
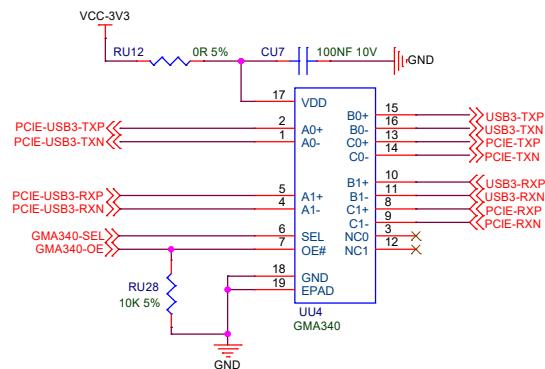
Green LED



ETHERNET_0



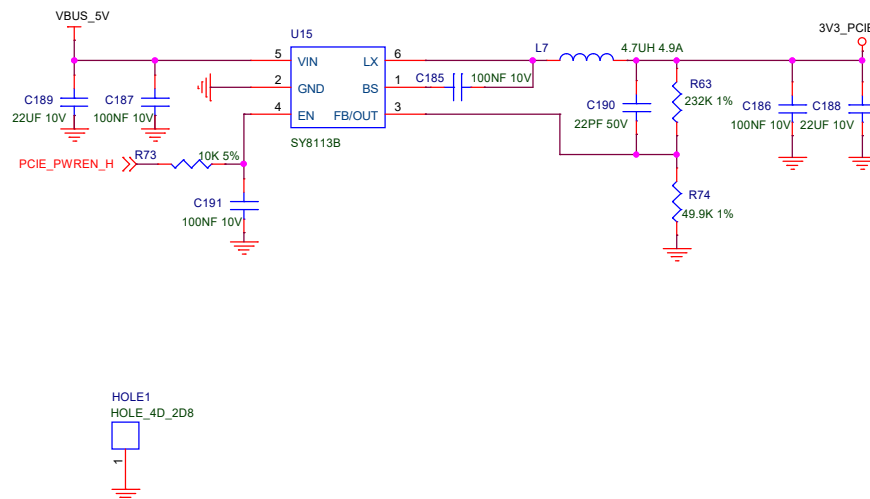
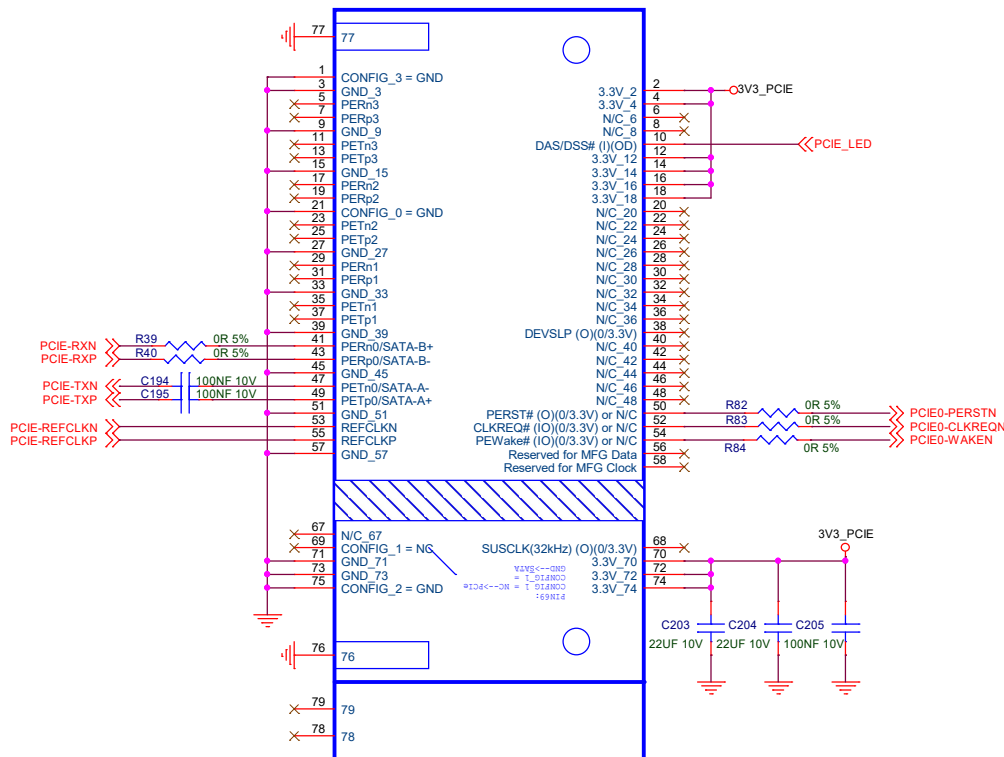
USB3.0



SEL (0=A to B,1=A to C)

PCIe2.0 x 1 Lane

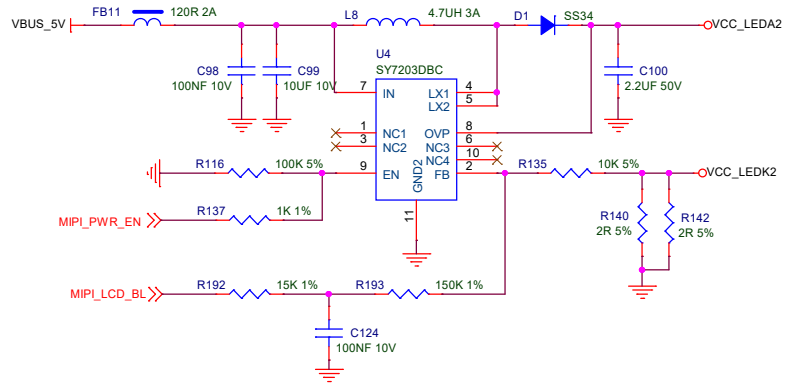
U16 M2_KEY_M



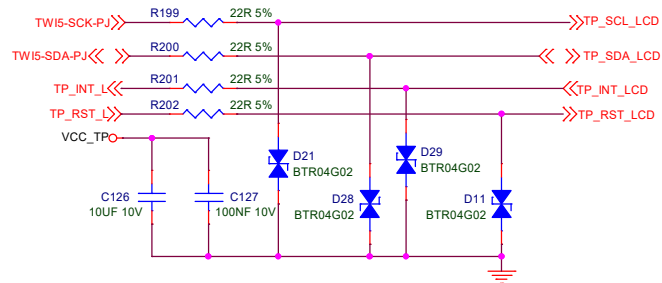
radxa

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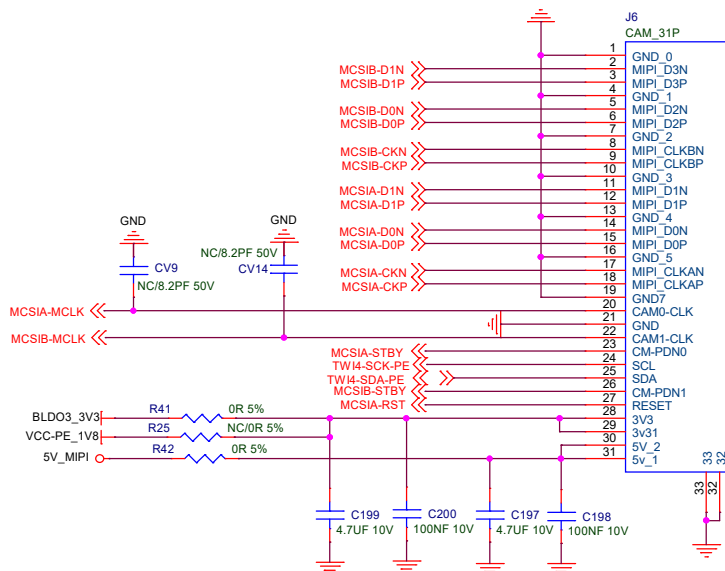
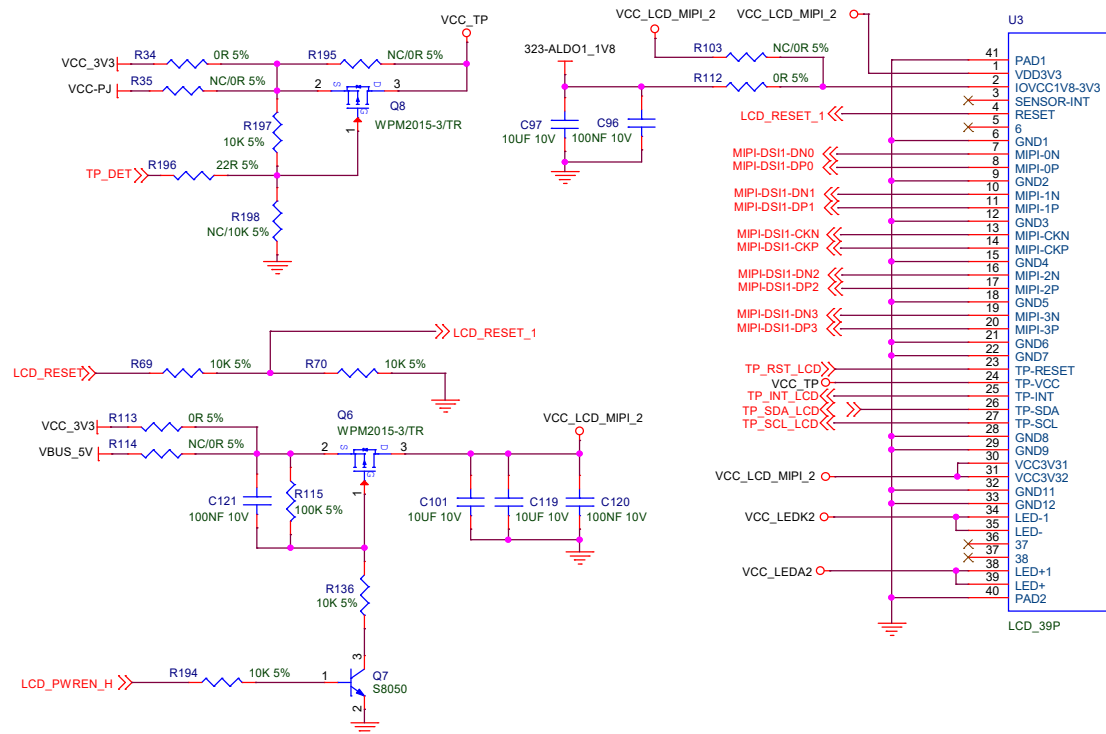
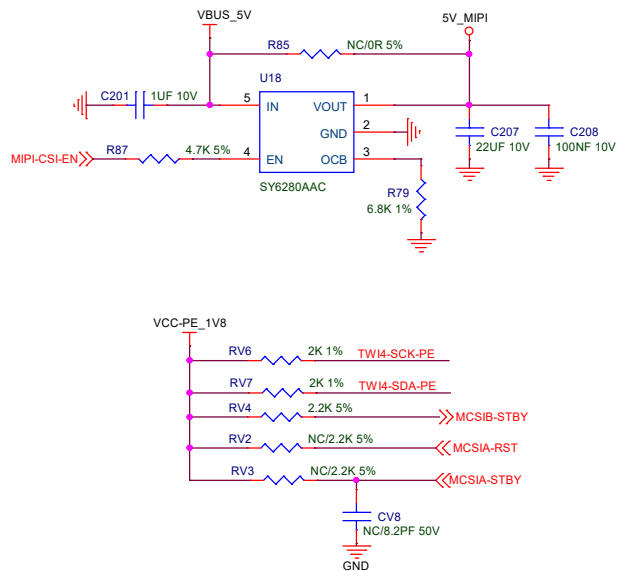
MIPI LCD



LCD TP

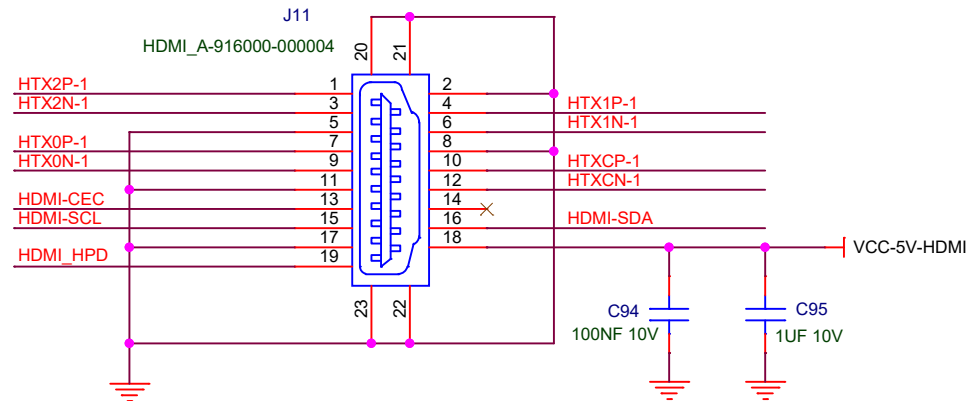
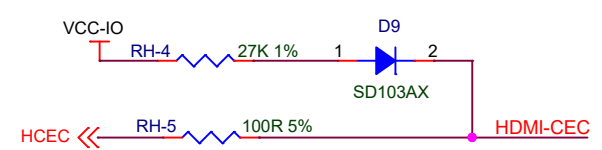
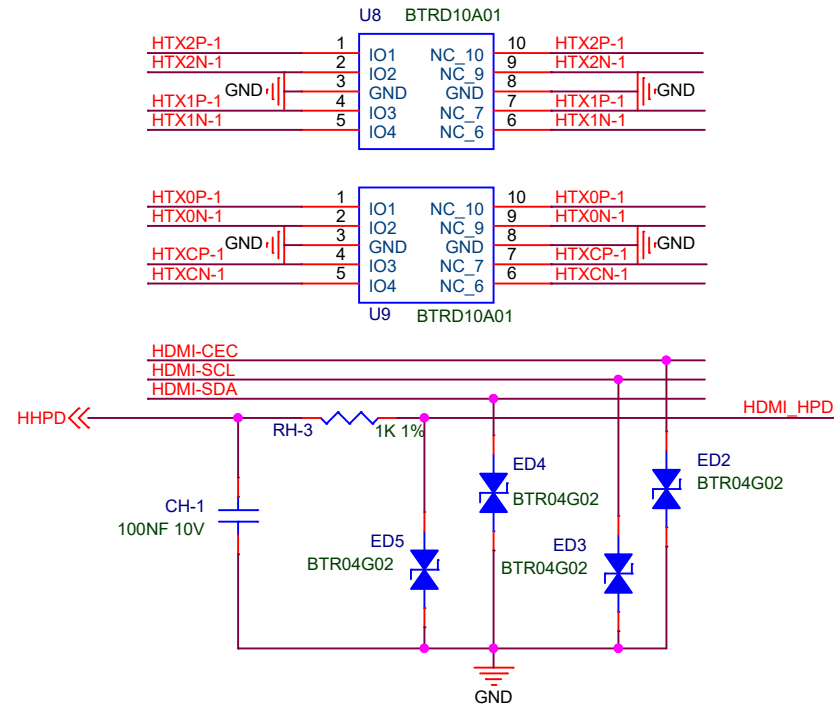
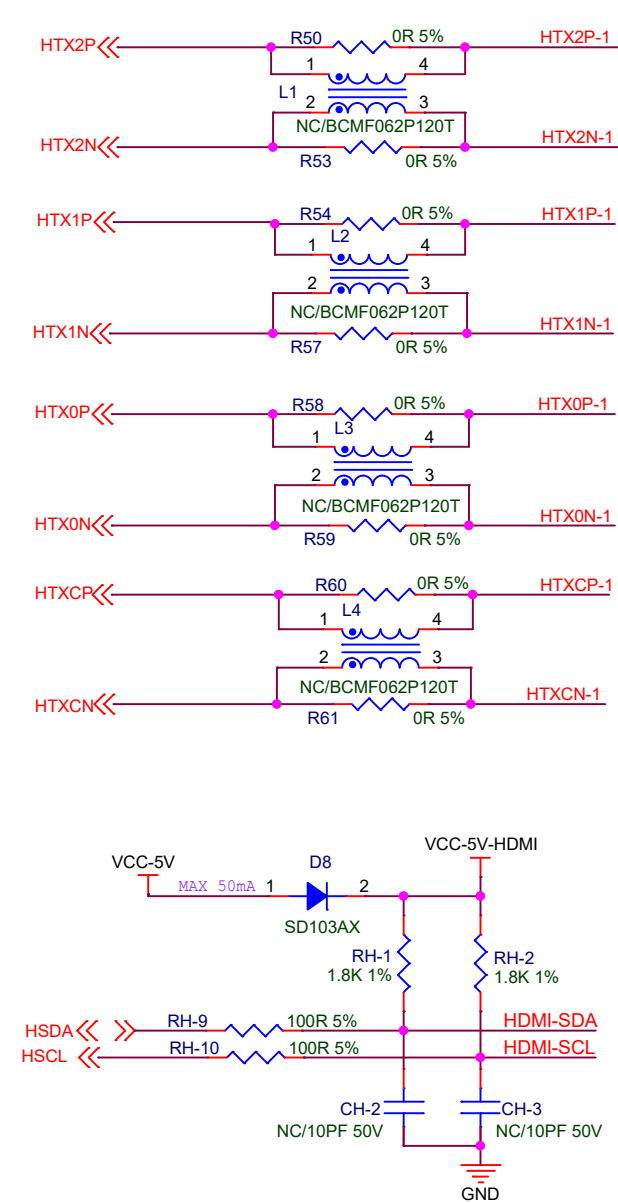


Camera



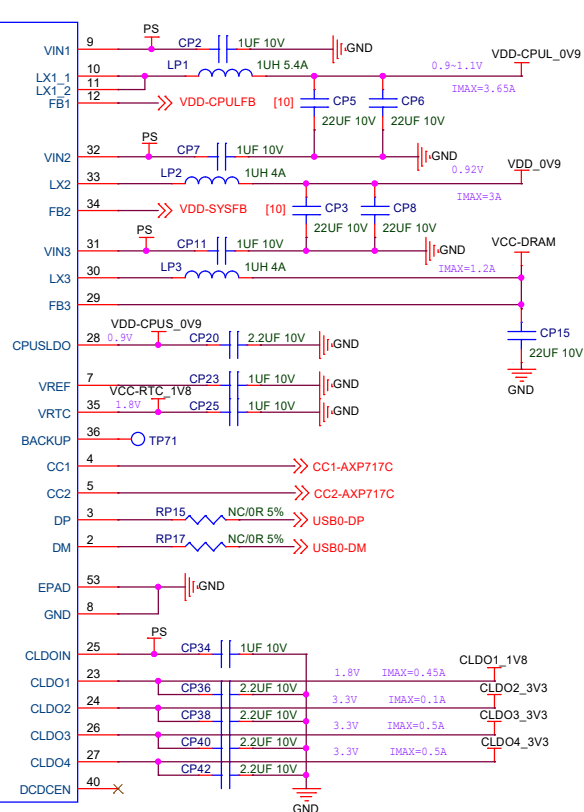
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HDMI OUT

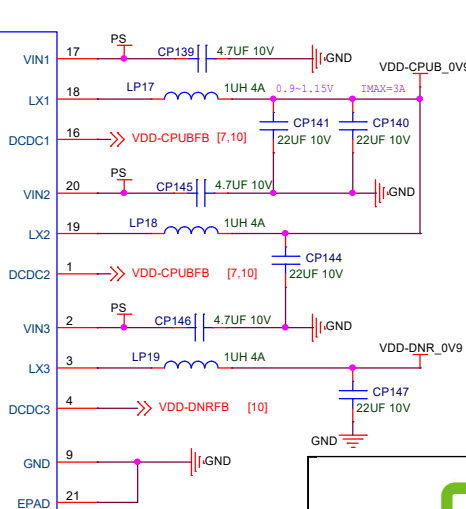


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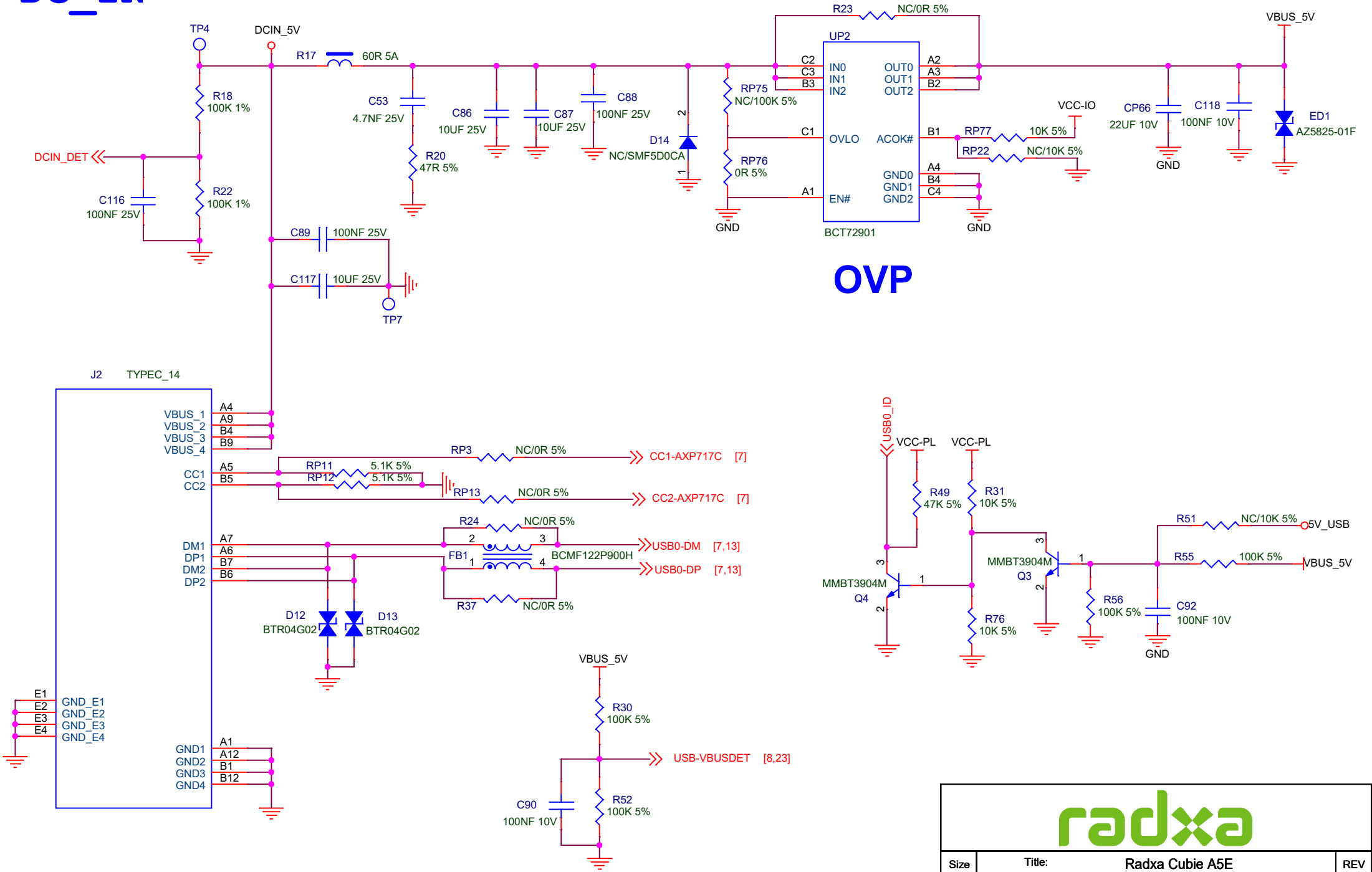


AXP323

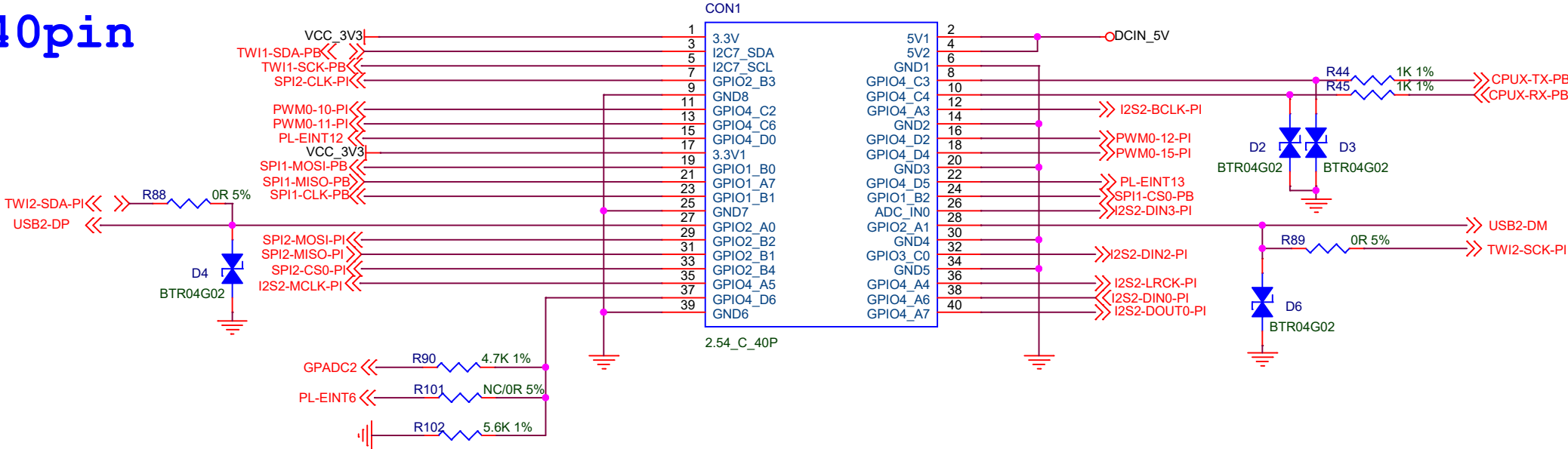


				
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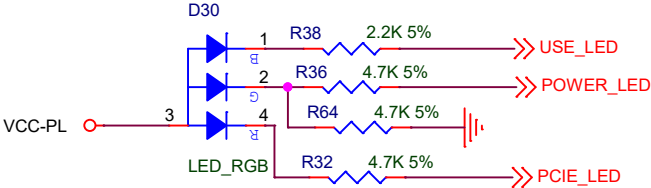
DC_IN



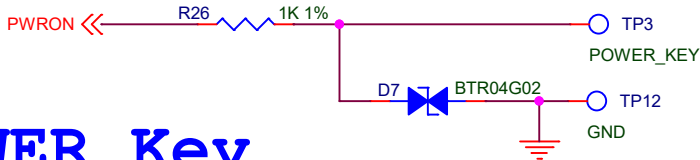
40pin



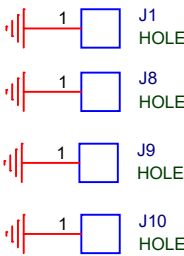
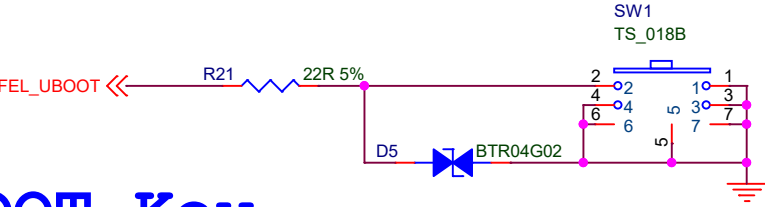
LED



POWER Key



UBOOT Key



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