



Radxa CM4 IO

Schematic

V1.10

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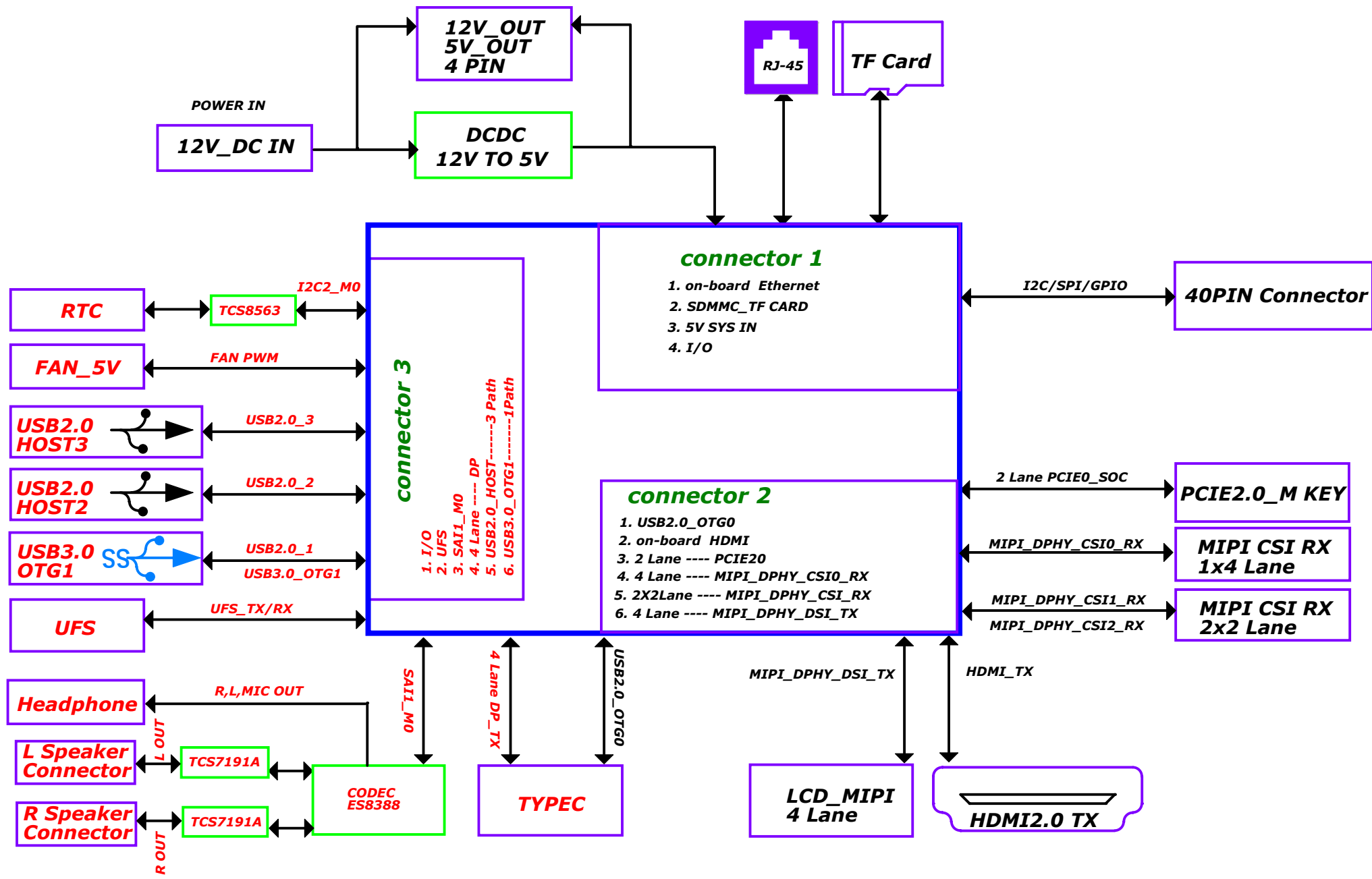
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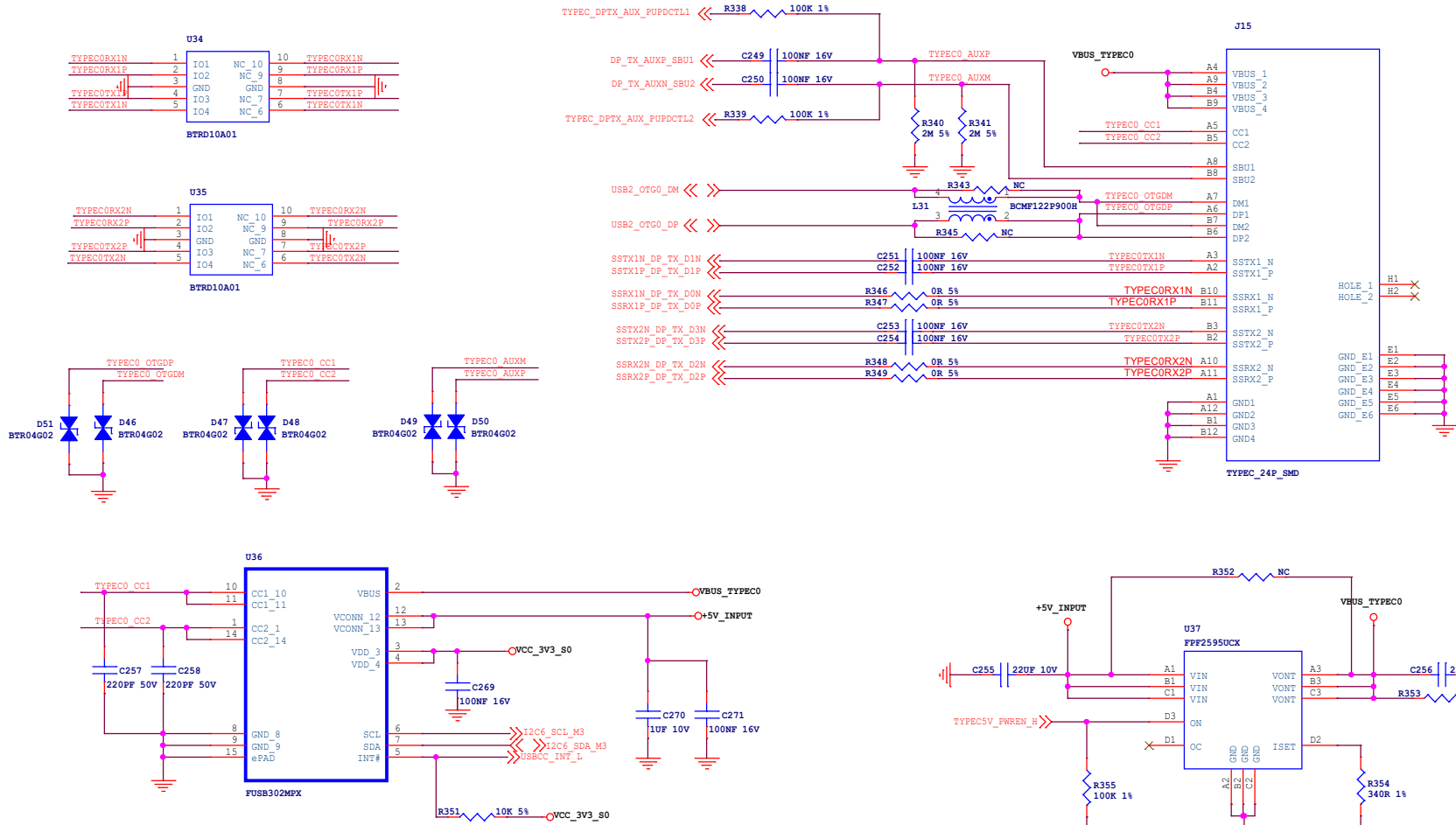
PCIE



Block Diagram

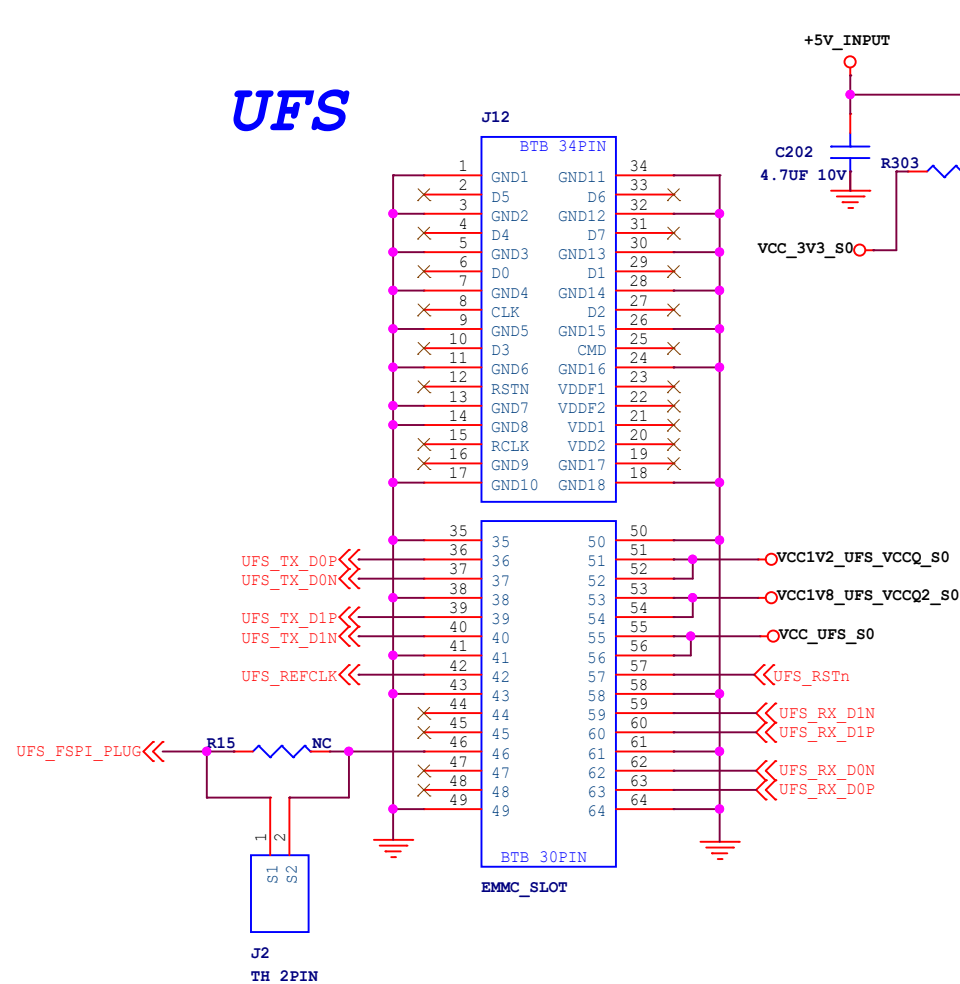


Type-C PORT

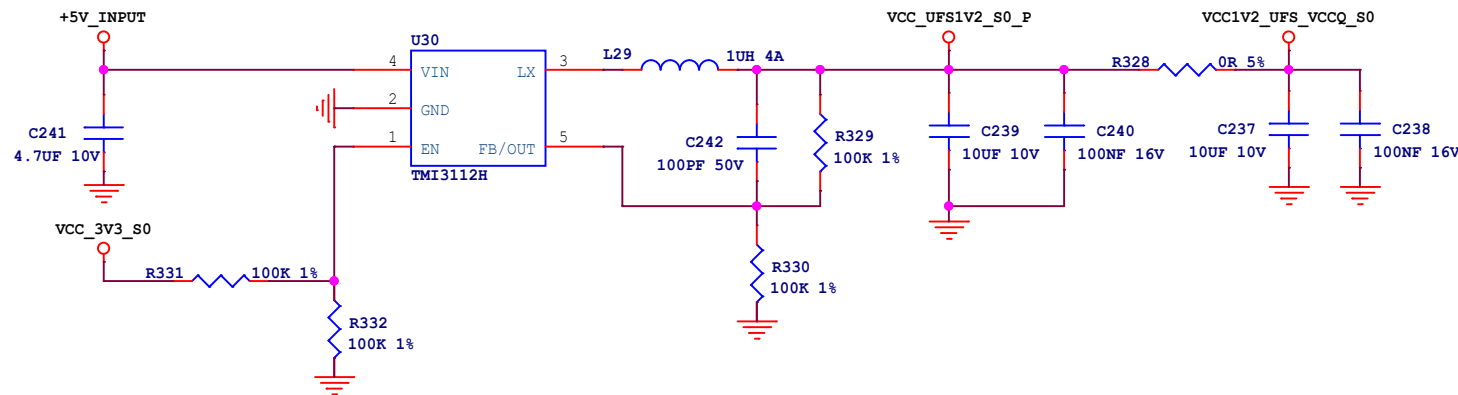
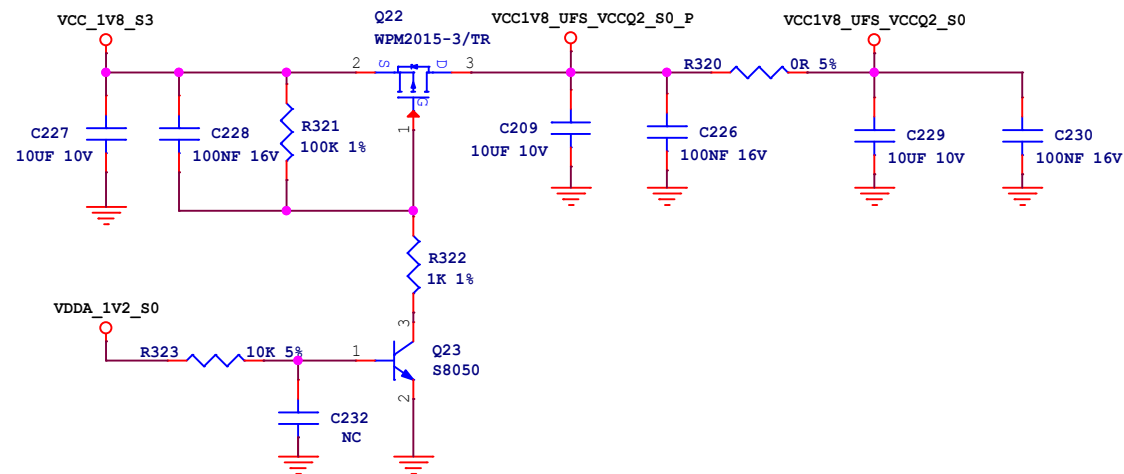
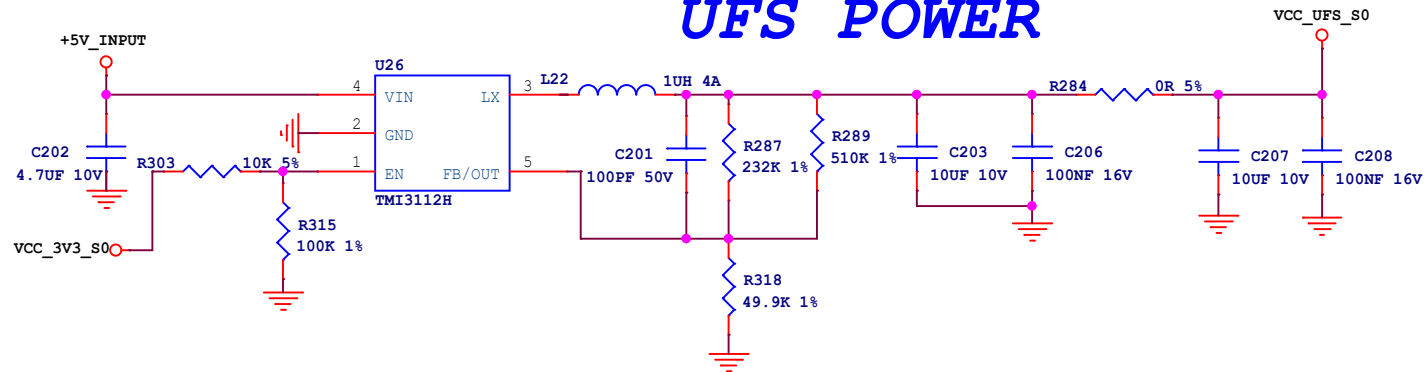


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UFS



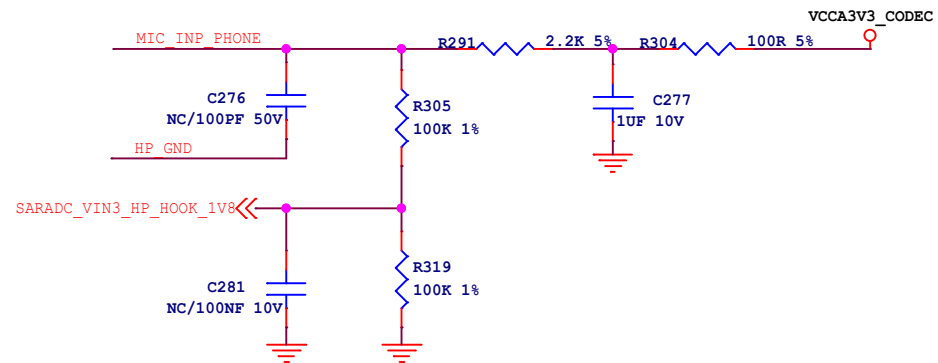
UFS POWER



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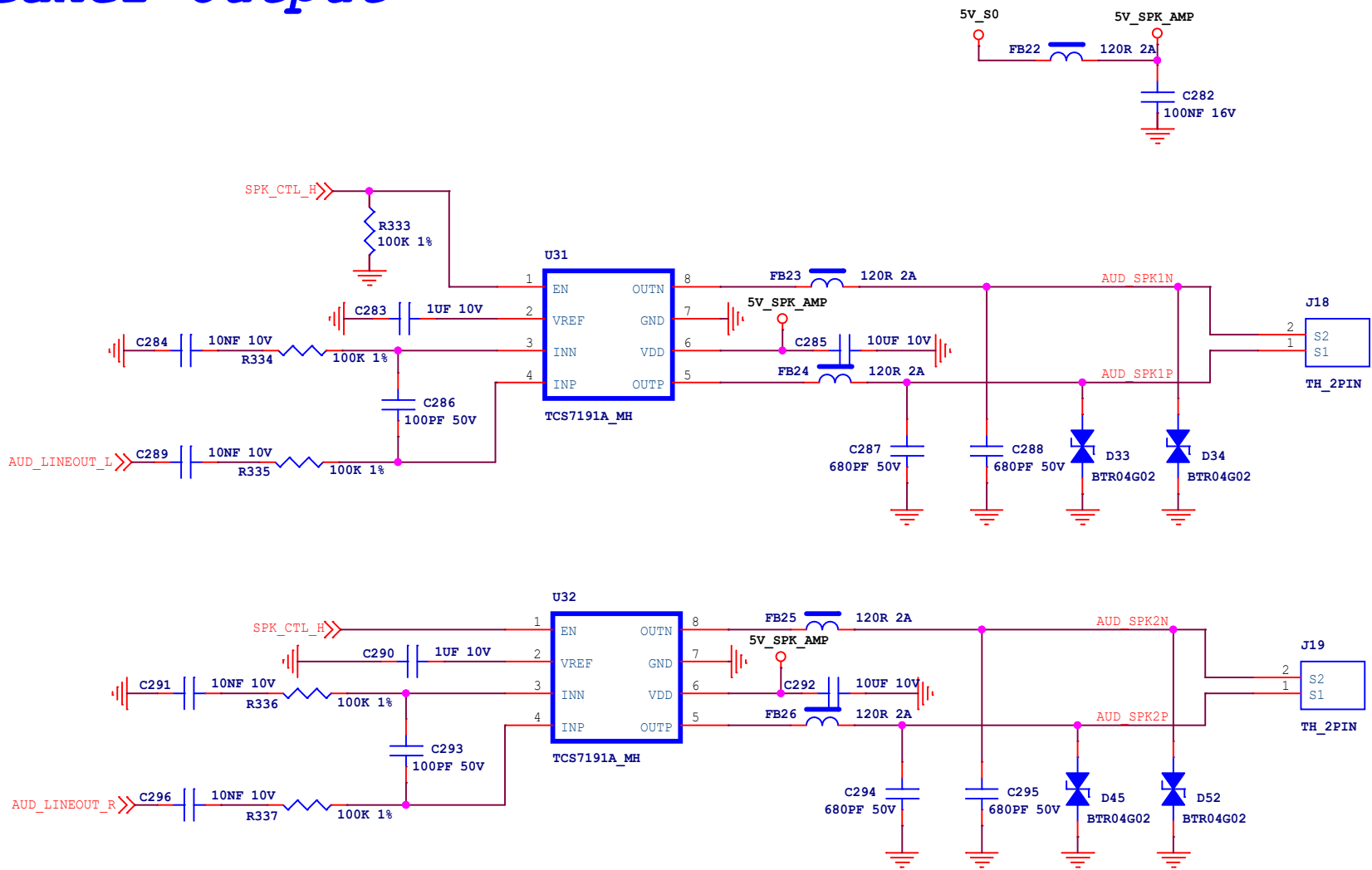
Size	Title: CM4 IO	REV
A4	Page Name: 05 UFS	V1.10
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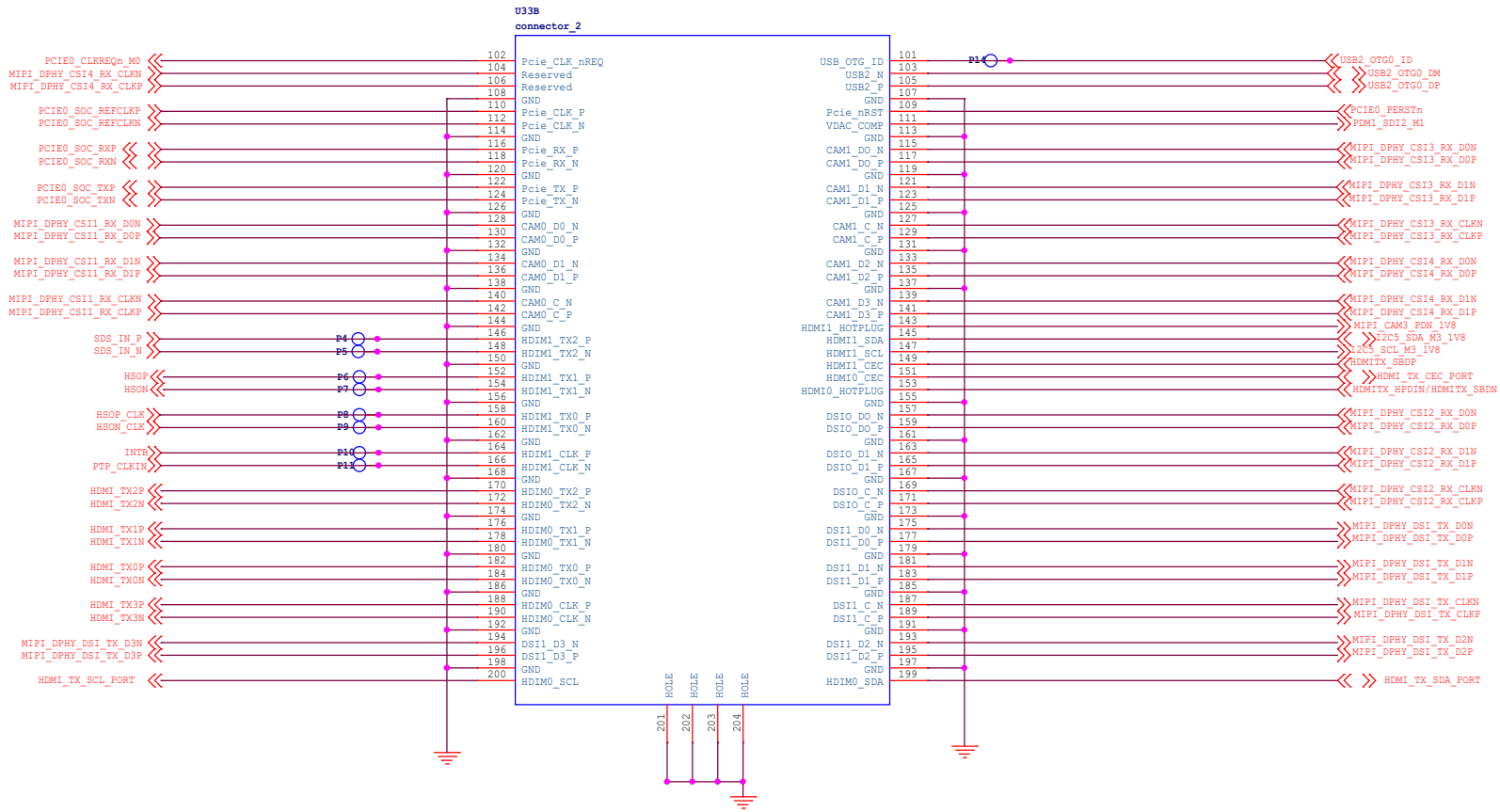
Headphone

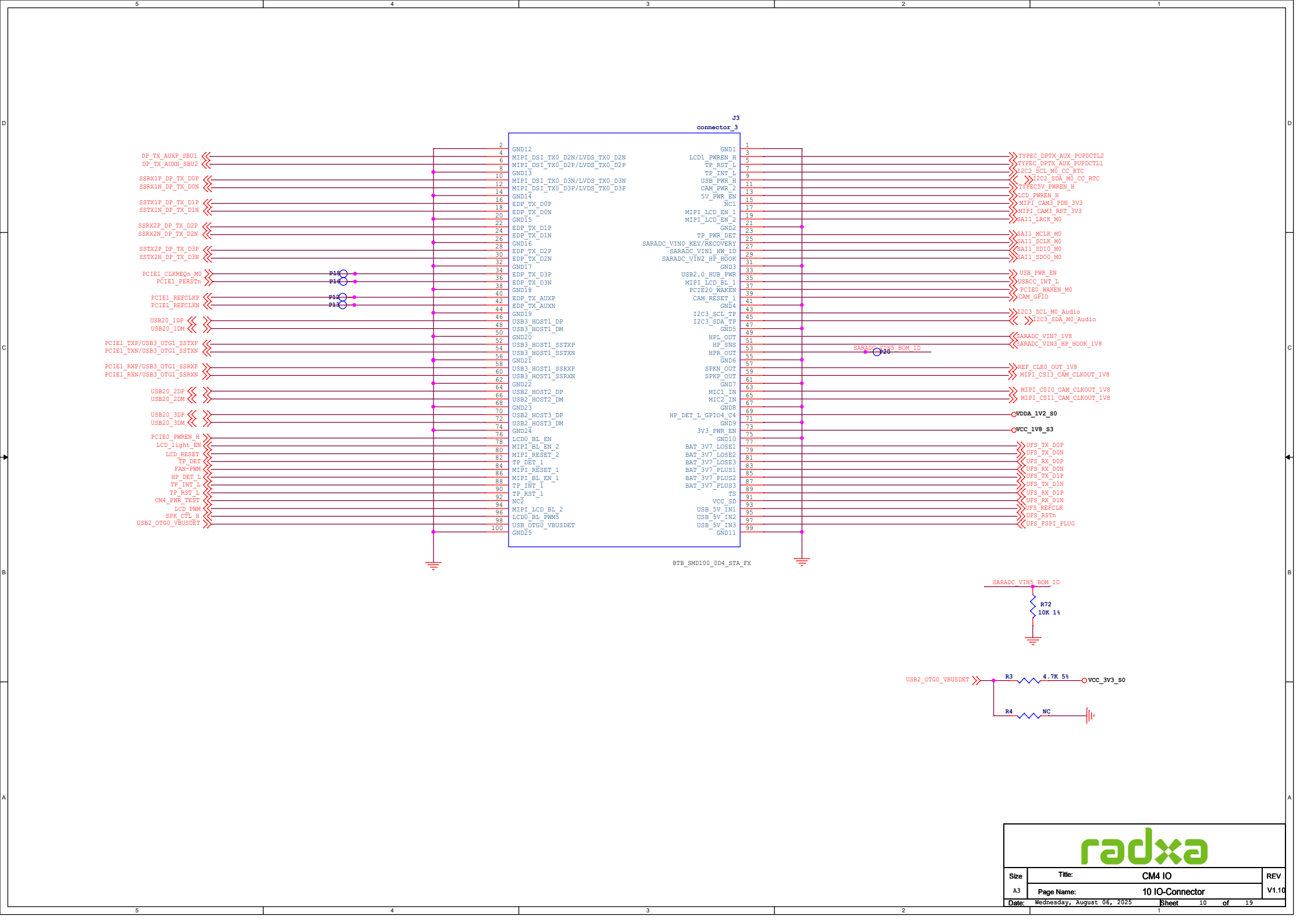


Size	Title: CM4 IO	REV V1.10
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A4		
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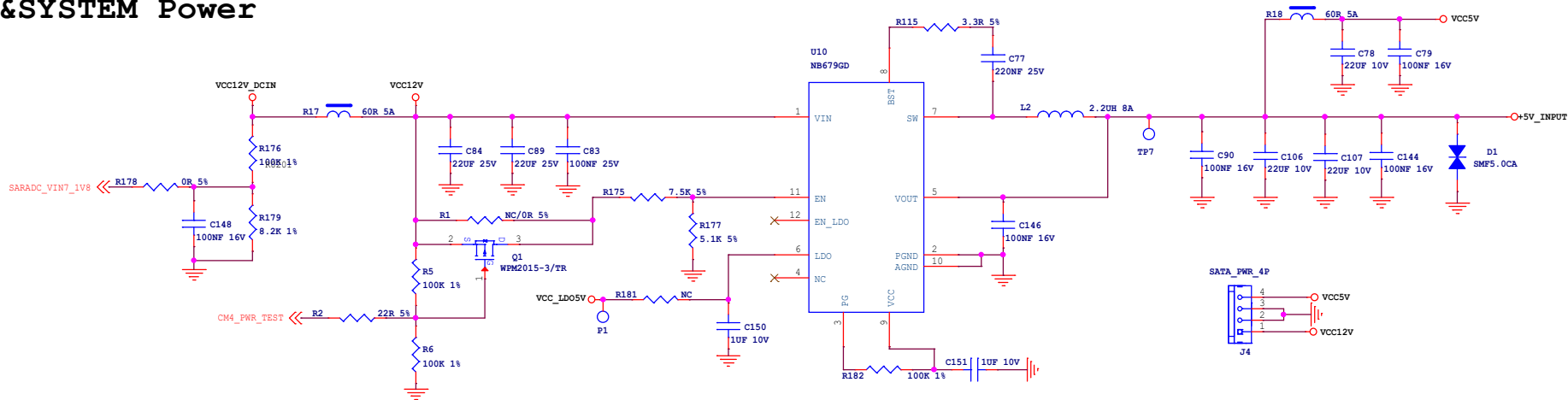
Speaker Output



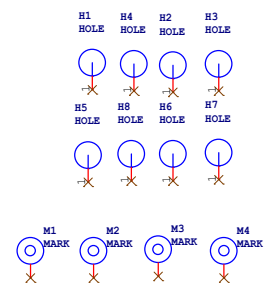
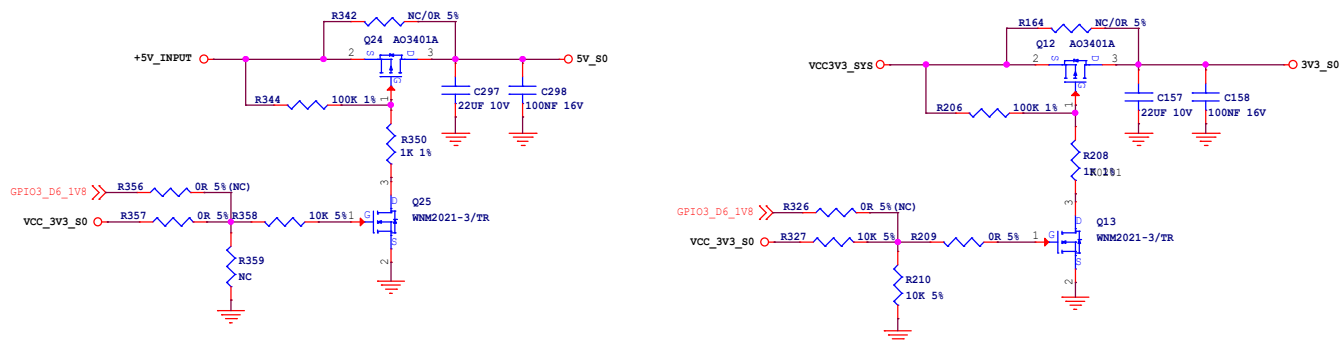
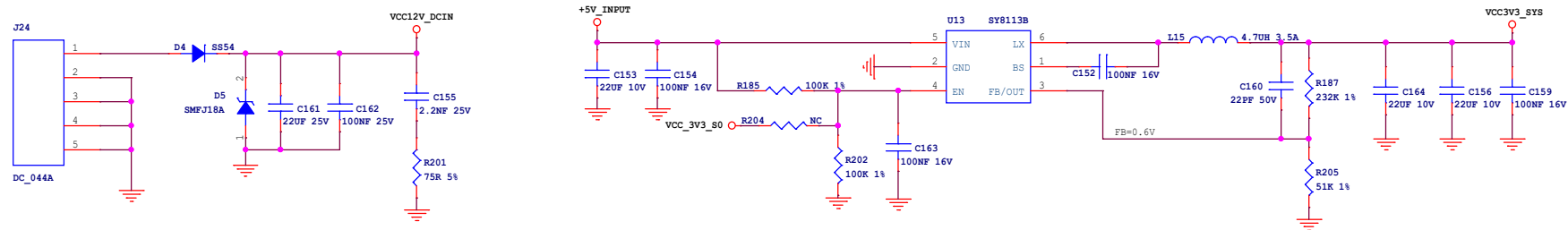




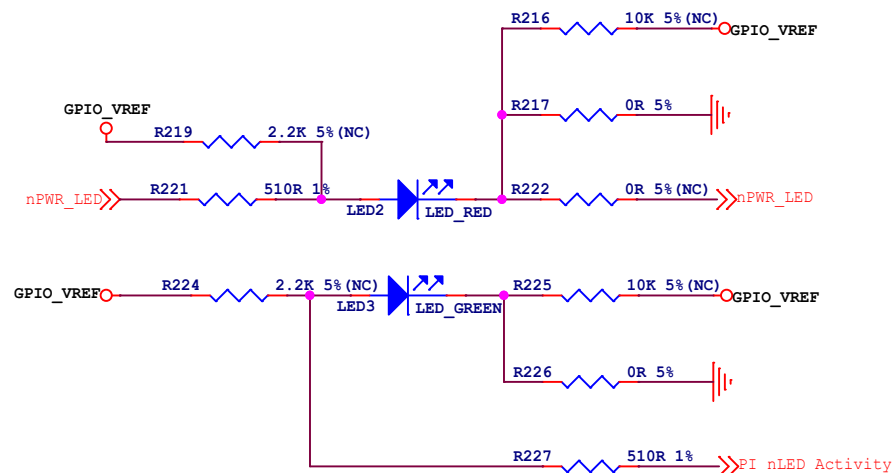
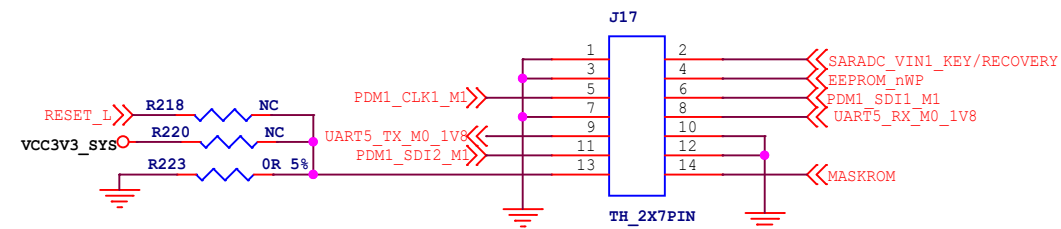
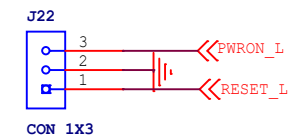
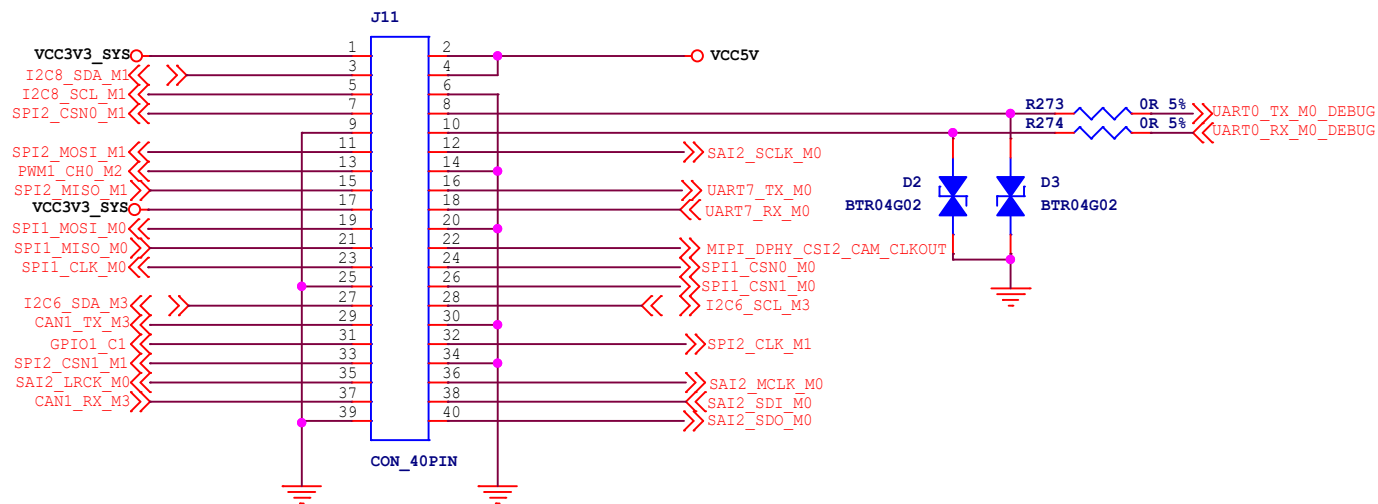
DC IN&SYSTEM Power



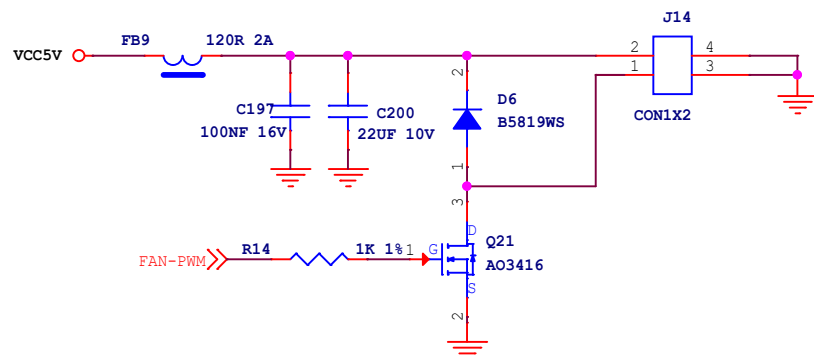
DC12V



40 PIN



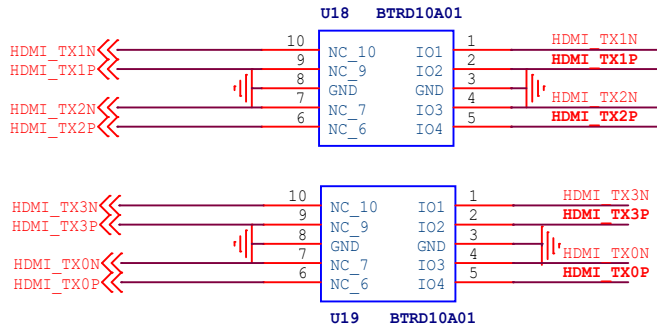
FAN



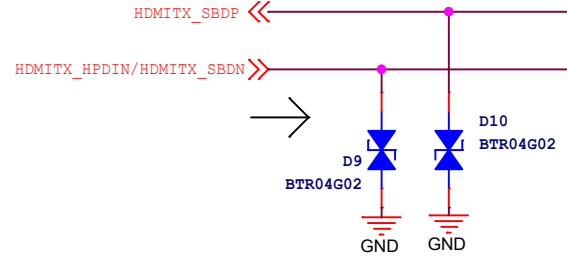
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A4	Page Name: 12 40PIN/FAN	V1.10
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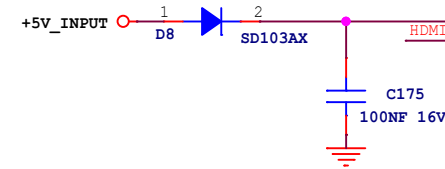
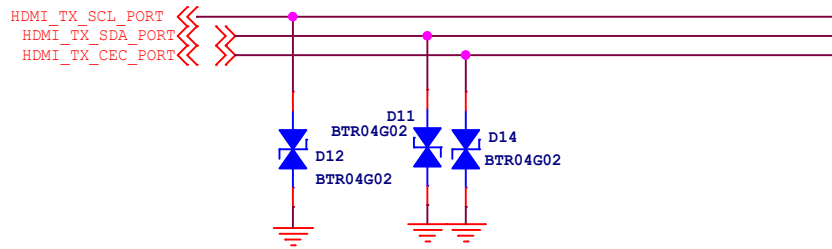
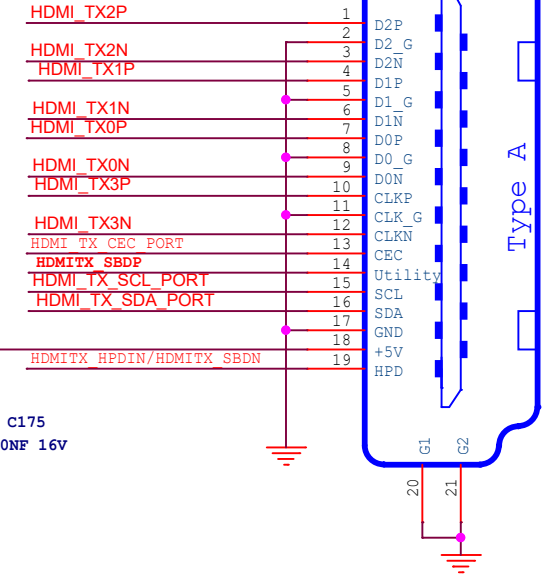
HDMI



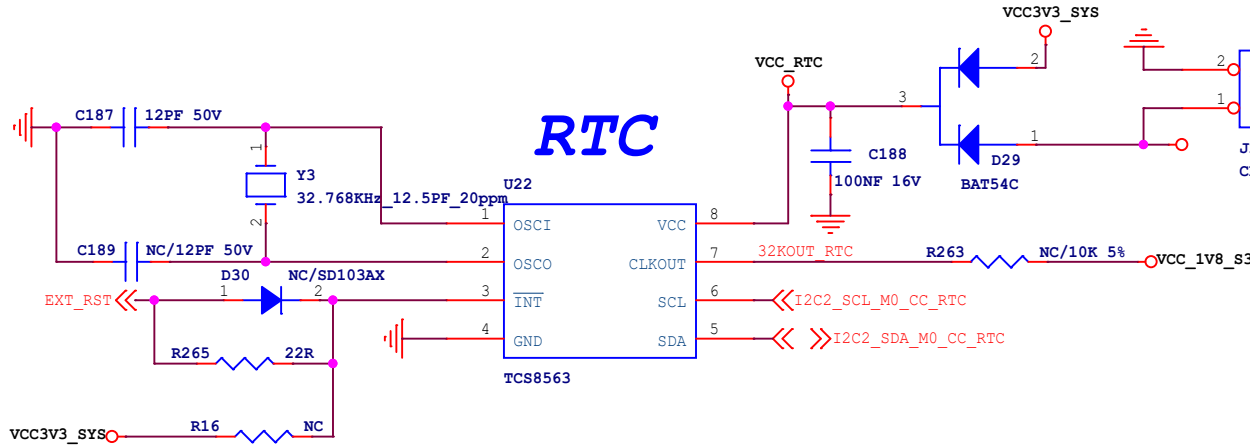
HDMI TX ARC/HPD



J5
HDMI_TYPE_A



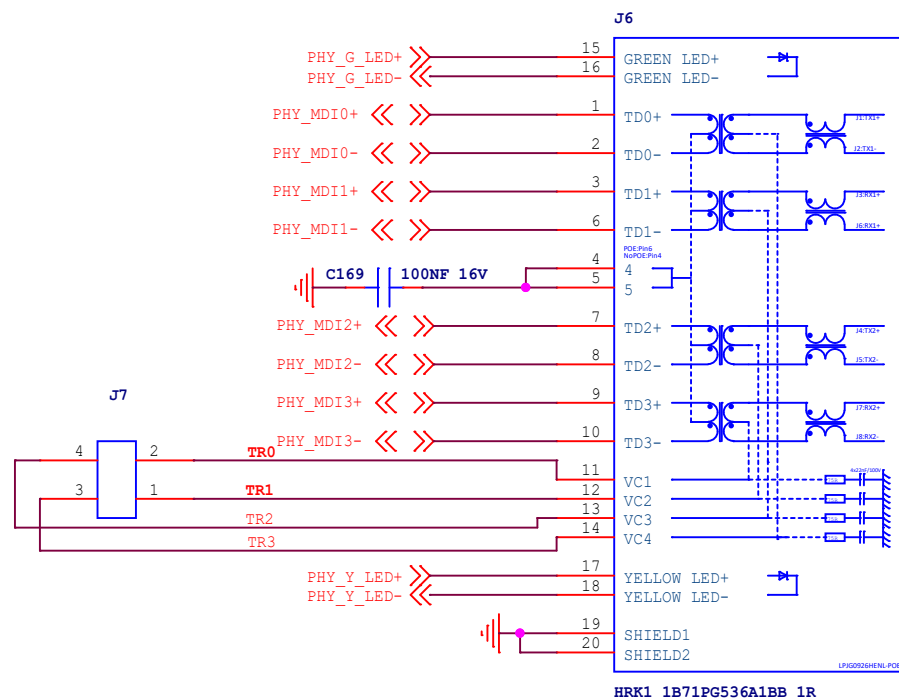
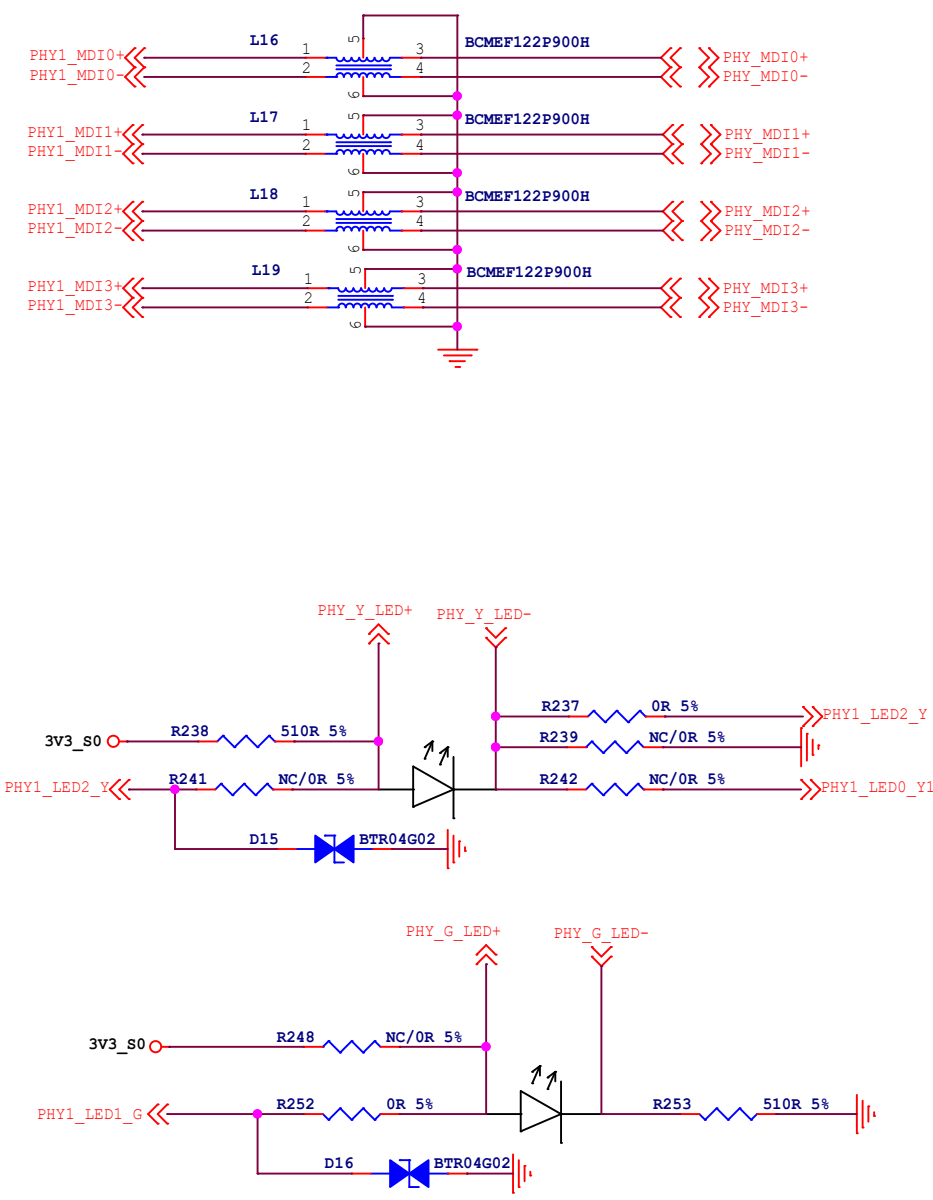
RTC_BATTERY



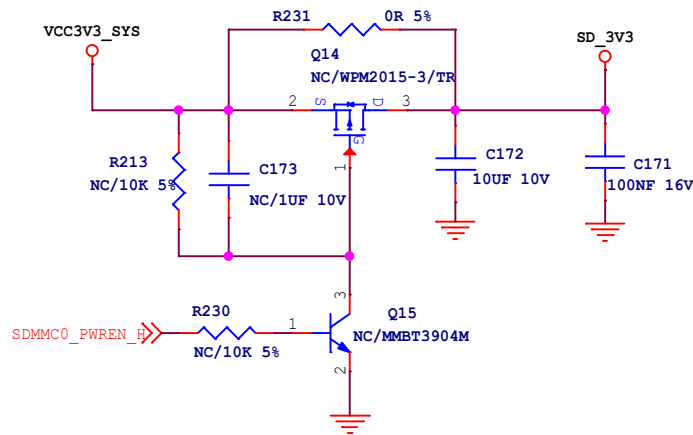
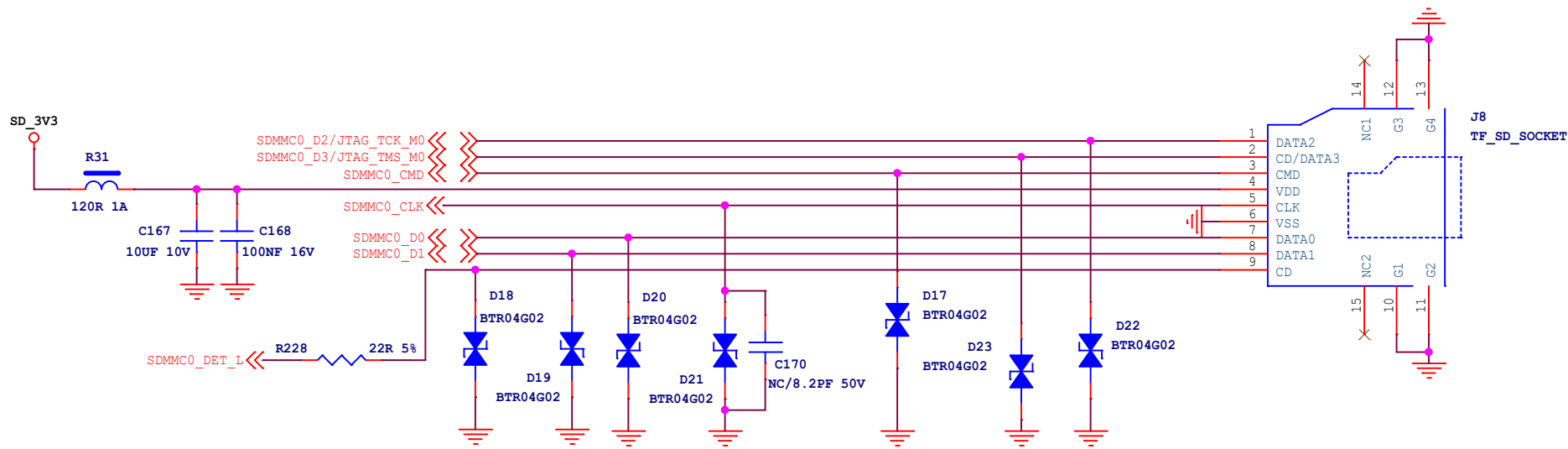
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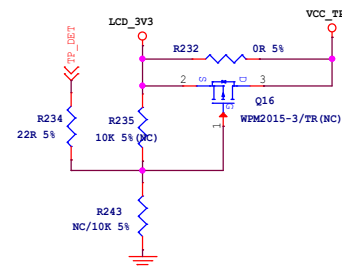
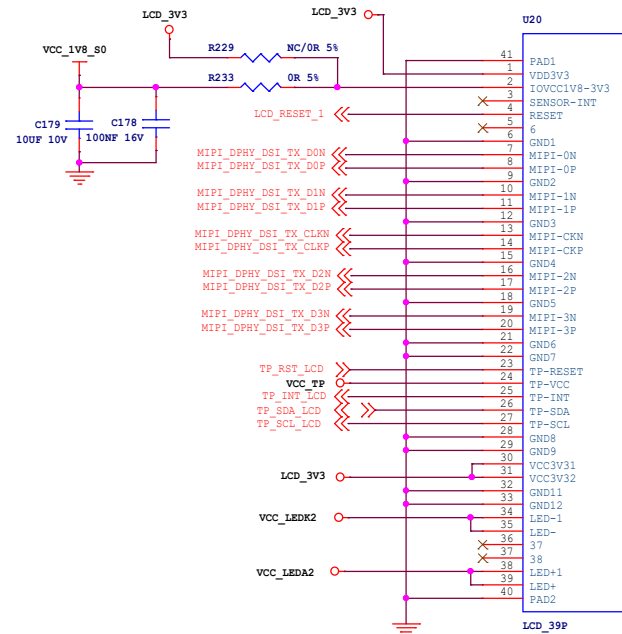
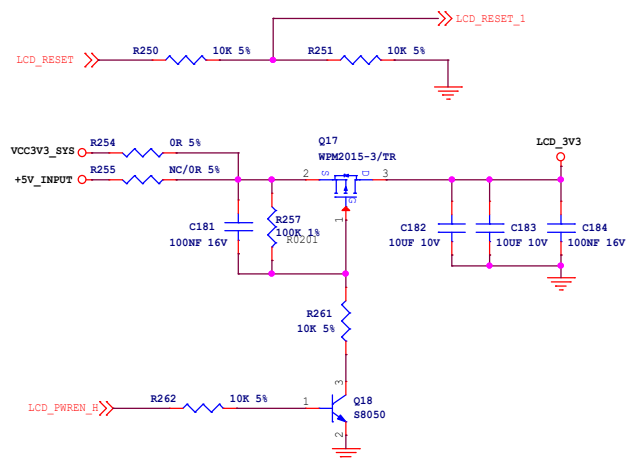
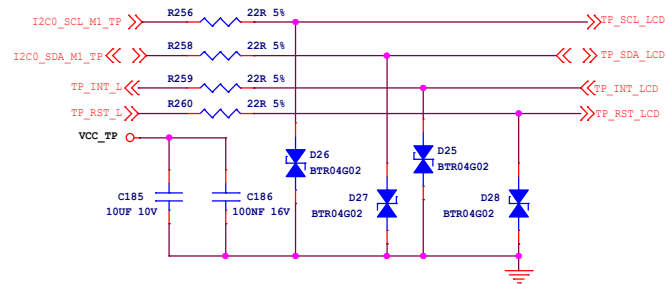
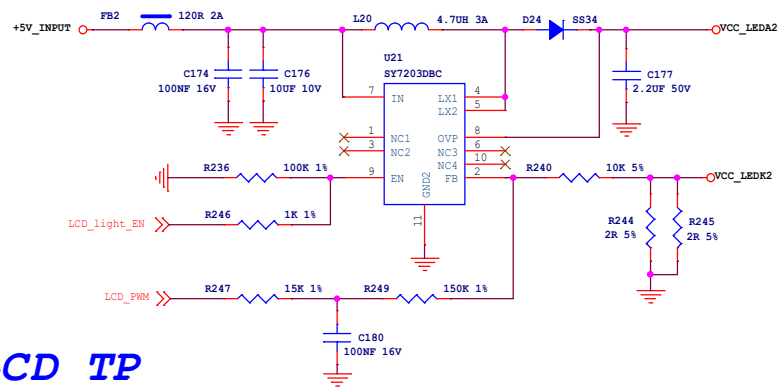
ETHERNET



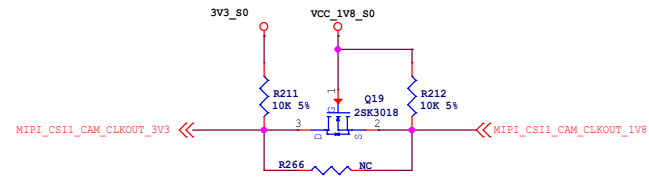
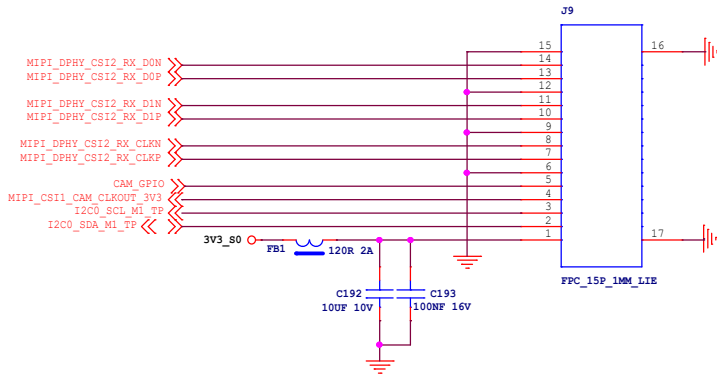
MicroSD Card



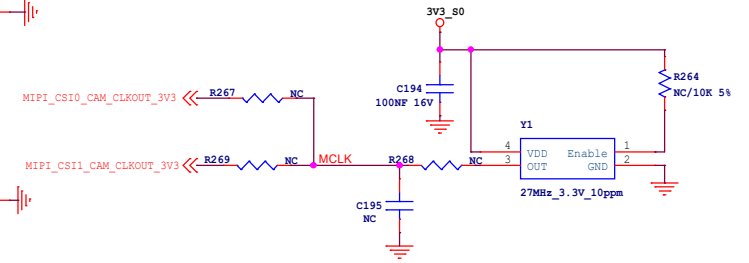
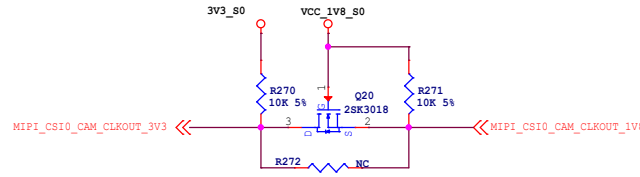
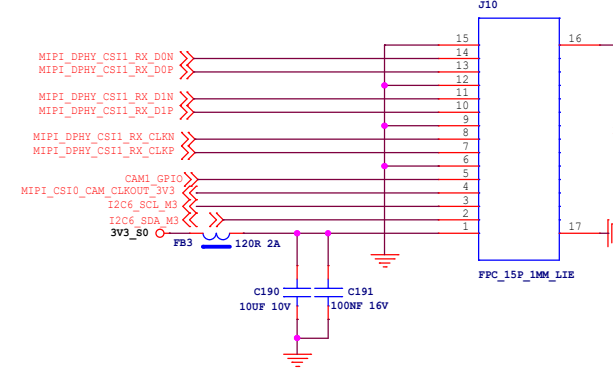
LCD_TP



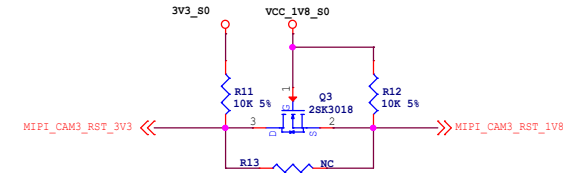
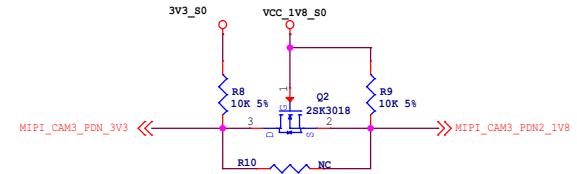
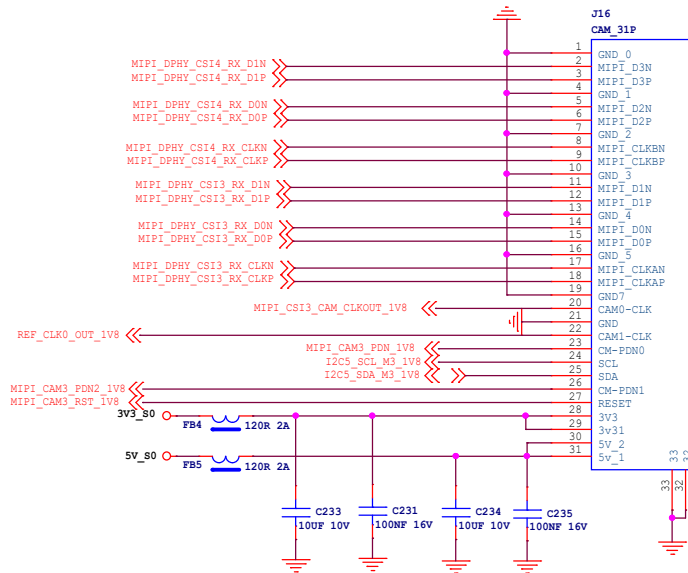
MIPI_CSI2_RX 2Lanes



MIPI_CSI1_RX 2Lanes



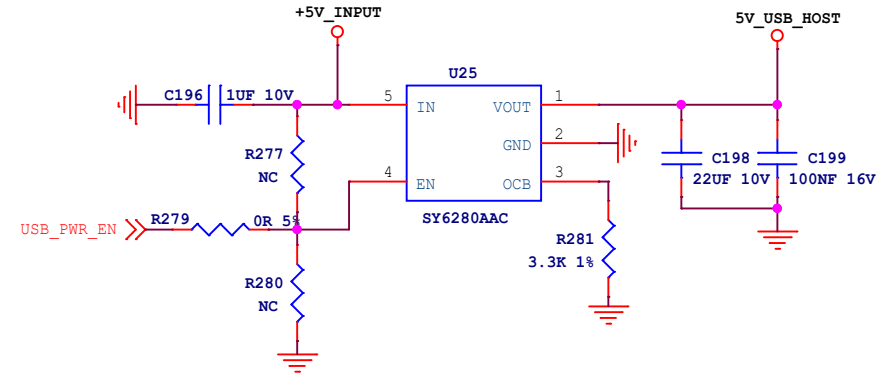
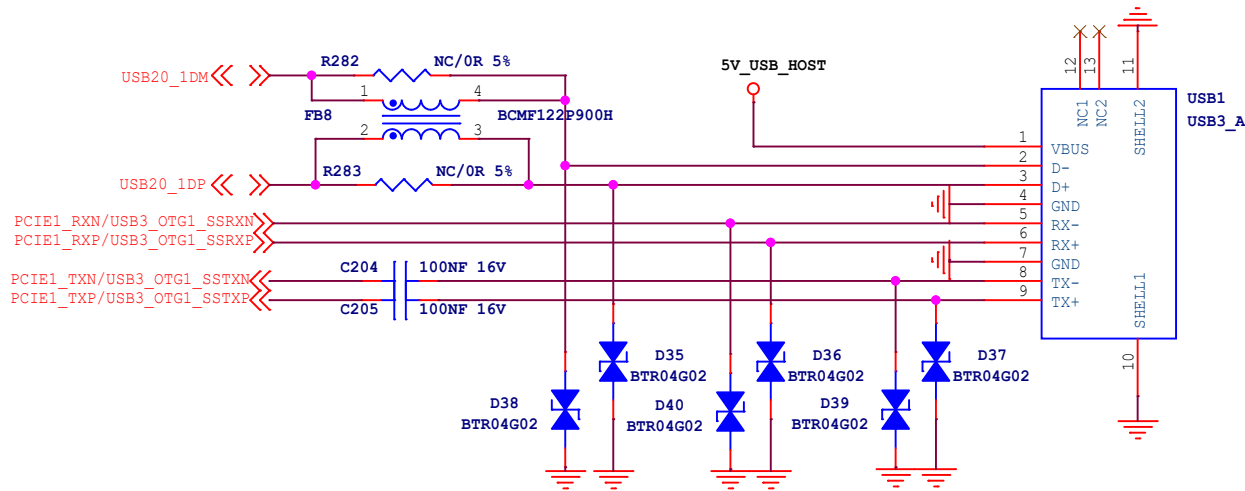
MIPI_CSI3_RX_2X2Lanes



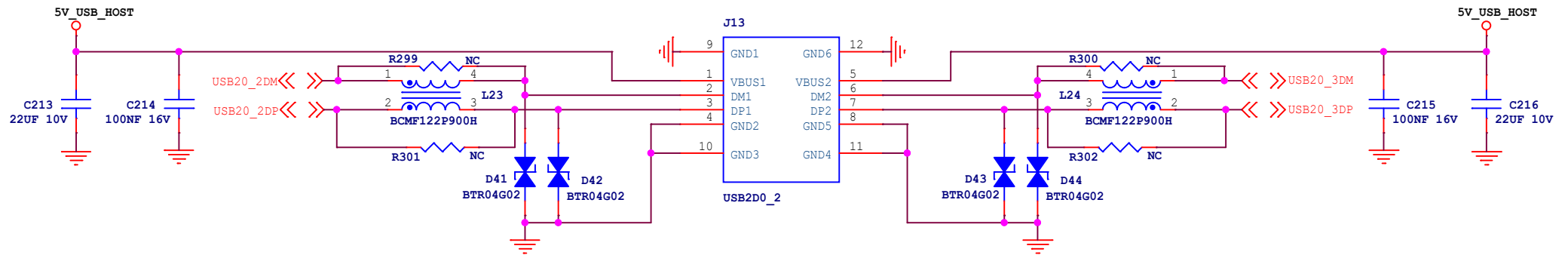
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USB3.0



USB2.0



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PCIe2.0 x 1 Lane

