

54321

VERSION HISTORY

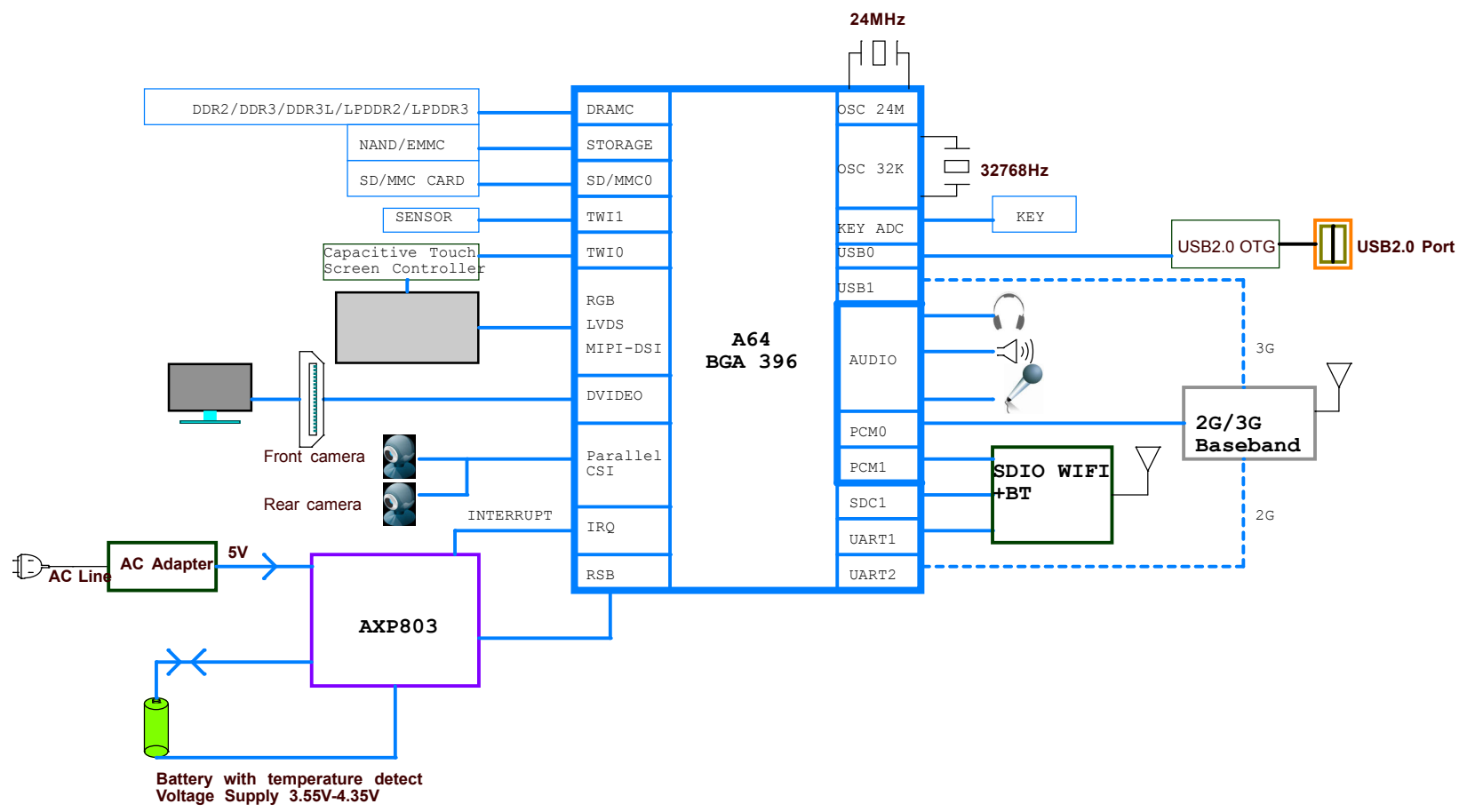
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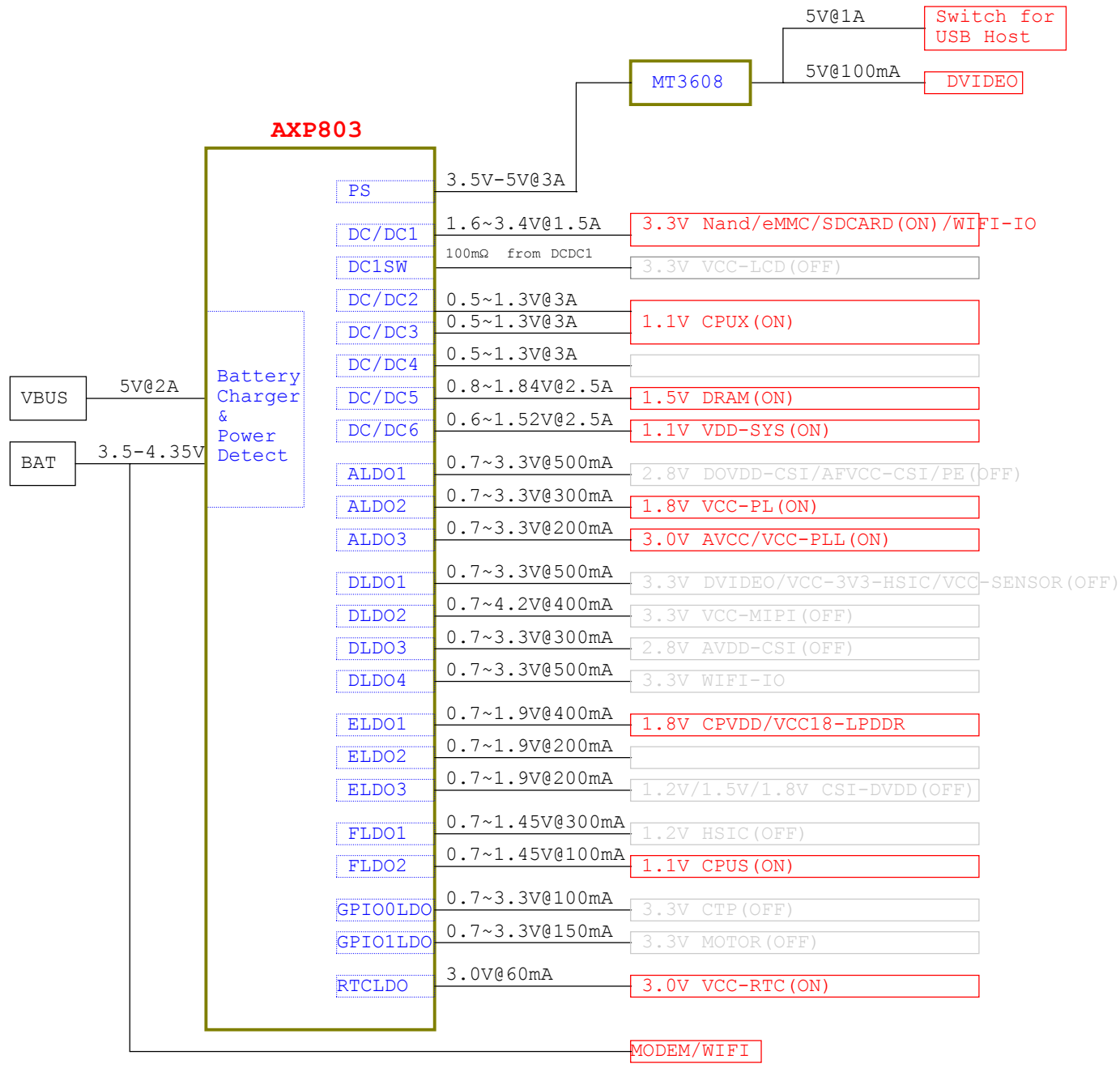
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02 BLOCK DIAGRAM
03 POWER TREE
04 LPDDR3 FGBA178
05 CPU
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Revision	Description	Date	Drawn	Checked	Approved
Ver 3.0	Release version	2017-05-03			

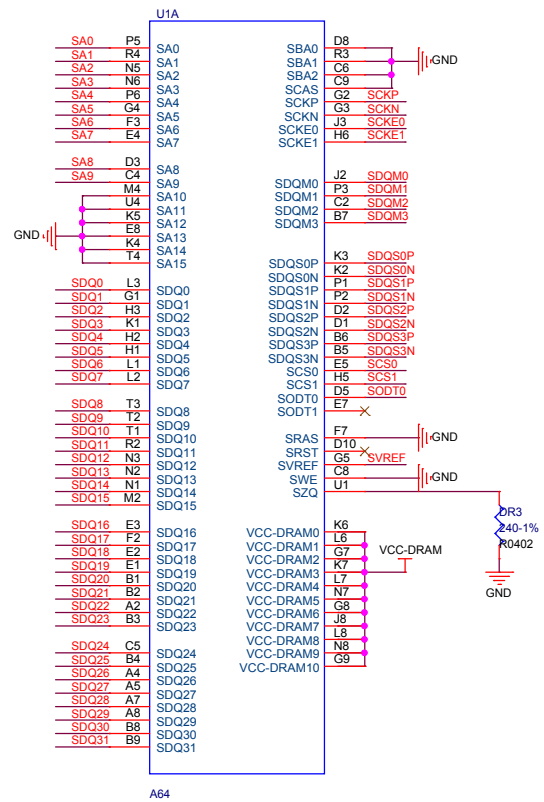
BLOCK DIAGRAM



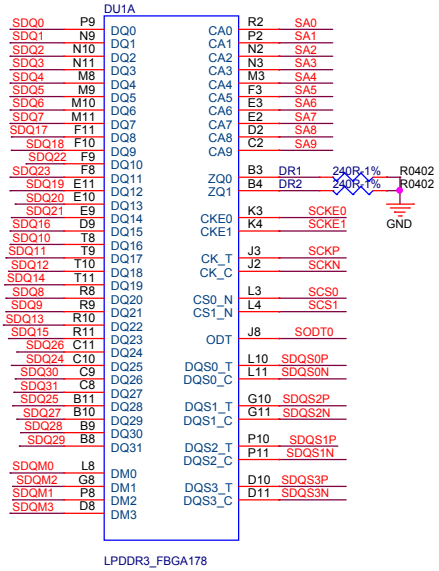
POWER TREE



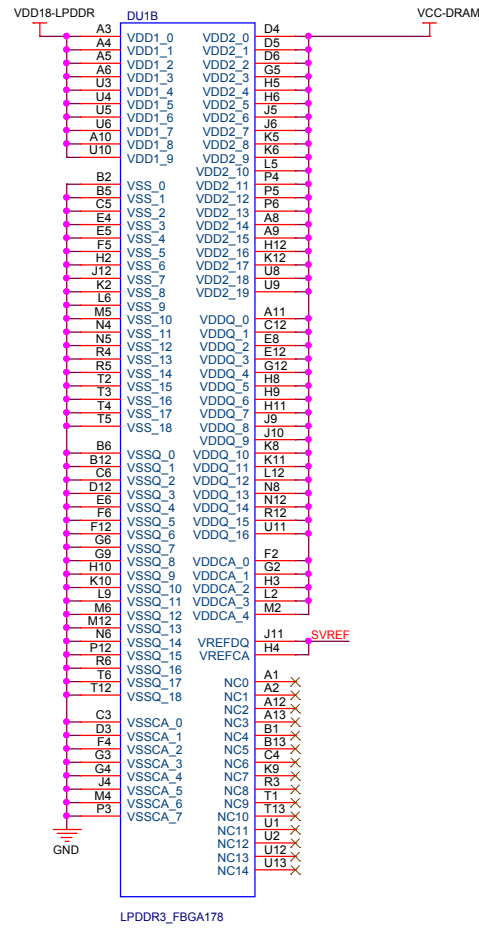
LPDDR3



2017-01-10

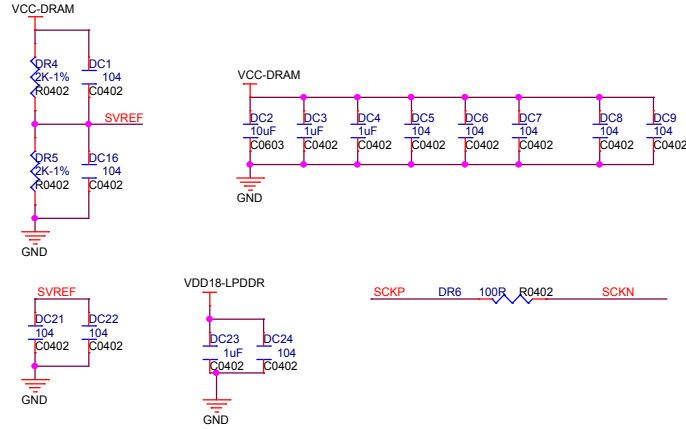


LPDDR3_FPGA178

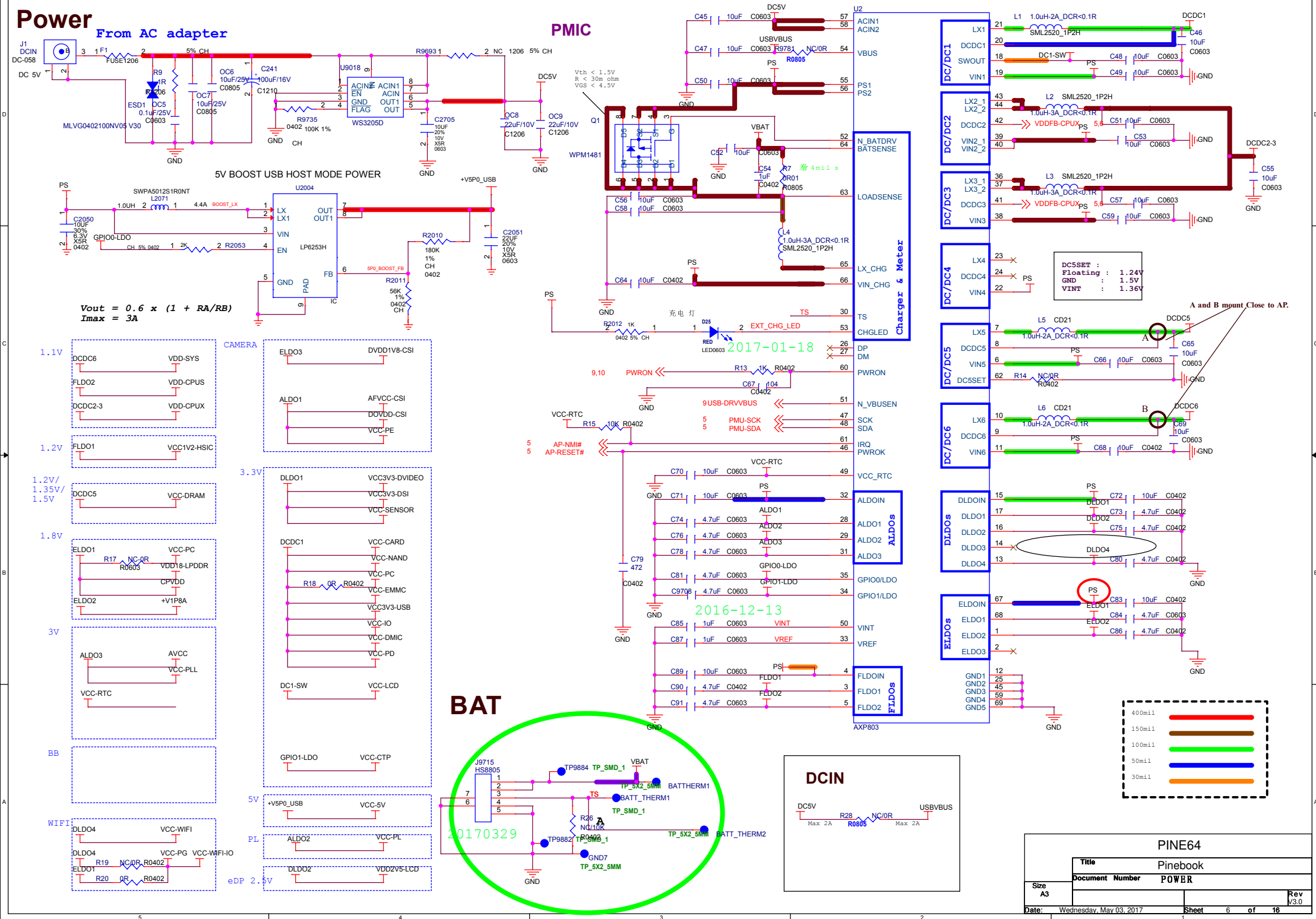


LPDDR3_FPGA178

2017-01-10

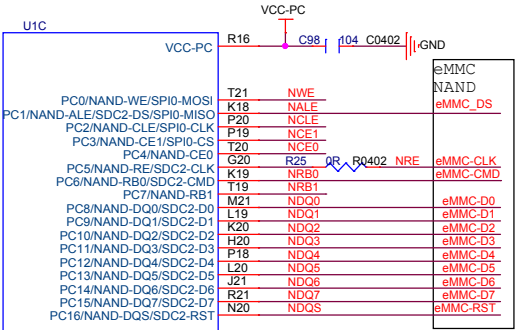


Power

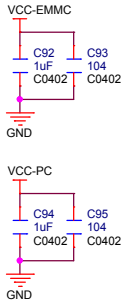
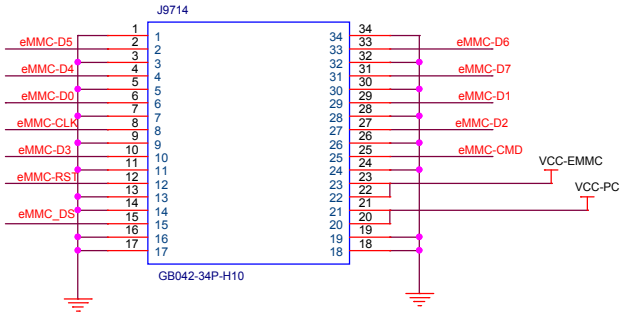
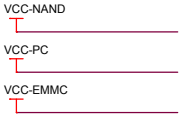


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NAND/eMMC



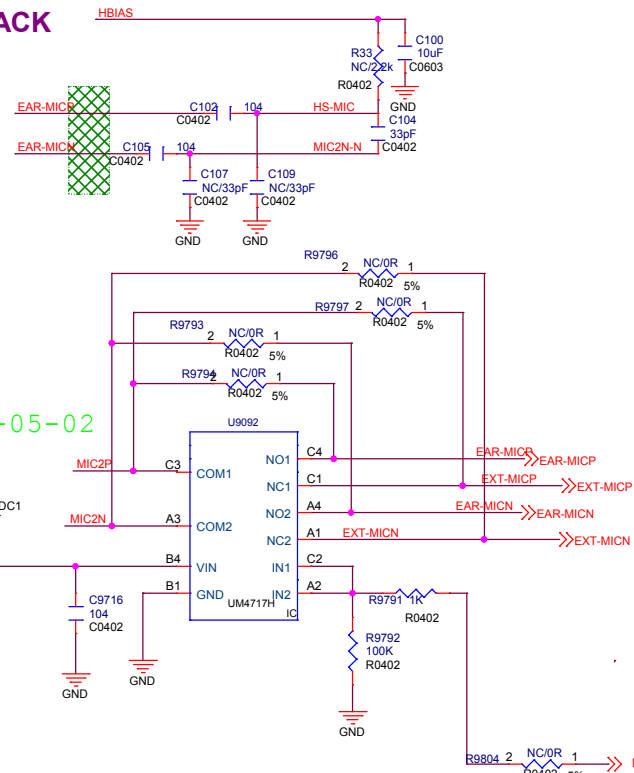
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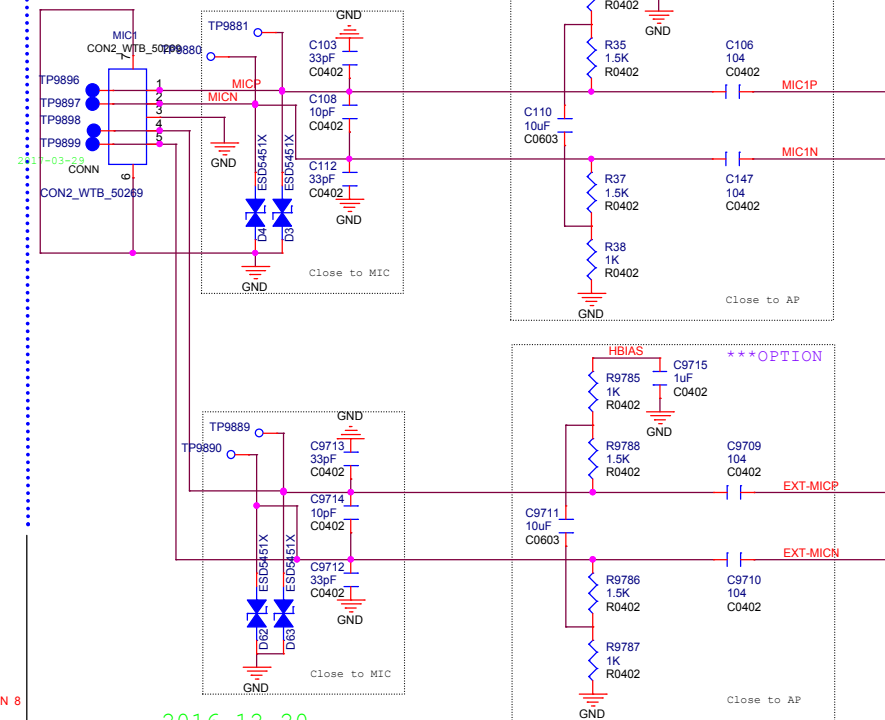
Both eMMC NAND and first TSOP NAND layout together

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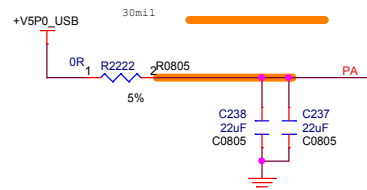
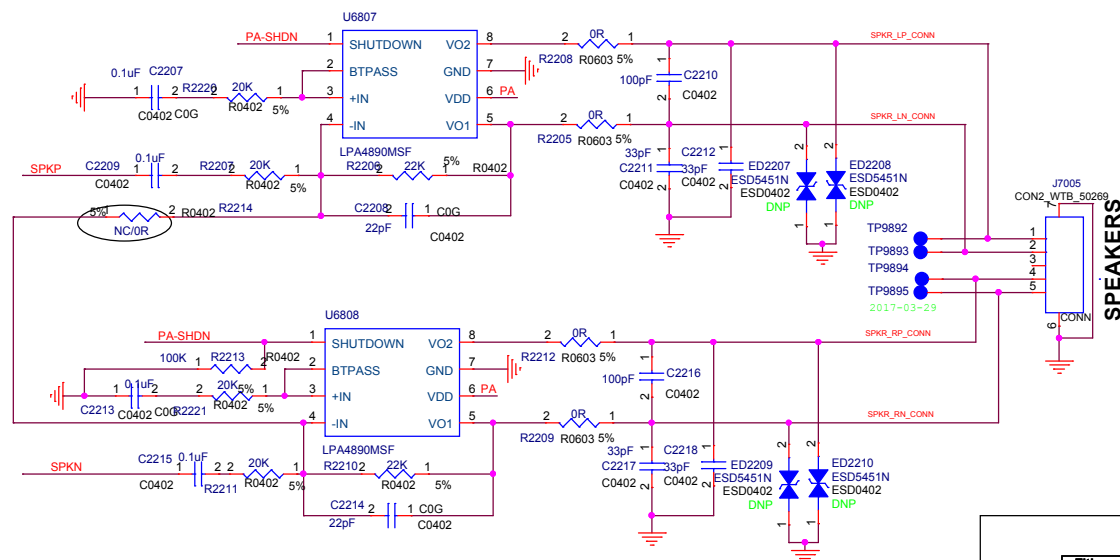
AUDIO



2017-05-02

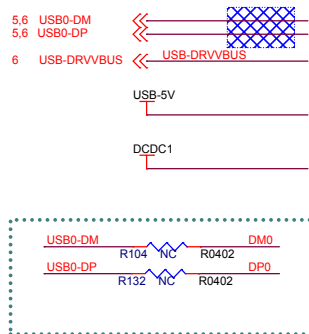
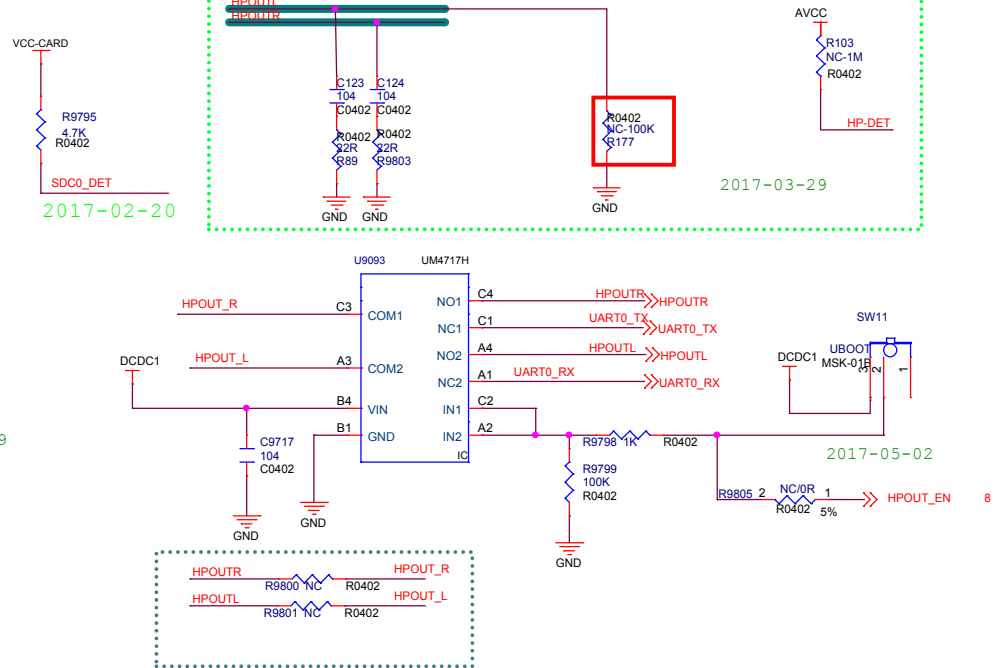
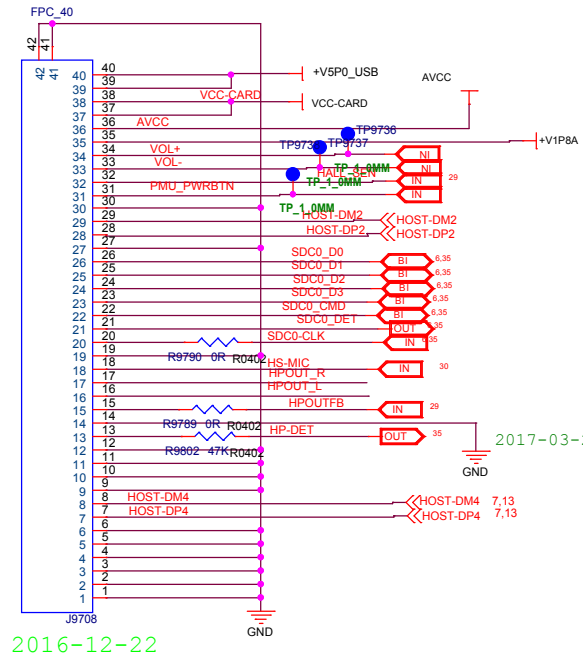
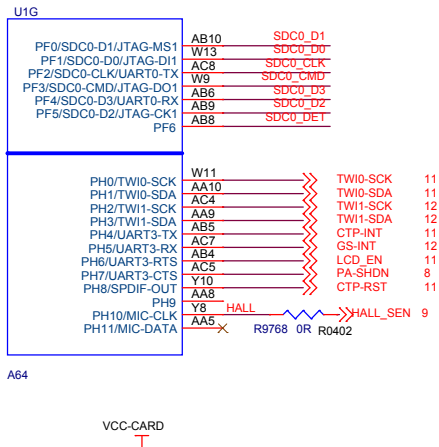


2016-12-20

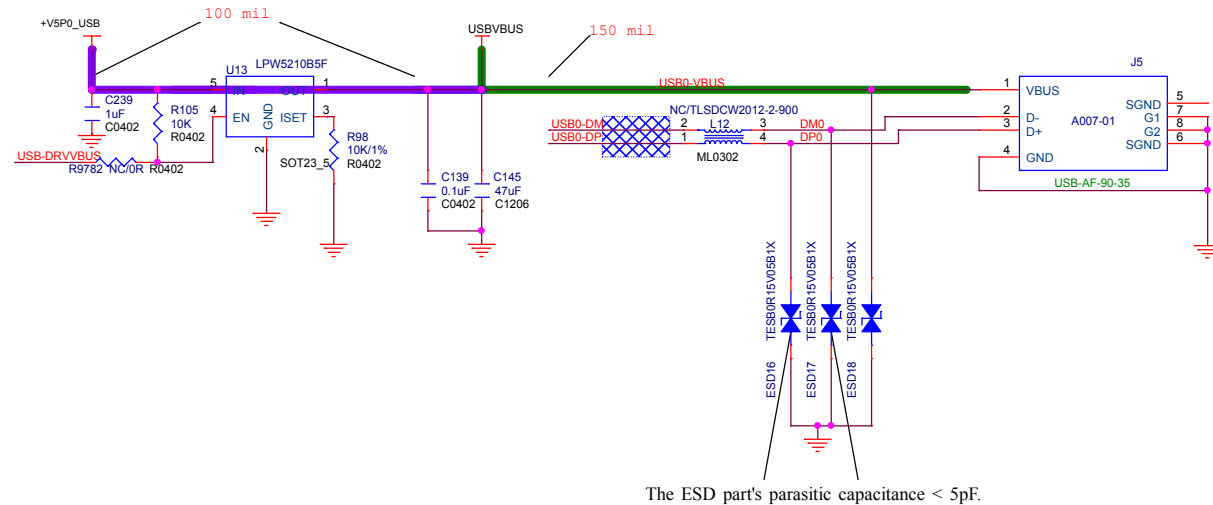


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T-CARD/USB



USB

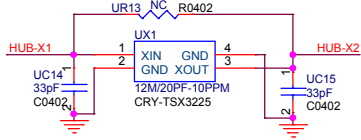
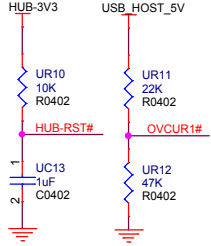
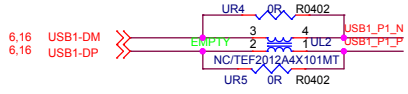


The ESD part's parasitic capacitance $< 5\text{pF}$.

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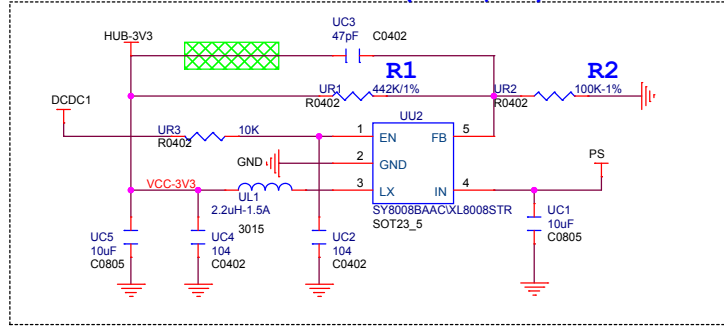
CTP

Differential pairs
Z0= 90 ohm +/-5 ohm



VCC-3V3

$$V_{out} = 0.6 * (1 + R1/R2) = 3.25V$$

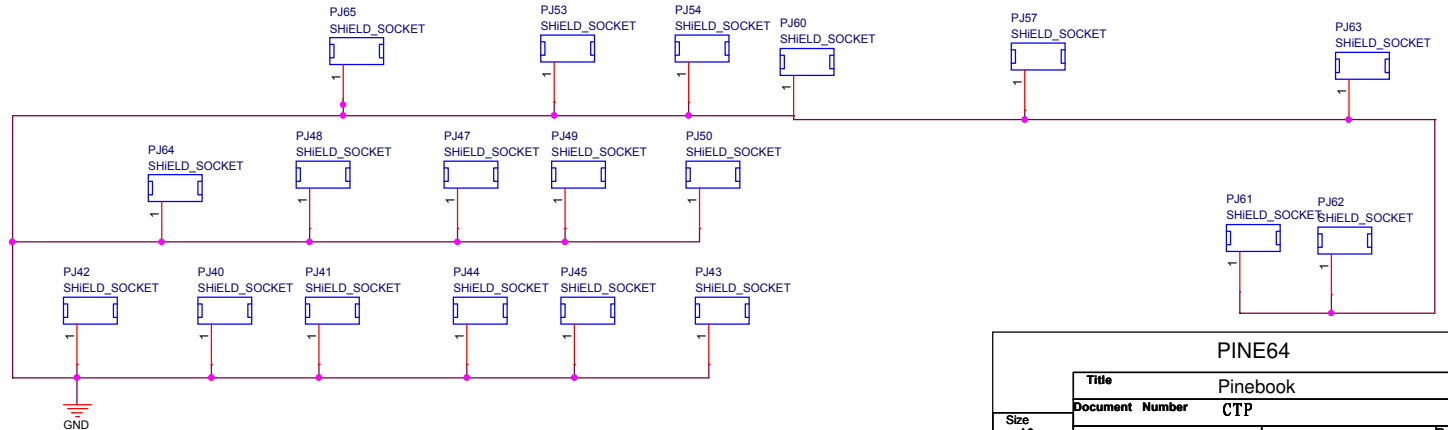
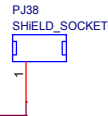
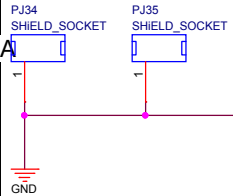
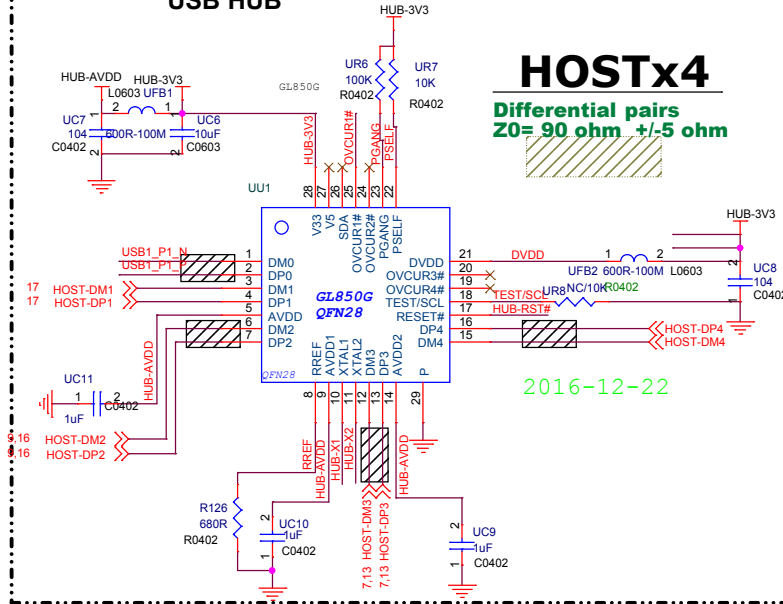


USB_HUB_USB_CONN

USB HUB

HOSTx4

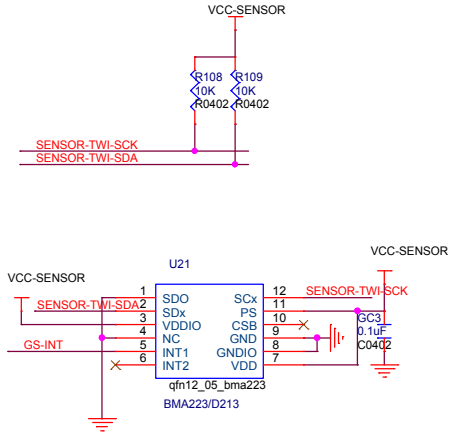
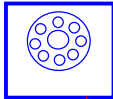
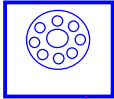
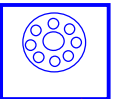
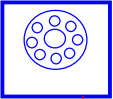
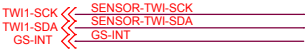
Differential pairs
Z0= 90 ohm +/-5 ohm



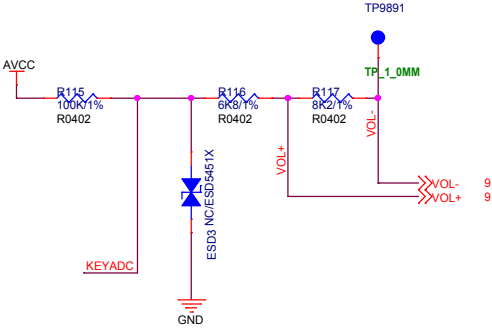
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SENSORS/MT/KEY

SENSOR



KEY

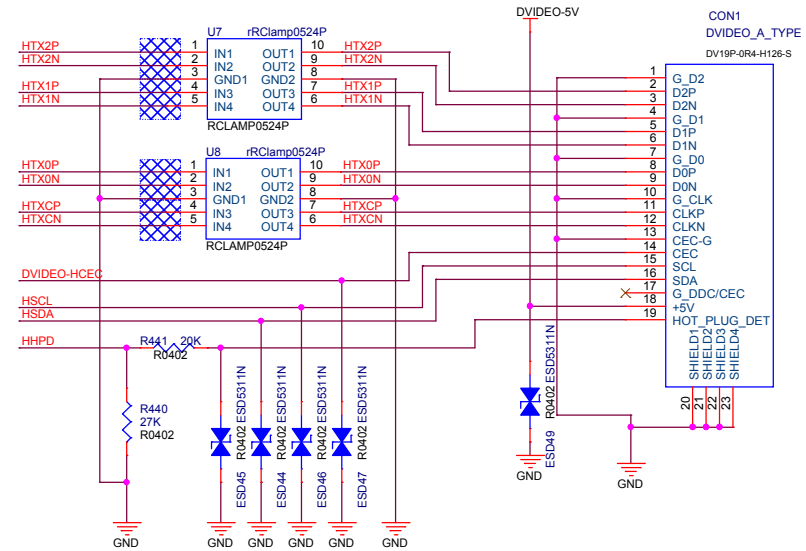
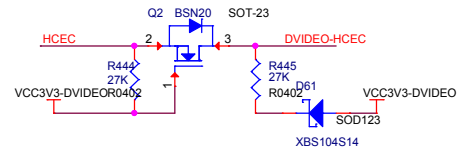
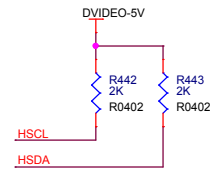


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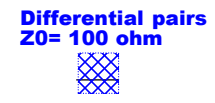
DIGITAL VIDEO



Differential pairs
Z0= 100 ohm

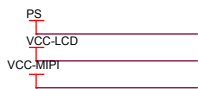
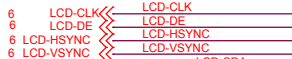
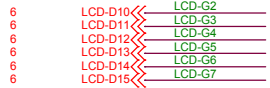
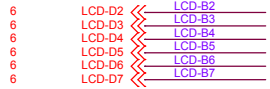
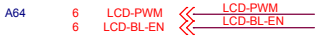
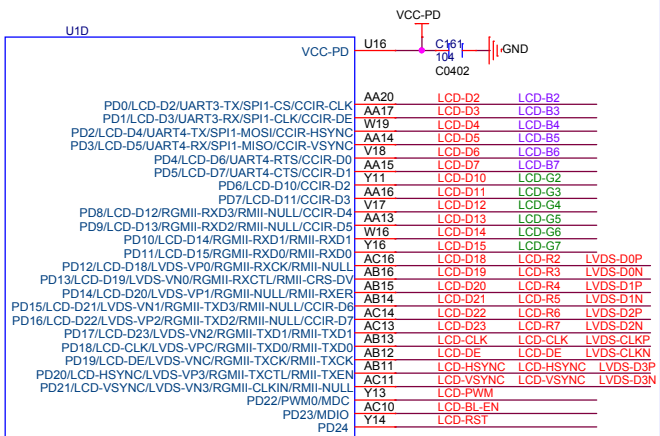


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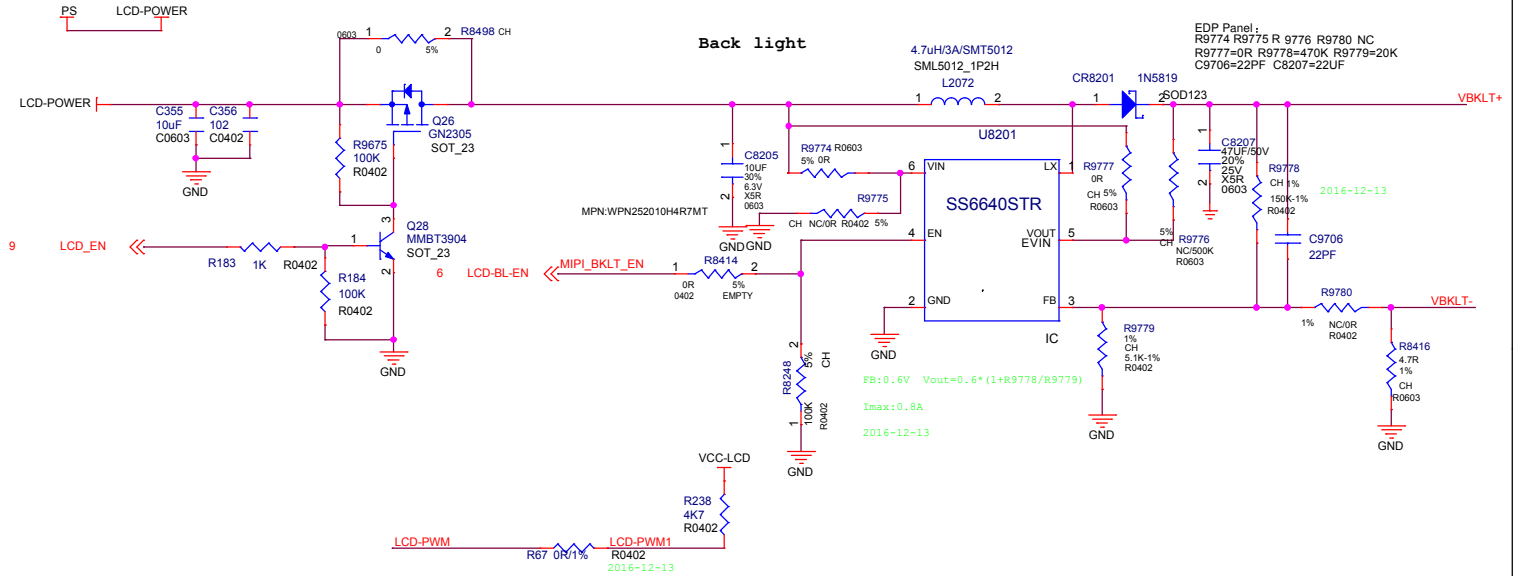
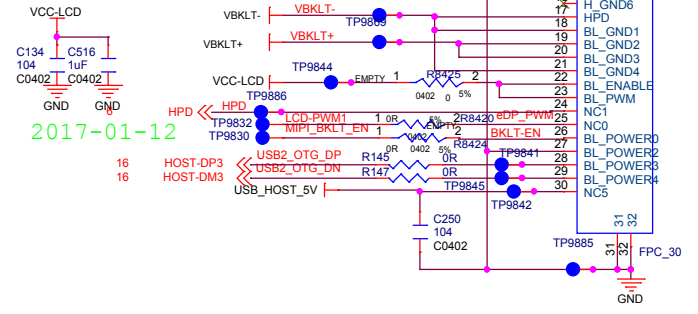


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EDP/MIPI



Differential pairs



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