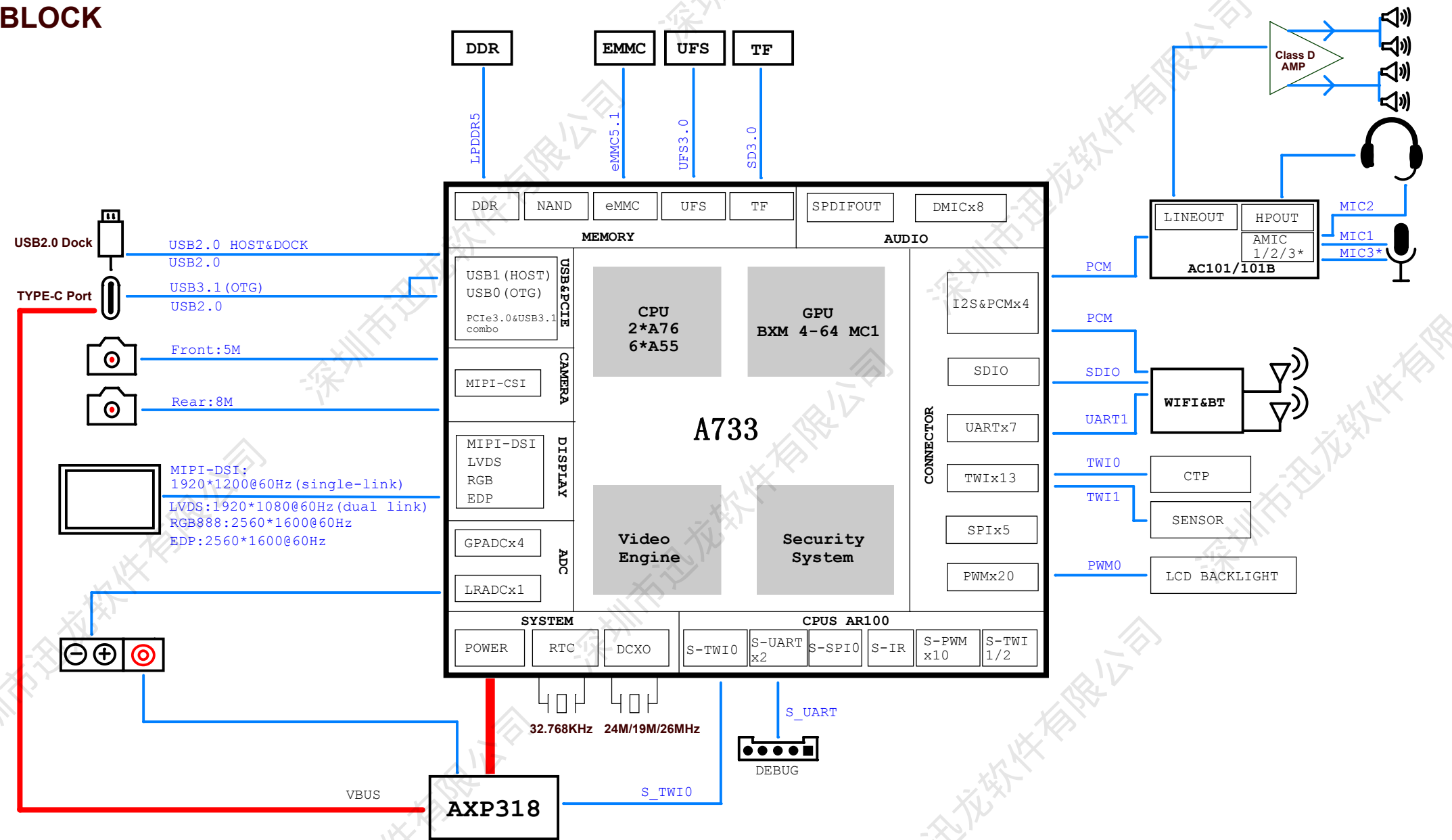
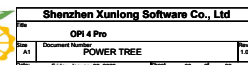
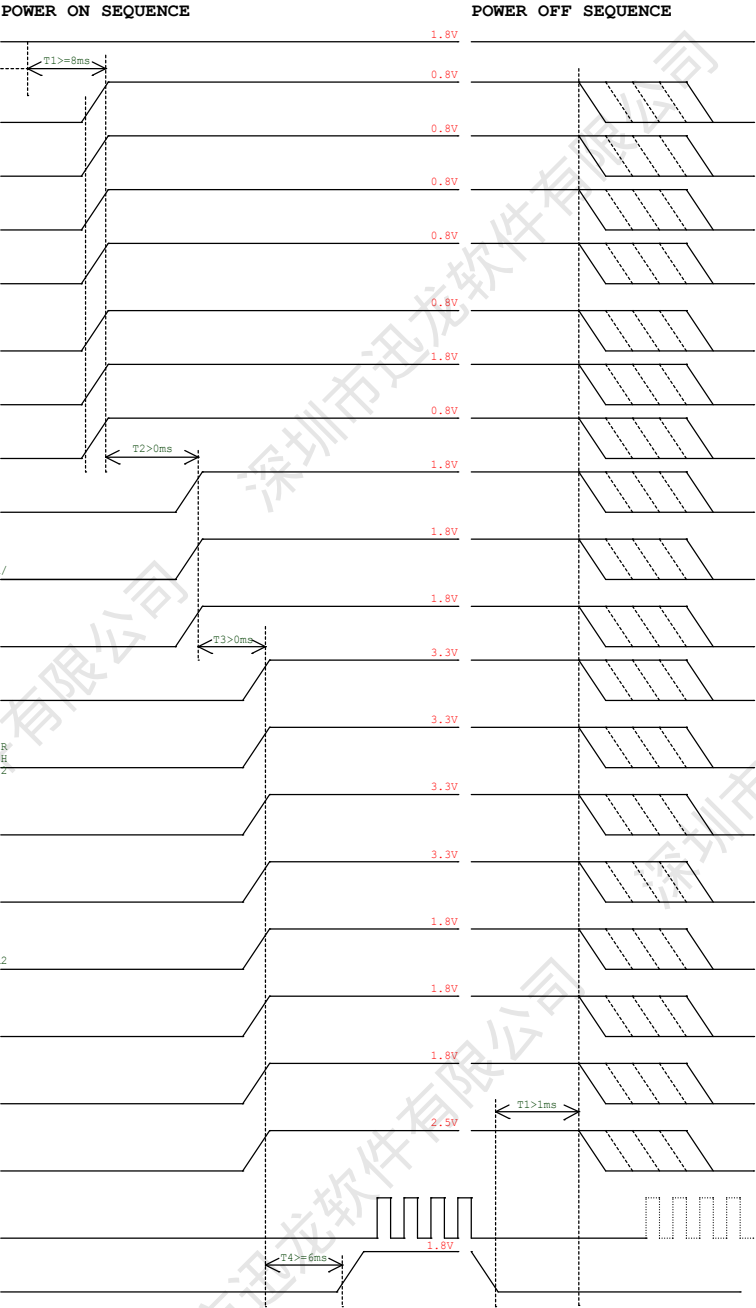


BLOCK

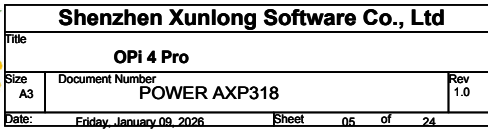




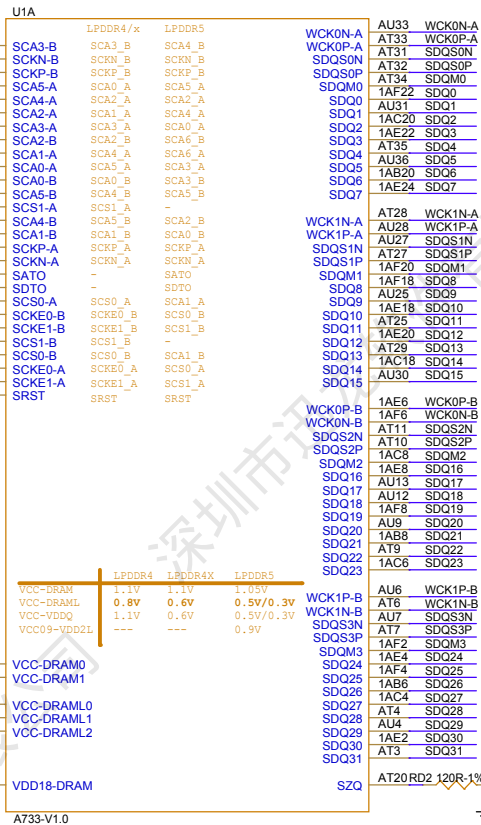
POWER SEQUENCE



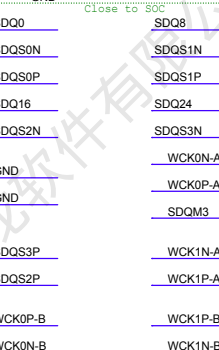
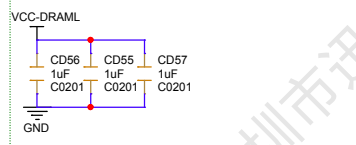
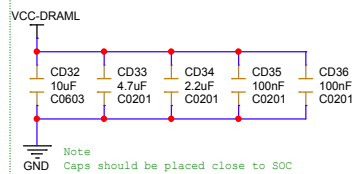
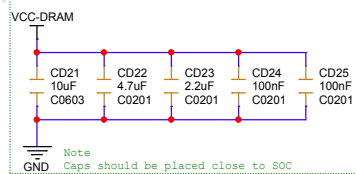
Shenzhen Xunlong Software Co., Ltd			
Title		A733	
Size		Document Number	
A3		POWER SEQUENCE	
Date:		Rev	
Friday, January 09, 2026		1.0	
Sheet		of	
04		23	



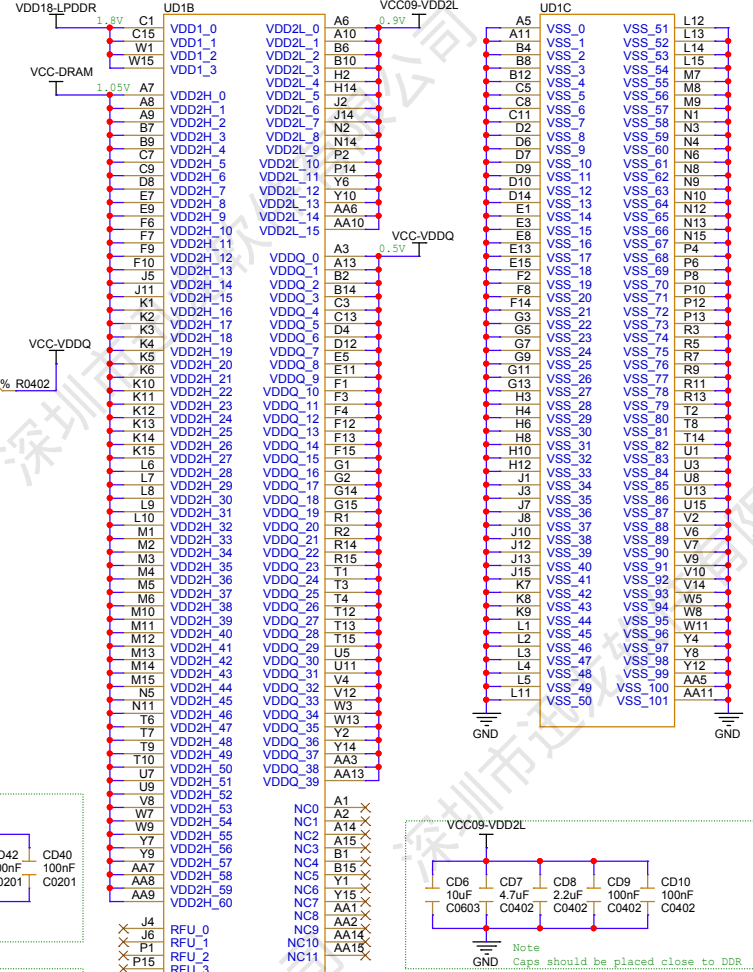
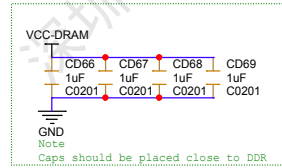
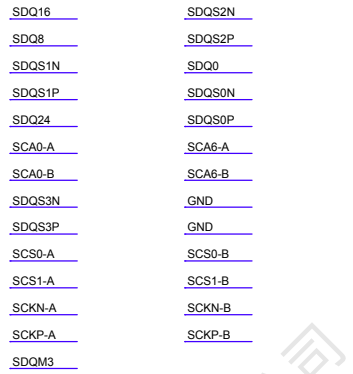
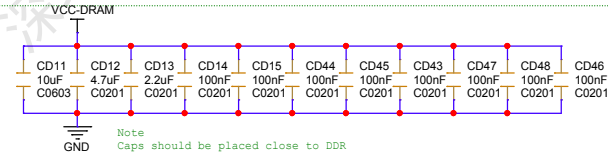
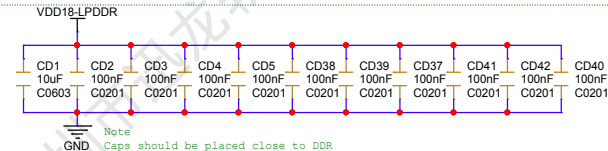
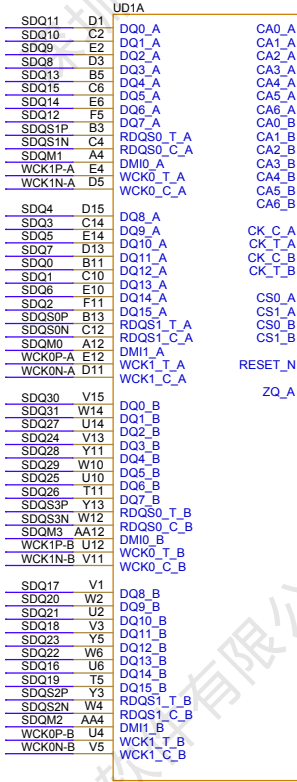
LPDDR5



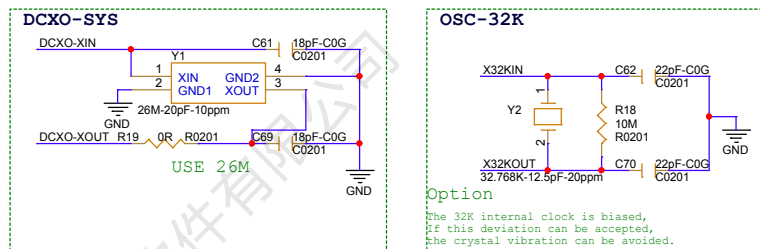
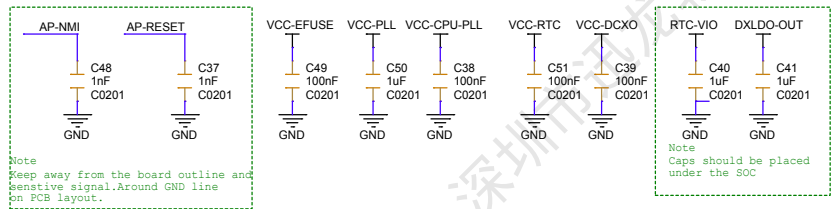
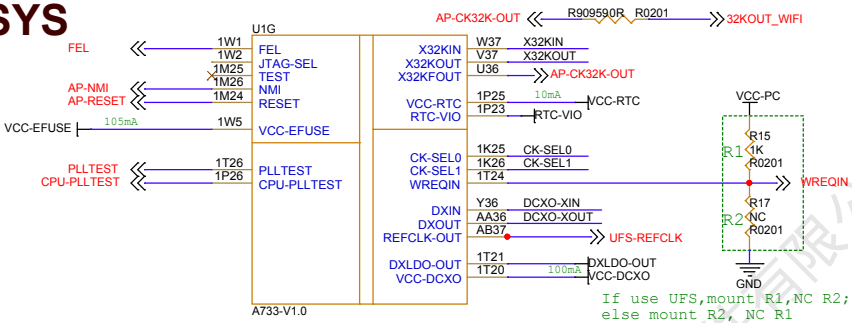
	LPDDR4	LPDDR4X	LPDDR5
VCC-DRAM	1.1V	1.1V	1.05V
VCC-DRAML	0.8V	0.6V	0.5V/0.3V
VCC-VDDQ	1.1V	0.6V	0.5V/0.3V
VCC09-VDD2L	---	---	0.9V



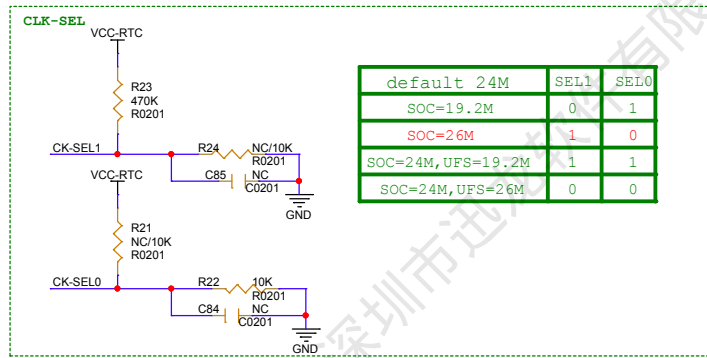
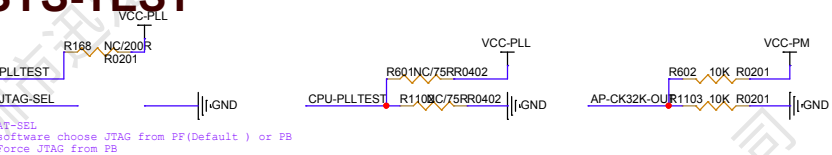
数据和地址信号选择同组走线最长信号，原理图只是举例



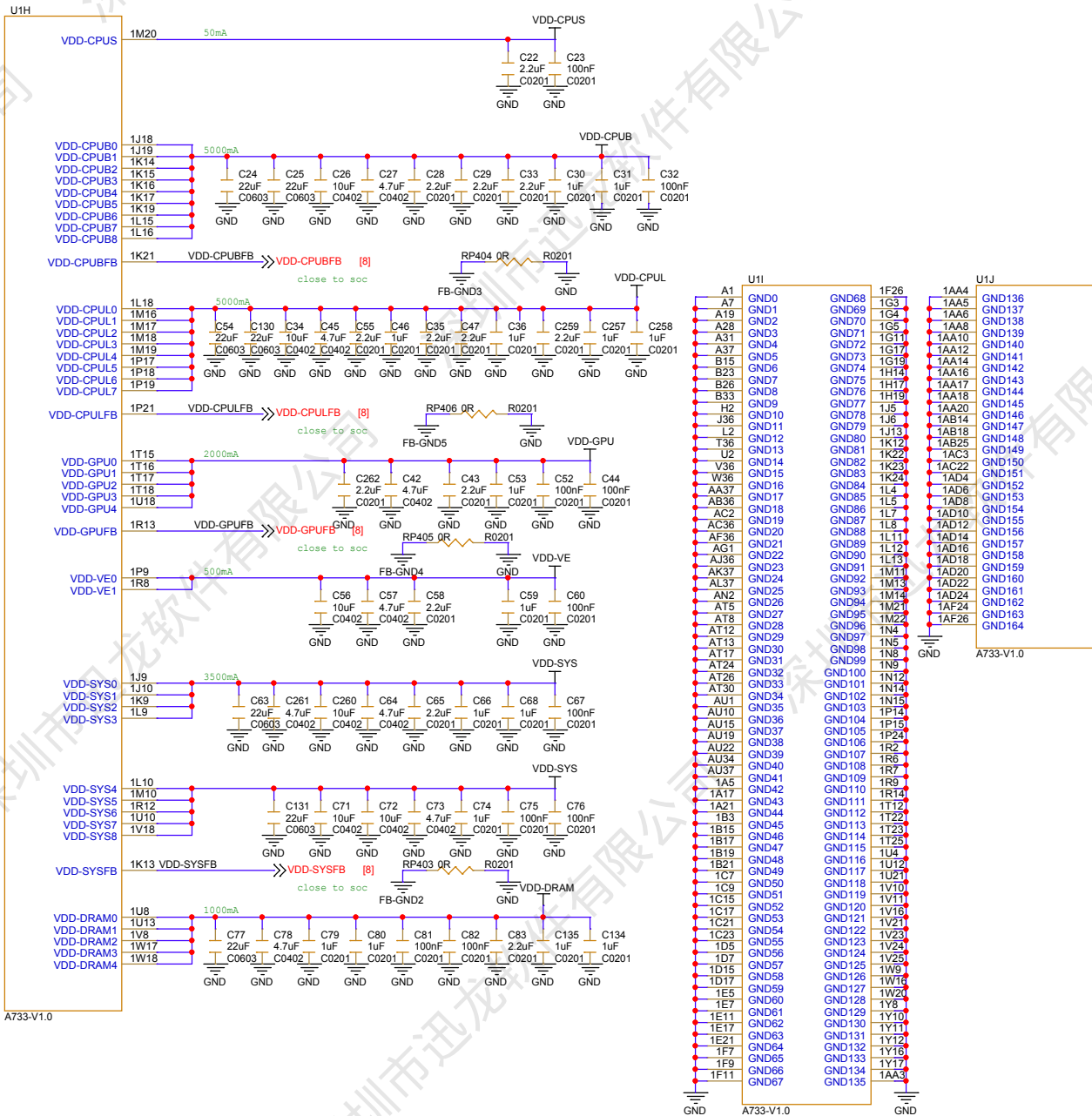
SYS



SYS-TEST



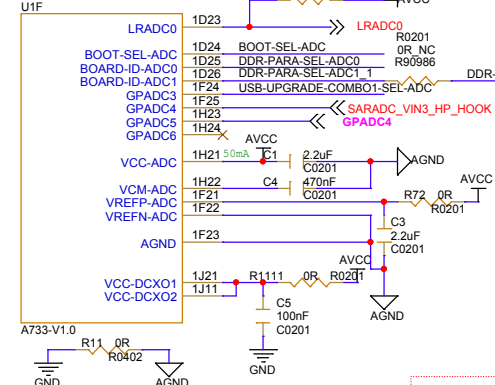
CORE POWER



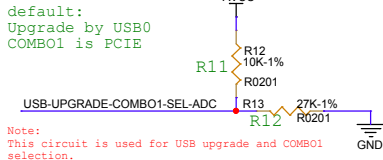
Shenzhen Xunlong Software Co., Ltd

Title			
OPI 4 Pro			
Size	Document Number	Rev	
A3	A733 SYS	1.0	
Date:	Friday, January 09, 2026	Sheet	07 of 23

ADC



Note:
This circuit is used for storage media boot selection.



R11	R12	Volt	Upgrade	COMBO1
10K-1%	1K-1%	163mV	USB2	PCIE
	2K7-1%	382mV	USB2	USB
	5K1-1%	608mV	USB2	floating
	27K-1%	1315mV	USB0	PCIE
	68K-1%	1569mV	USB0	USB
	NC	1800mV	USB0	floating

BOOT-SEL-ADC			Boot Media Type Select	
R1-1%	R2-1%	Volt		
NC	10K	0mV	SMHC0->MLC NAND->SLC NAND->TRY(except SPI4 in PI)	
10K	1K	163mV	SMHC0->SLC NAND->MLC NAND->TRY(except SPI4 in PI)	
10K	2K2	325mV	SMHC0->EMMC_USER->EMMC_BOOT->TRY(except SPI4 in PI)	
10K	3K9	505mV	SMHC0->EMMC_BOOT->EMMC_USER->TRY(except SPI4 in PI)	
6K8	4K7	736mV	SMHC0->SPI0 NOR->TRY(except SPI4 in PI)	
6K8	6K8	900mV	SMHC0->SPI0 NAND->TRY(except SPI4 in PI)	
4K3	6K8	1102mV	SMHC0->SPI4 NOR in PI->TRY(except SPI4 in PI)	
2K2	10K	1475mV	SMHC0->SPI4 NAND in PI->TRY(except SPI4 in PI)	
10K	NC	1800mV	SMHC0->UFS->TRY(except SPI4 in PI)	

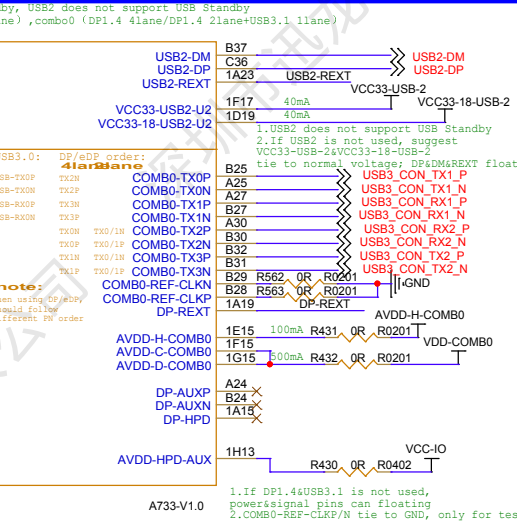
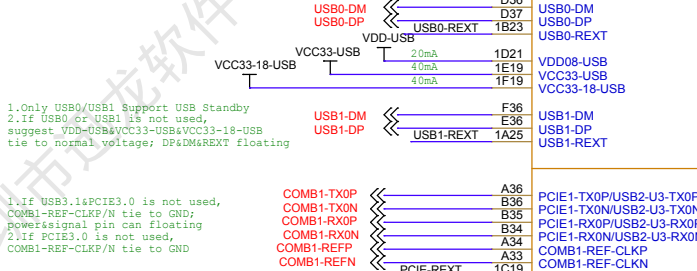
Note:
This circuit is used for DDR parameter configuration selection.

GPIO Level(Set by the pull-up and R4 pull-down resistance of PH16 GPIO)	GPADC Voltage(Fixed pull-up R5 is 10K-1%,Set the voltage by adjusting pull-down resistor R6)	DDR PARA
0	163mV (1K-1%)	DDR PARA 1
0	382mV (2.7K-1%)	DDR PARA 2
0	608mV (5.1K-1%)	DDR PARA 3
0	811mV (8.2K-1%)	DDR PARA 4
0	1050mV (14K-1%)	DDR PARA 5
0	1315mV (27K-1%)	DDR PARA 6
0	1569mV (68K-1%)	DDR PARA 7
0	1800mV (NC)	DDR PARA 8
1	163mV (1K-1%)	DDR PARA 9
1	382mV (2.7K-1%)	DDR PARA 10
1	608mV (5.1K-1%)	DDR PARA 11
1	811mV (8.2K-1%)	DDR PARA 12
1	1050mV (14K-1%)	DDR PARA 13
1	1315mV (27K-1%)	DDR PARA 14
1	1569mV (68K-1%)	DDR PARA 15
1	1800mV (NC)	DDR PARA 16

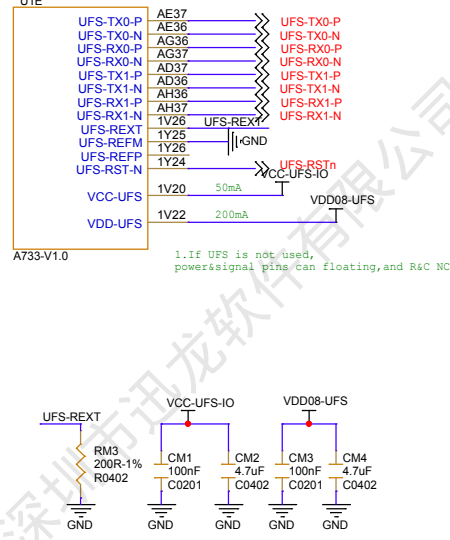
Note:
This circuit is used for board template selection.

BOARD-ID-SEL-ADC				BoardID Config
	R9	R10	Volt	
10K-1%	10K-1%	1K-1%	163mV	Config-1
		2K7-1%	382mV	Config-2
		5K1-1%	608mV	Config-3
		8K2-1%	811mV	Config-4
		14K-1%	1050mV	Config-5
		27K-1%	1315mV	Config-6
		68K-1%	1569mV	Config-7
		NC	1800mV	Config-8

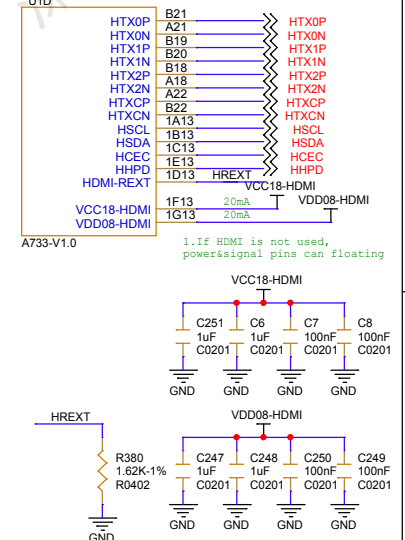
USB&EDP&PCIE

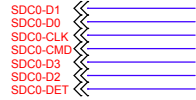


UFS

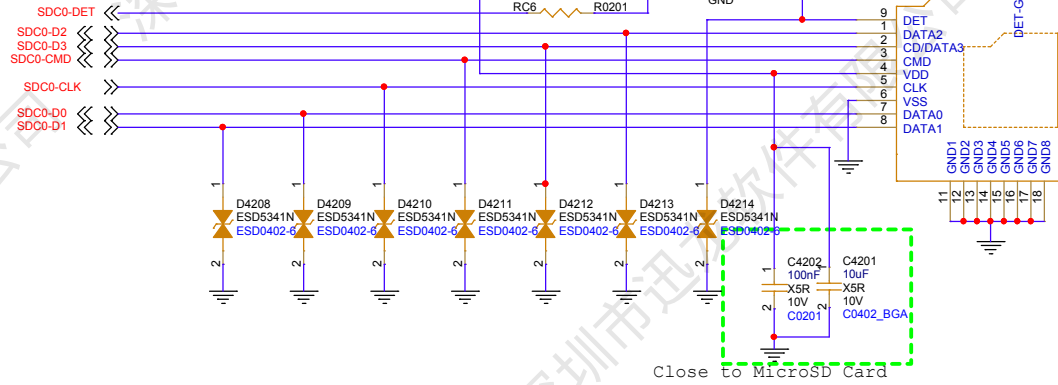


HDMI





没插卡之前PIN9、PIN10短路
插卡之后9PIN与10PIN开路。

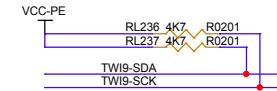
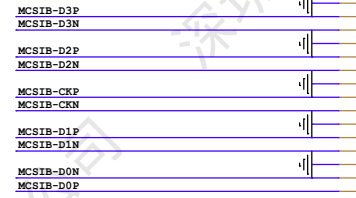
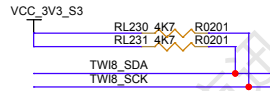
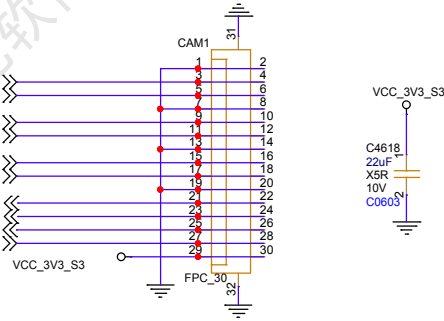
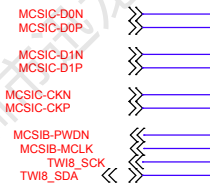
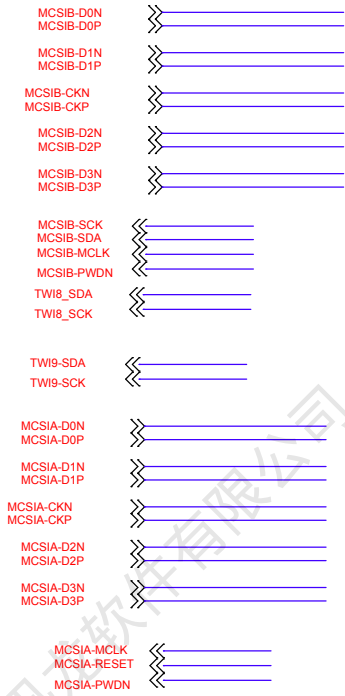


Close to MicroSD Card



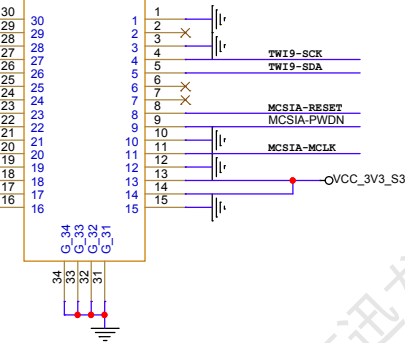
Shenzhen Xunlong Software Co., Ltd			
Title			
OPI 4 Pro			
Size	Document Number	Rev	
A3	TF Card	1.0	
Date:	Friday, January 09, 2026	Sheet	10 of 23

VI-Camera DPHY_RX



MIPI D-PHY1
CAM2

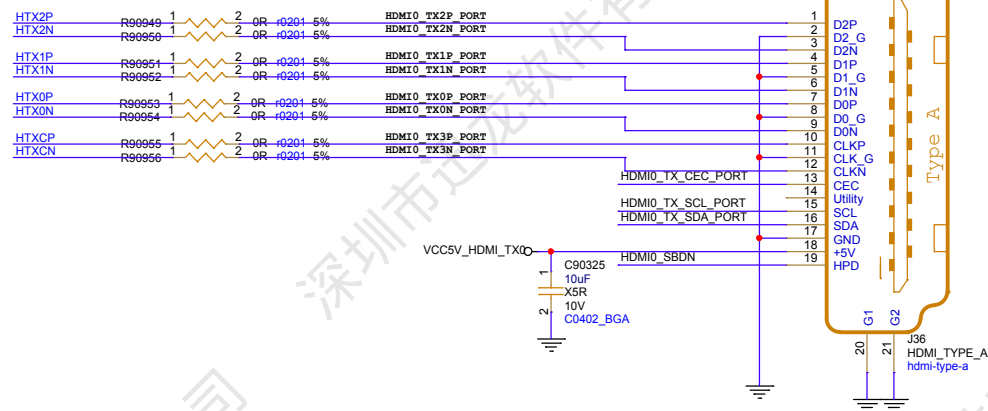
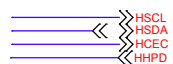
OK-10F030-04
CON SMD OK-10F030-04

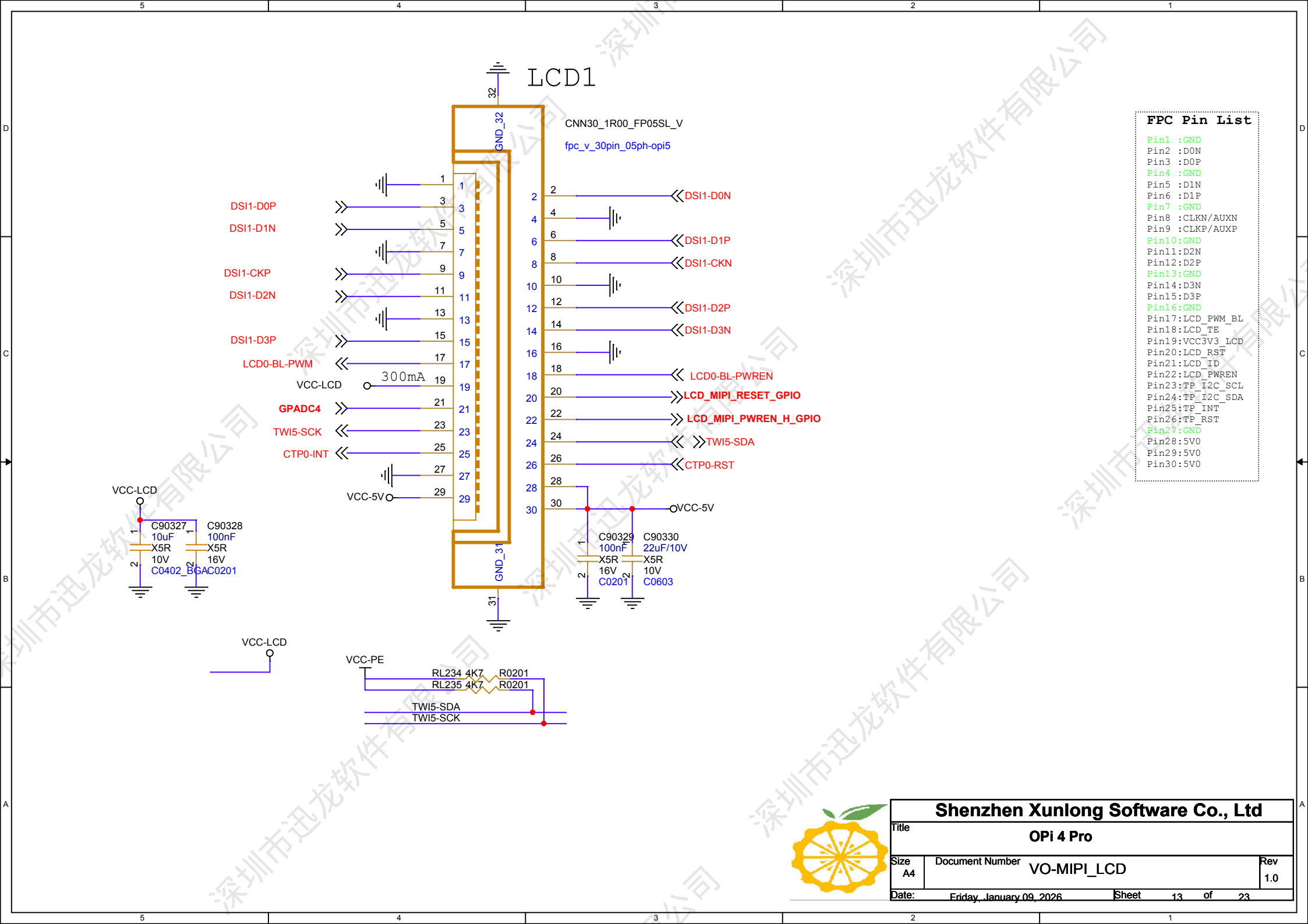


Shenzhen Xunlong Software Co., Ltd			
Title		OPI 4 Pro	
Size	Document Number	VI-Cam MIPI CSIA-RX	
A3			Rev 1.0
Date:	Friday, January 09, 2026	Sheet 11	of 23

Diagram showing the pin headers for the HTX module:

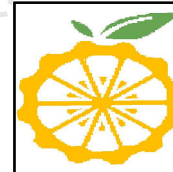
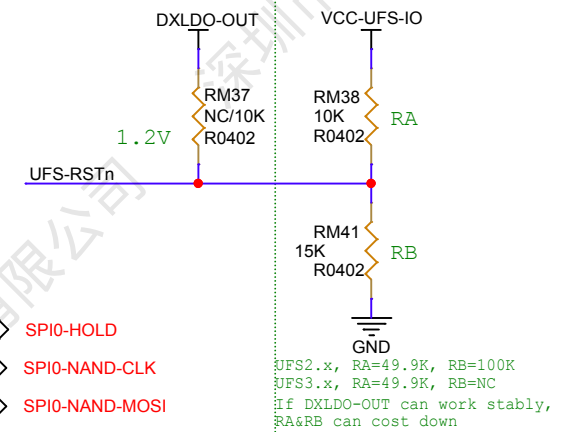
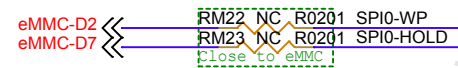
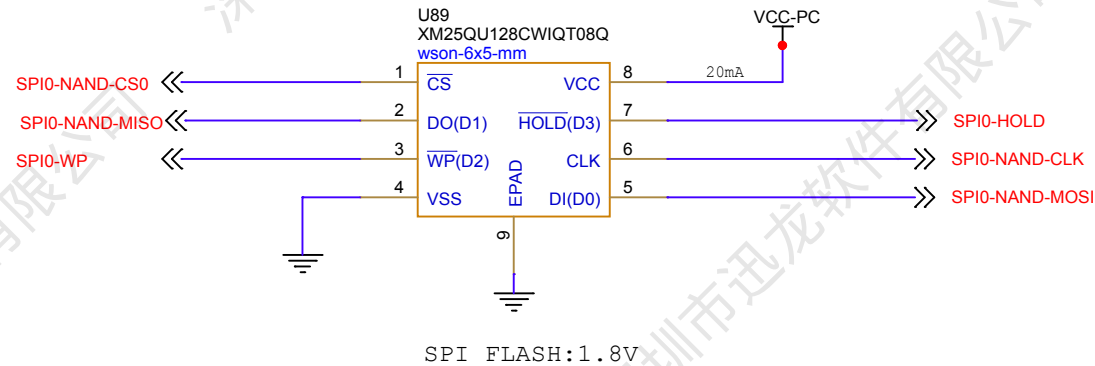
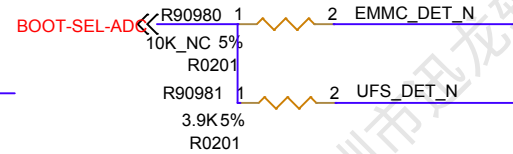
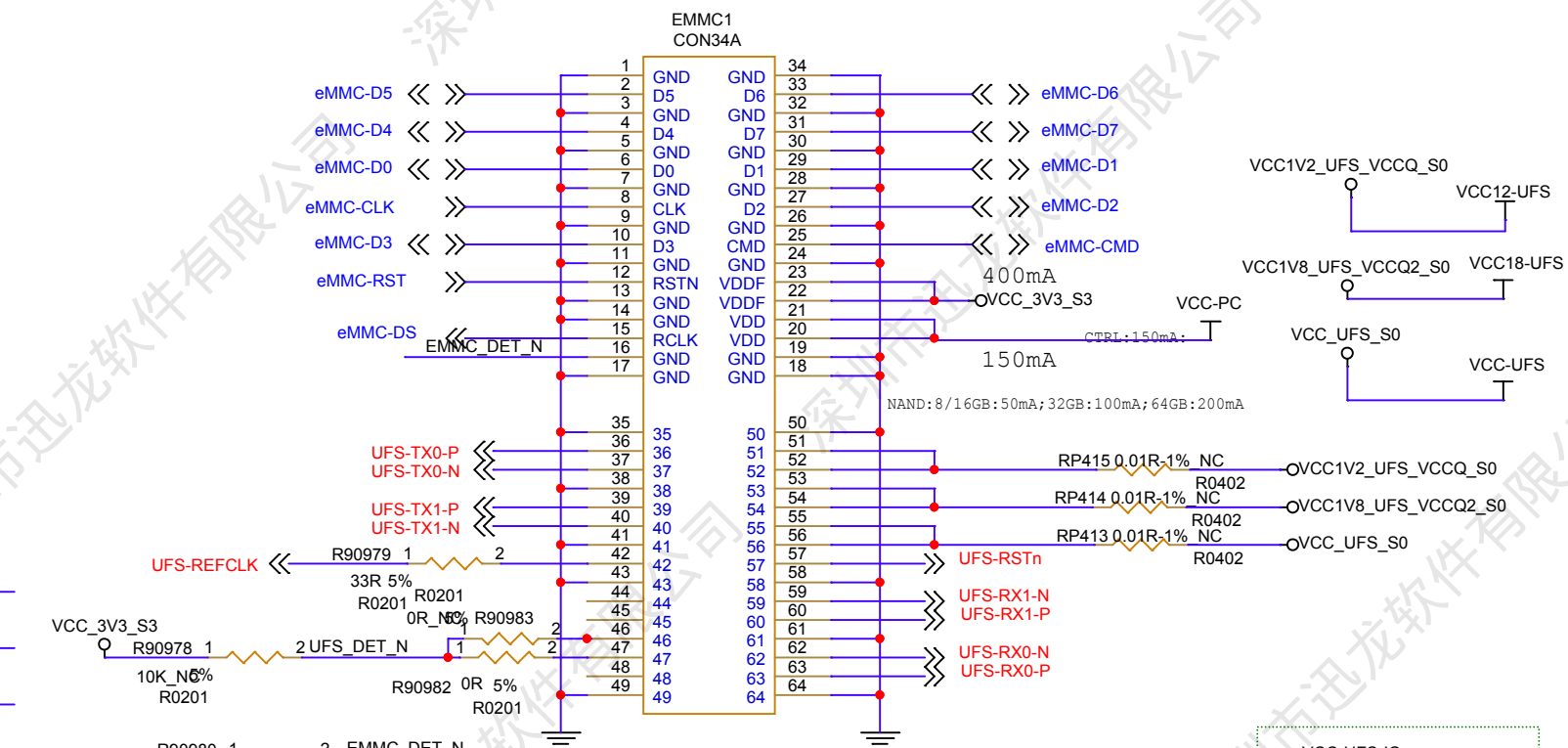
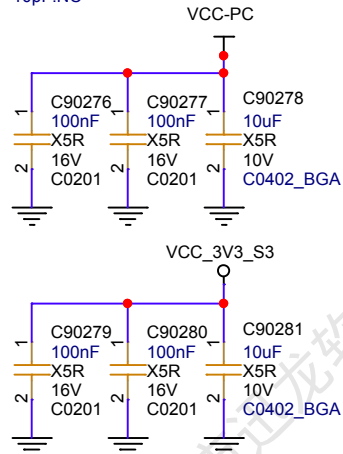
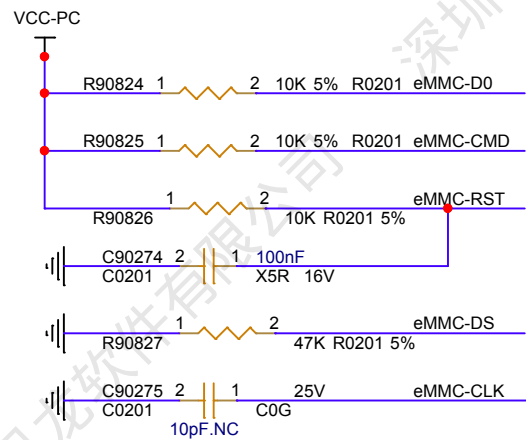
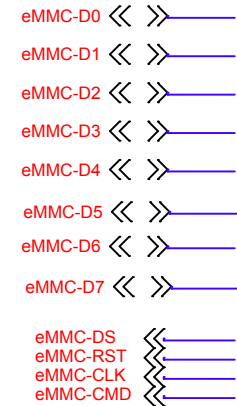
- HTX0P
- HTX0N
- HTX1P
- HTX1N
- HTX2P
- HTX2N
- HTXCP
- HTXCN





Shenzhen Xunlong Software Co., Ltd		
Title OPI 4 Pro		
Size A4	Document Number VO-MIPI_LCD	Rev 1.0
Date: Friday, January 09, 2026	Sheet 13	of 23

eMMC FLASH



Xunlong Co., Limited

Design Name

OPI 4 Pro

Size
A4

Page Name

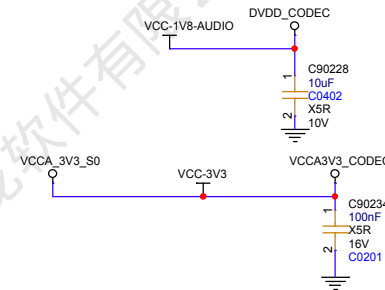
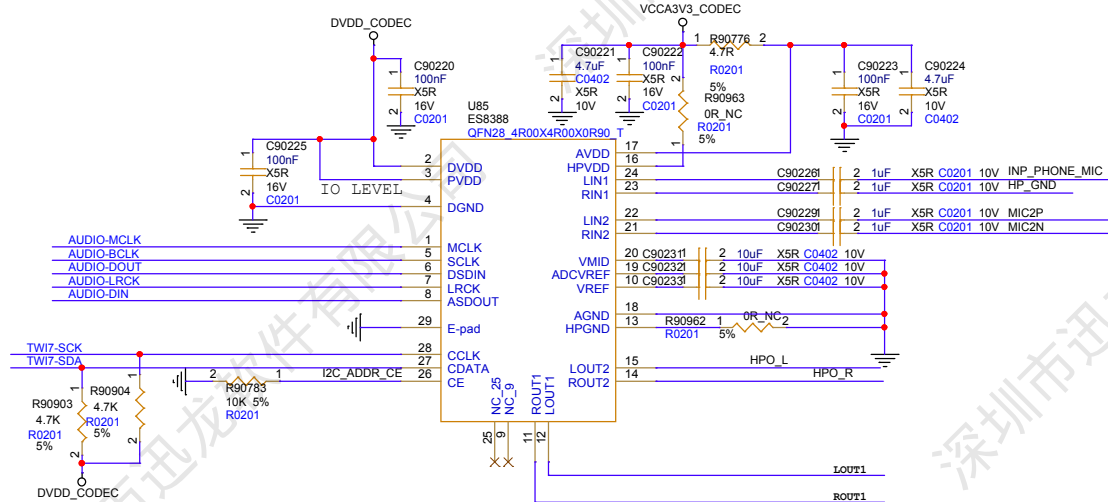
Flash-eMMC Flash

Rev
1.0

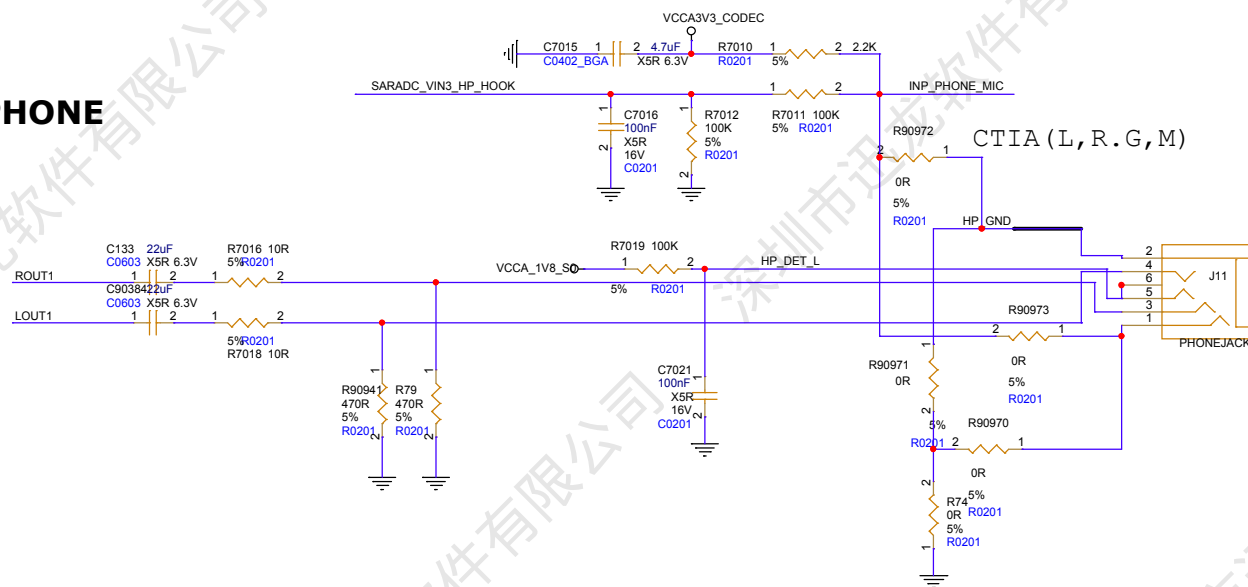
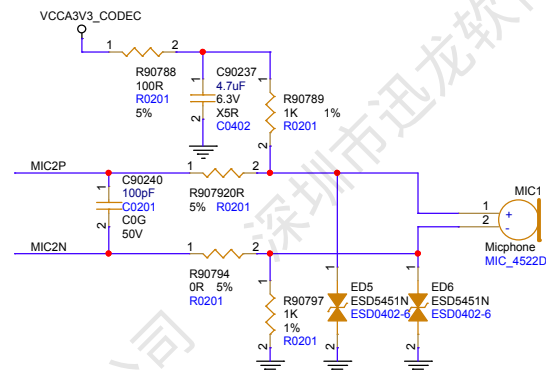
Date: Friday, January 09, 2026

Sheet 14 of 23

EARPHONE



Analog MIC

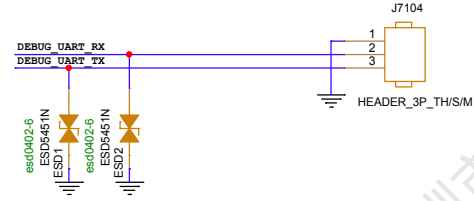


Sheet 16 of 23

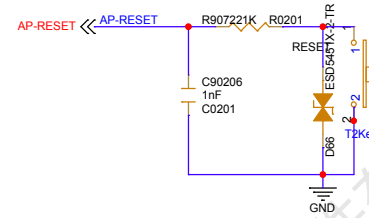
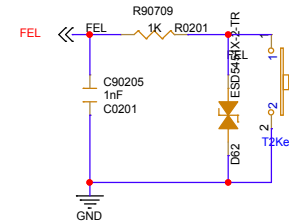
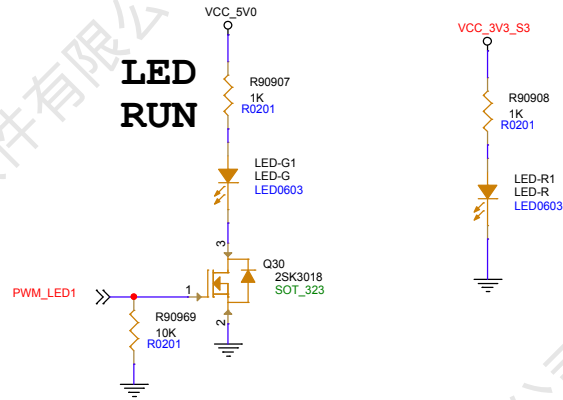
CPUX DEBUG

UART-0-CPUX-TX
UART-0-CPUX-RX

UART-0-CPUX-TX R906971K R0201 DEBUG_UART_TX
UART-0-CPUX-RX R907081K R0201 DEBUG_UART_RX

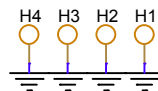
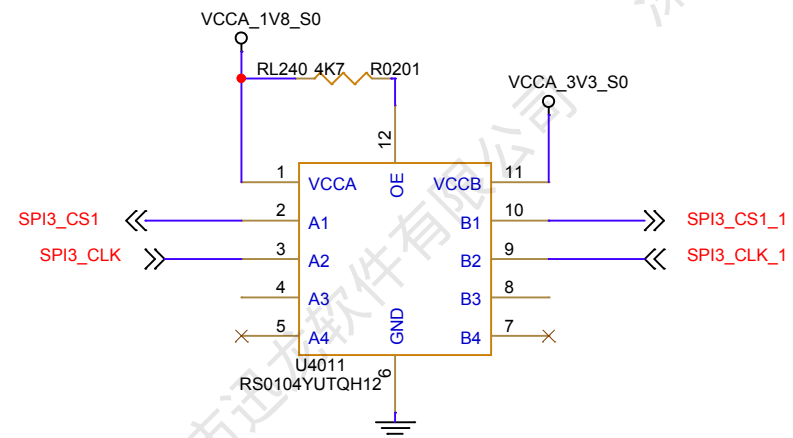
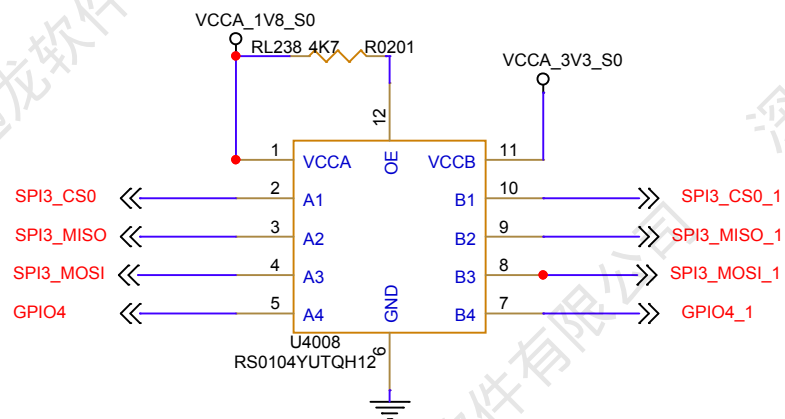
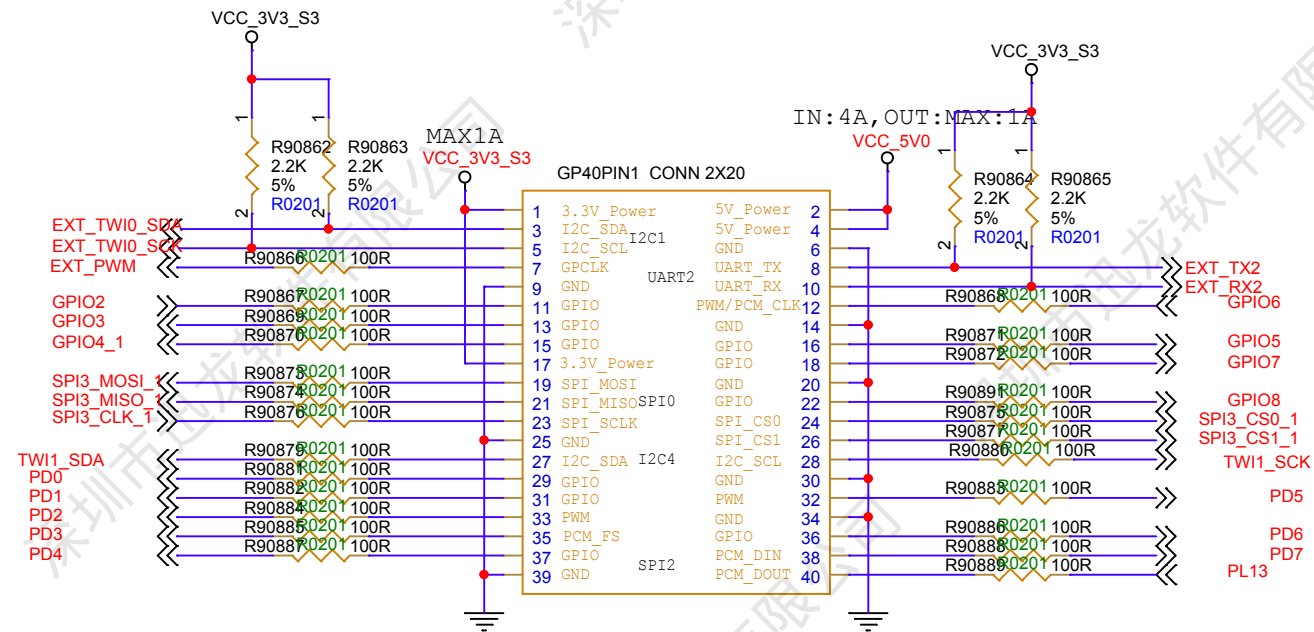


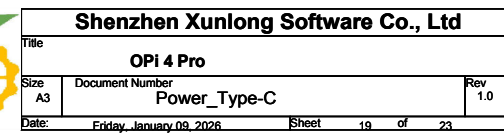
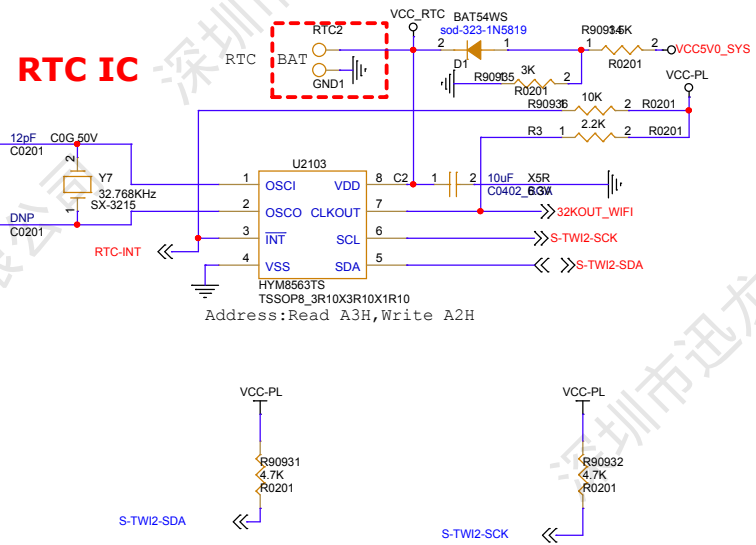
LED
RUN

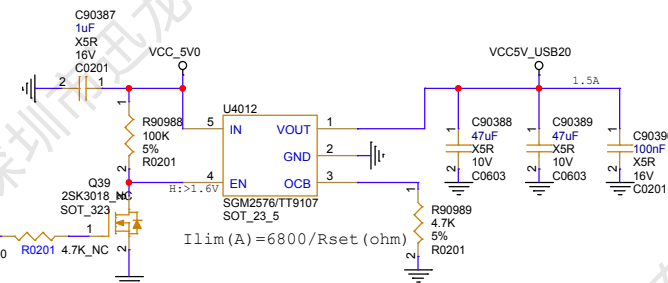
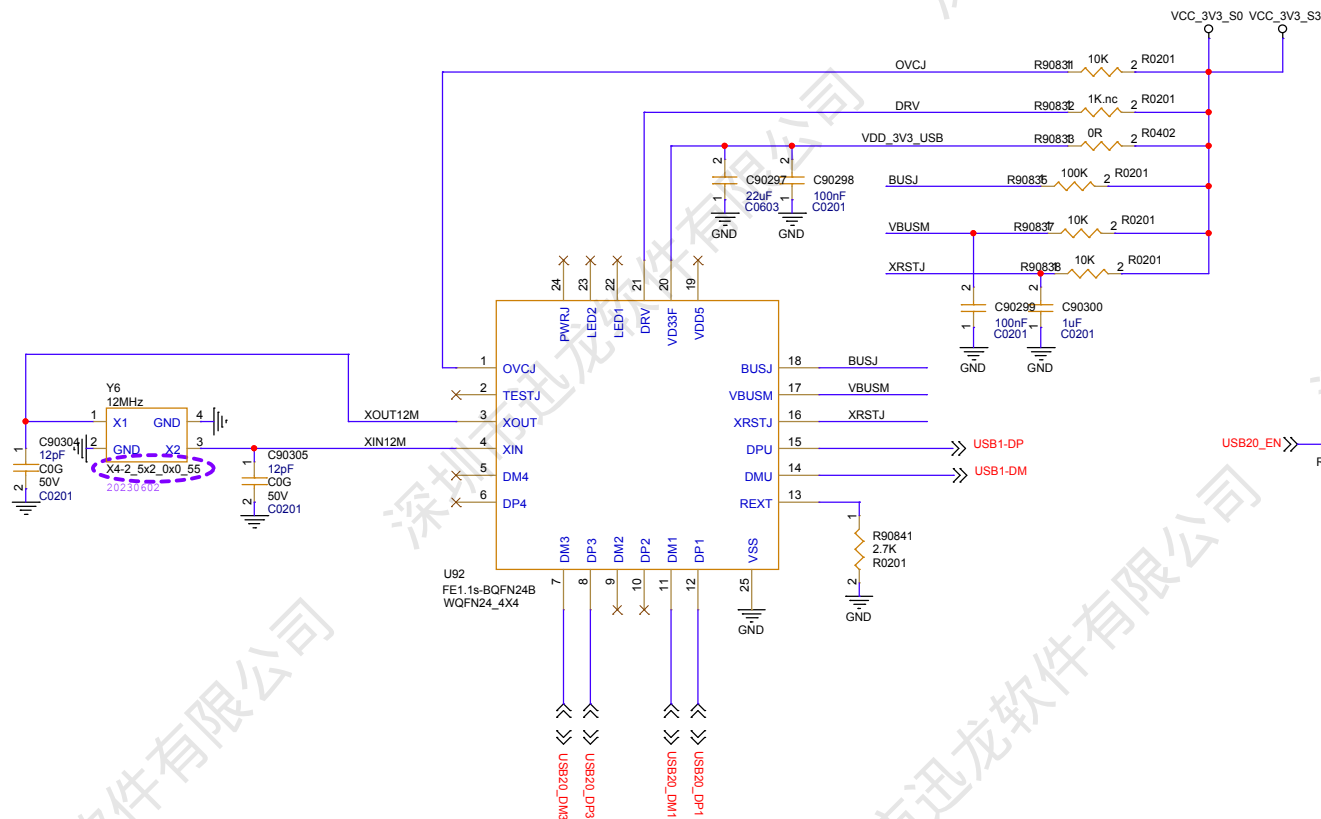


Shenzhen Xunlong Software Co., Ltd			
Title		OPI 4 Pro	
Size	Document Number	Rev	
A3	DEBUG/TEST	1.0	
Date:	Friday, January 09, 2026	Sheet	17 of 23

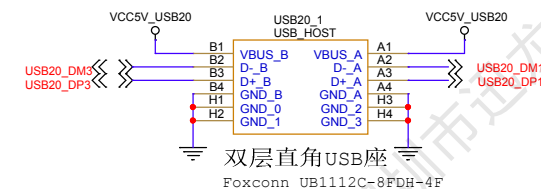
EXT I/O



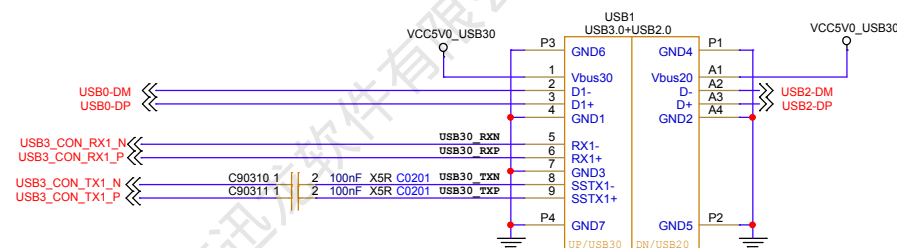
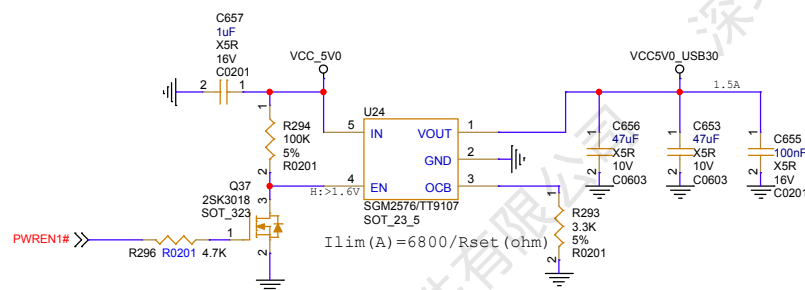




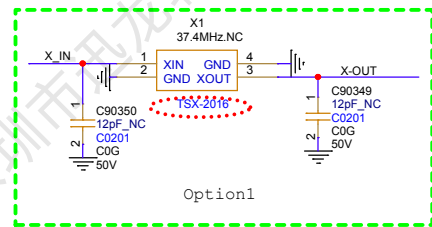
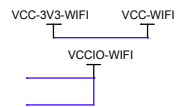
USB2.0*2



双层直角USB座
Foxconn UB1112C-8FDH-4F



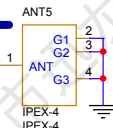
Shenzhen Xunlong Software Co., Ltd			
Title			
OPI 4 Pro			
Size	Document Number	Rev	
A3	USB20/USB30 HOST Port	1.0	
Date:	Friday, January 09, 2026	Sheet	21 of 23



Note: VBAT peak-current is at least 600mA.

AP-WAKE-BT
BT-WAKE-AP
VCC-WIFI

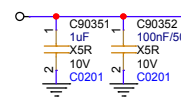
RF Microstrip
Z0= 50 ohm



WL-REG-ON
WL-WAKE-AP
WL-SDIO-D2
WL-SDIO-D3
WL-SDIO-CMD
WL-SDIO-CLK
WL-SDIO-D0
WL-SDIO-D1

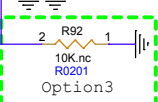
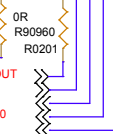
WL_REG_ON
WL_HOST_WAKE
SDIO_DATA_2
SDIO_DATA_3
SDIO_DATA_CMD
SDIO_DATA_CLK
SDIO_DATA_0
SDIO_DATA_1
GND2
VIN_LDO_OUT
VDDIO

VCCIO-WIFI



Option2

AP-CK32K-OUT
I2S1_DIN0
I2S1_BCLK
I2S1-DOUT0
I2S1_LRCK



Option3

N_VDDSWP_IN
N_VDDSWP_OUT
N_VDDSWPIO

UART_CTS_N
UART_RXD
UART_TXD
UART_RTS_N
TX1
TX2
N_REG_PU
N_I2C_SCL
GND5
N_I2C_SDA
BT_RST_N

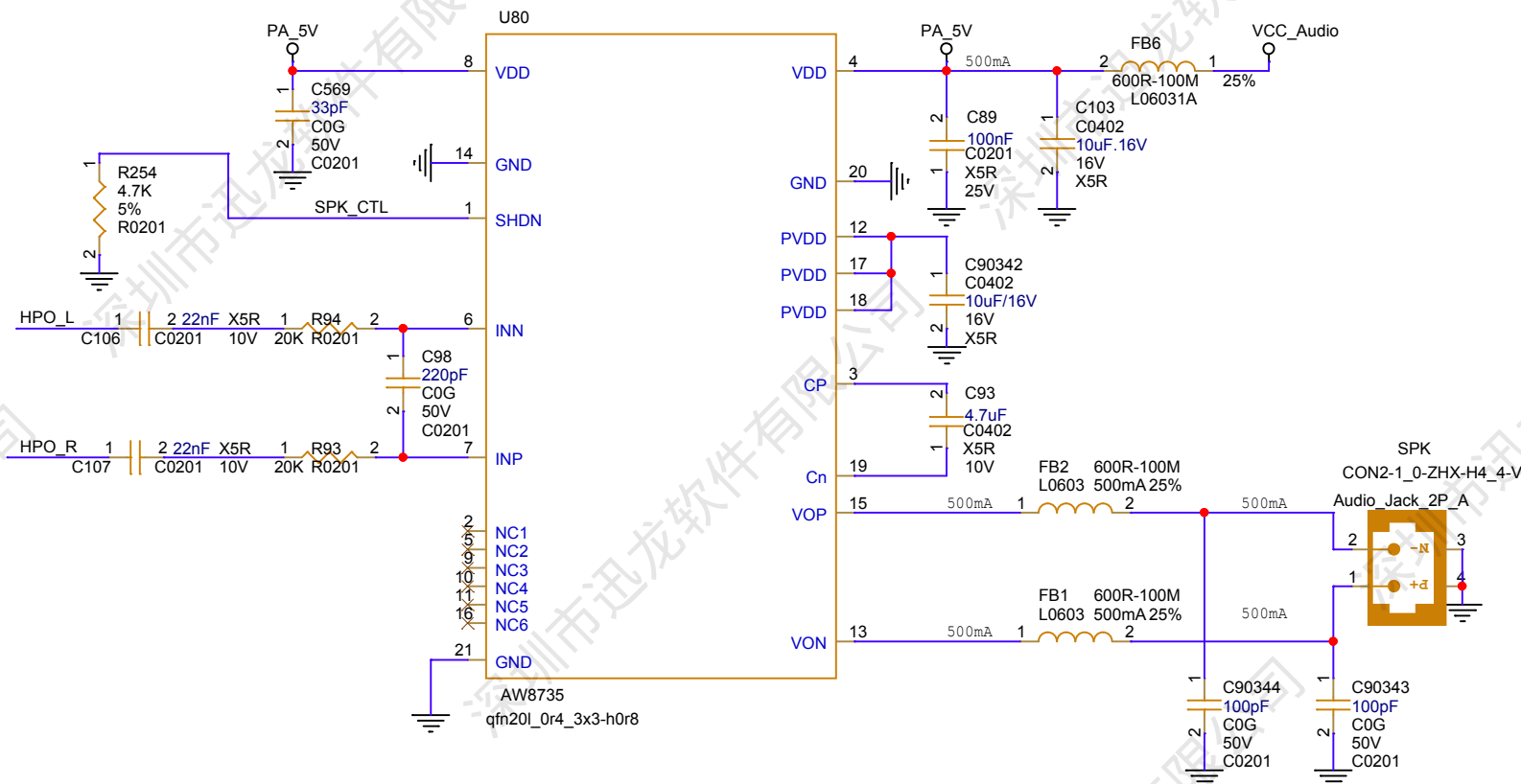
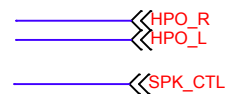


Option4



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Title			
OPI 4 Pro			
Size	Document Number		Rev
A3	WIFI/BT-SDIO		1.0
Date:	Friday, January 09, 2026	Sheet	22 of 23

SPK PA



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Title			
OPI 4 Pro			
Size	Document Number		Rev
A4	SPK_PA		1.0
Date:	Friday, January 09, 2026		Sheet 23 of 23