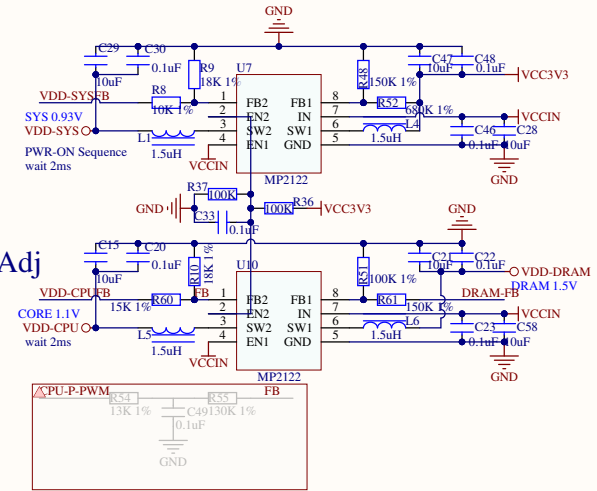
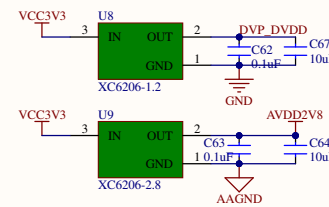
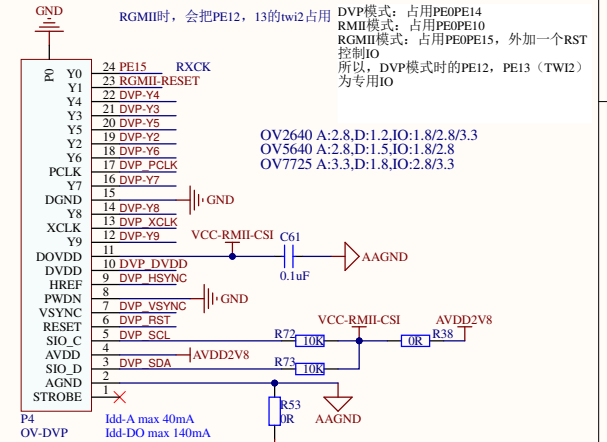
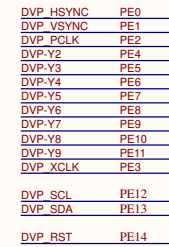




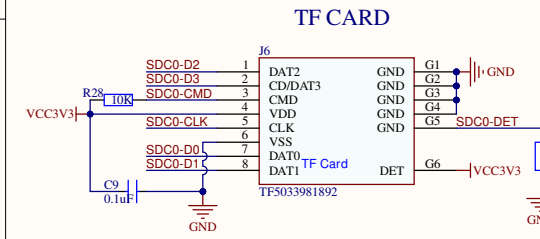
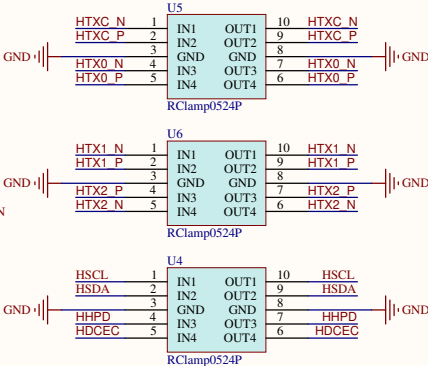
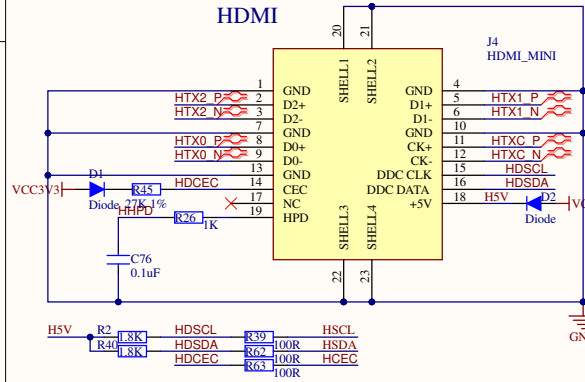
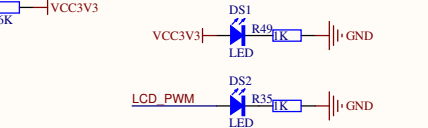
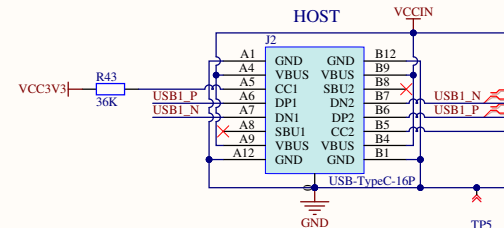
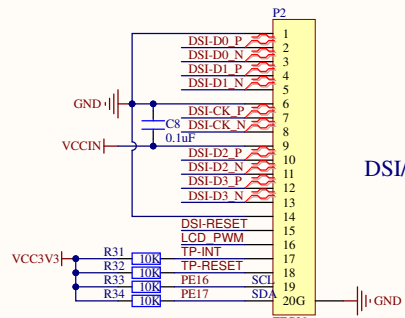
DRAM



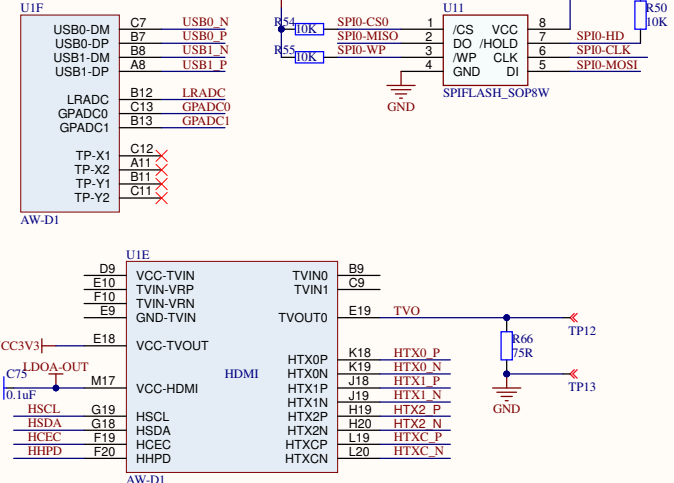
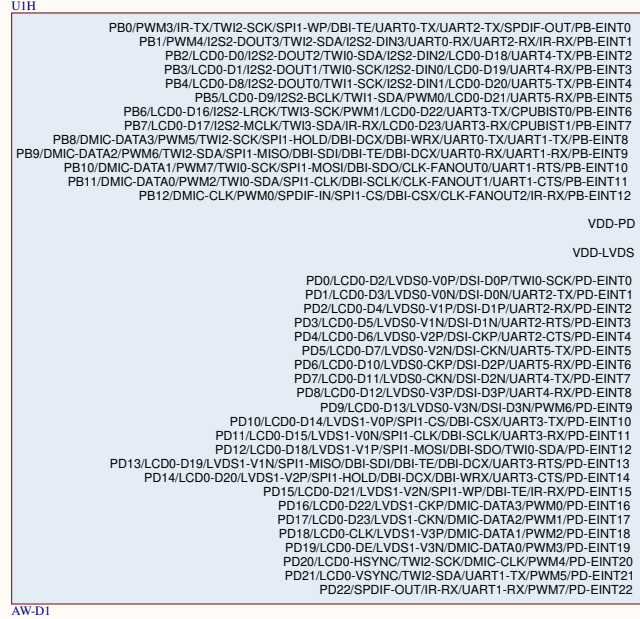
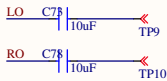
DVP(with TWI2)/RMII/RGMII



DSI/LVDS + CTP(twi3)



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