A20-OLinuXino-Lime_Rev_J

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Sheet: 1/1

1 x [4Gb DDR3 SDRAM (256Mx16)]

U1 — K4B4G1646E-BYMA0CU

We have used a number of fully compatible but different DDR3 memories due to supply unavailability. In such cases the memory part name in the schematic might remain outdated. It is recommended to always refer to the exact memory name printed on the component itself.

LED

PH2/LED — R1 2k/1% — LED1 LED/GREEN/0603

Button

UBOOT_SEL — RECOVERY TSW34-W16PR-220-LF/SIDE

A20(T2)/A10

U7

Ethernet

U2 — LAN8710A-EZC-ABC

The PHY is setted up as follows:
MODE: All capable(10/100Base).
Auto-negotiation enabled.
MII Configuration
SMI address: 0x01

MAC Address EEPROM

16 Kbit (2048x8) — AT24C16C-SSHM-T(SOIC-8) — I2C Address: 0xA0

LAN — RJ45 SIDE — RJLBC-060TC11(LPE5013EDNL)

U4 — ESDS314DBVR

MicroSD

SD/MMC1 — TFC-WPAPR-08

HDMI

U5 — NAXRCLAMP0524P

LCD EXTENSION

LCD_CON — MALE-PAV16X-2x20-HIGH

GPIO EXTENSION

GPIO-2 — MALE-PAV16X-2x20-HIGH

USB-OTG

U9 SY6280AAC — USB_OTG — AT24C16C-SSHM-T(SOIC-8)

USB-HOSTs

U10 SY6280AAC — USB_HOST1

U11 SY6280AAC — USB_HOST2

SATA

SATA

POWER SUPPLY CIRCUIT

U12 — AXP209(QFN48)

128M-BIT SERIAL FLASH MEMORY Present in -sXMB

U22 — NAKU256128JUSIG

EMMC, SPI FLASH, NAND

FLASH_CONNECTOR — DF12(3.0)-40DS-0.5V(86)