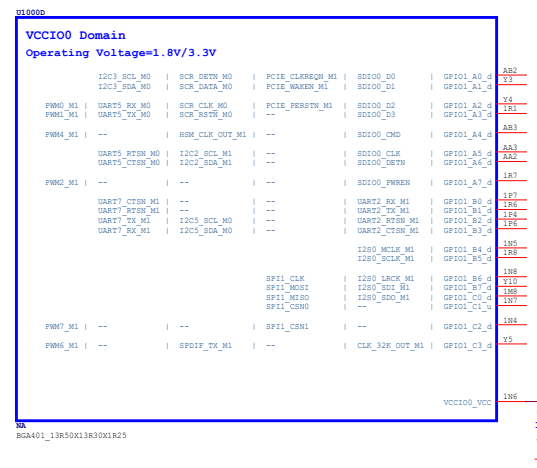
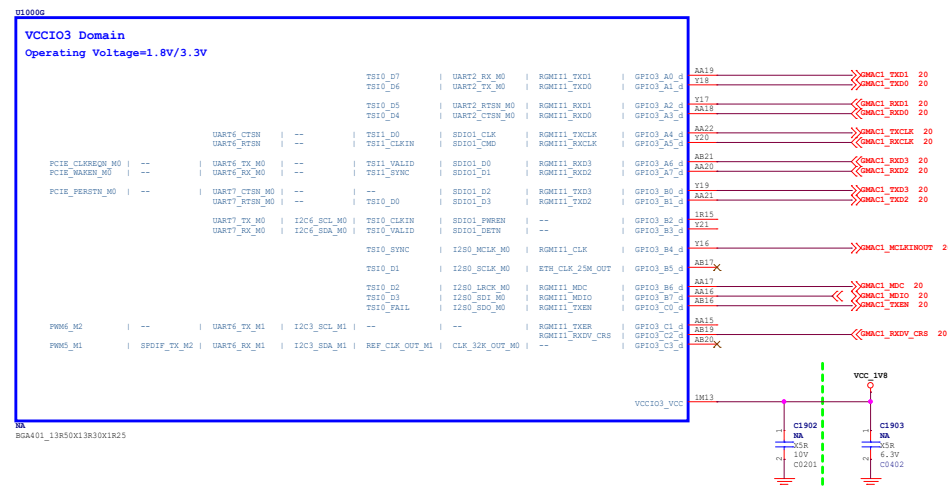


RK3528_D (VCCIO0 Domain)

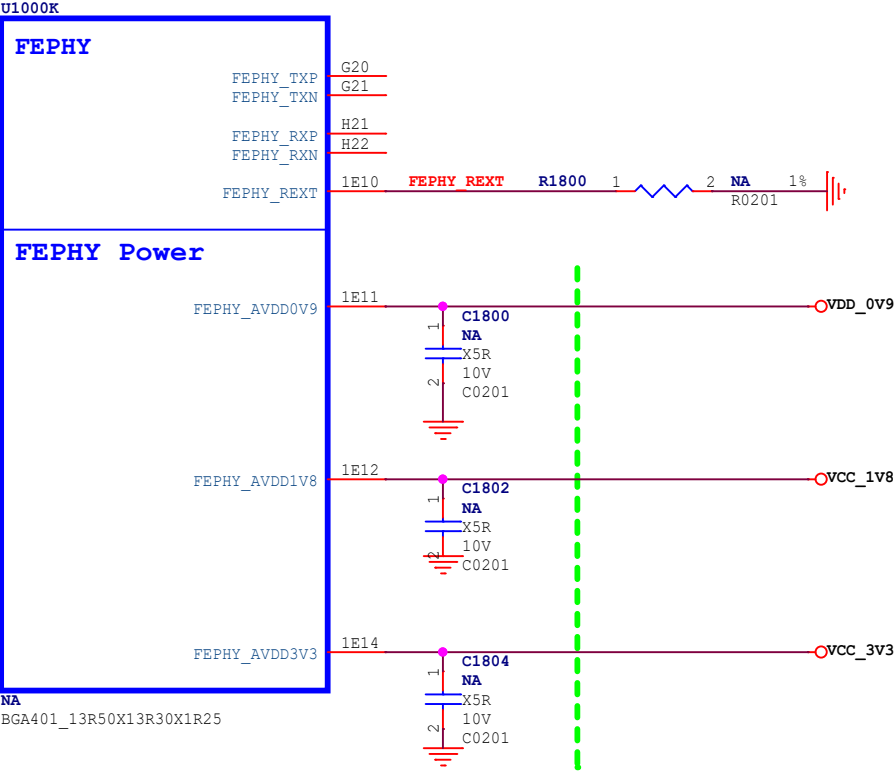


Note:
Caps of between dashed green lines and U1000 should be placed under the U1000 package.
Other caps should be placed close to the U1000 package

RK3528_G (VCCIO3 Domain)



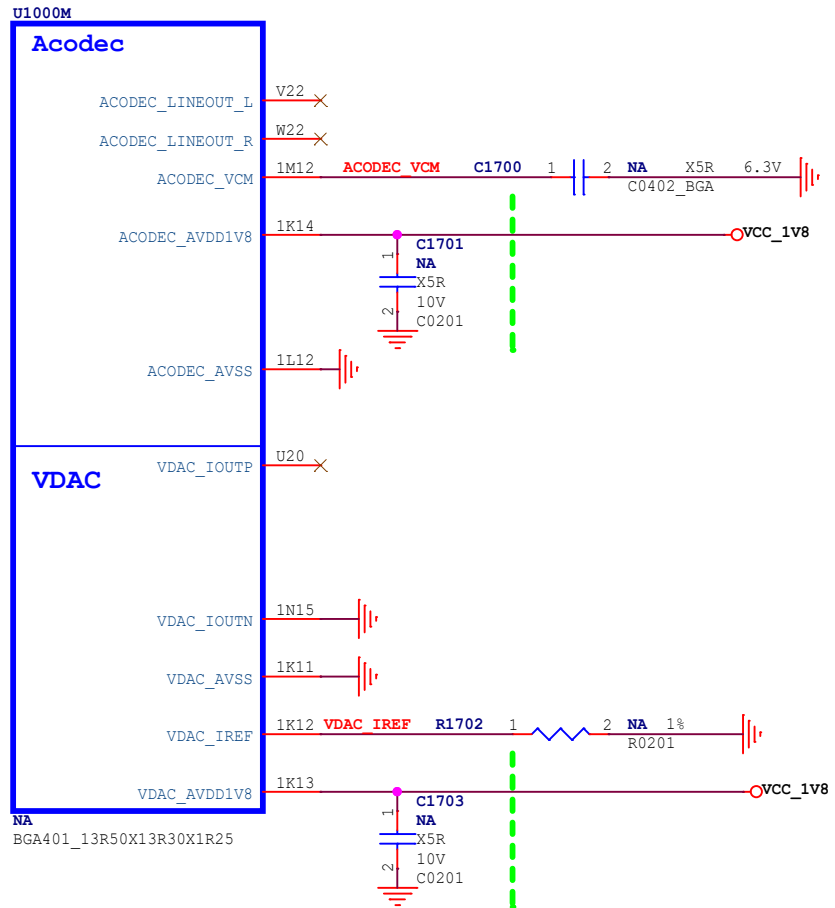
RK3528_K (Embed FEPHY)



Note:

Caps of between dashed green lines and U1000 should be placed under the U1000 package.
Other caps should be placed close to the U1000 package

RK3528_M (Acodec/VDAC)



Note:

Caps of between dashed green lines and U1000 should be placed under the U1000 package.
Other caps should be placed close to the U1000 package

RK3528_L (HDMI PHY)

U1000L

HDMI2.1 TMDs

HDMI_TX_CLKN
HDMI_TX_CLKP
HDMI_TX_D0N
HDMI_TX_D0P
HDMI_TX_D1N
HDMI_TX_D1P
HDMI_TX_D2N
HDMI_TX_D2P

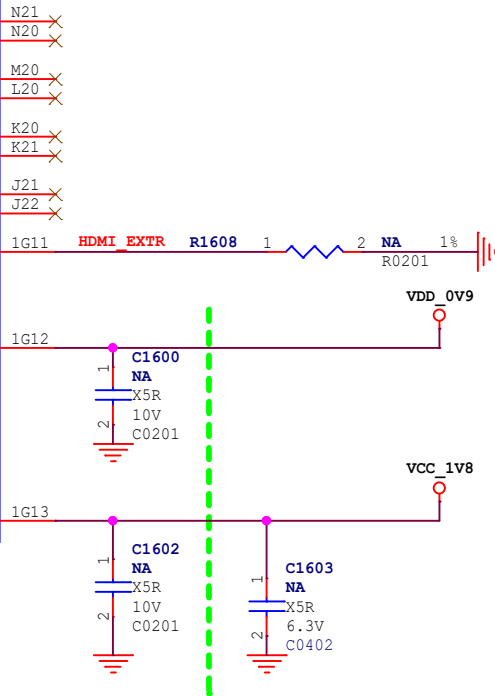
HDMI_REXT

HDMI PHY Power

HDMI_TX_DVDD0V9

HDMI_TX_AVDD1V8

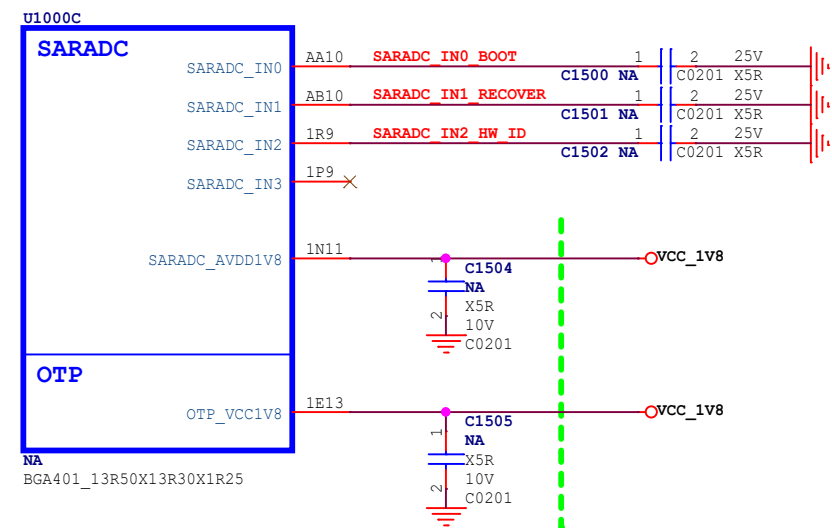
NA
BGA401_13R50X13R30X1R25



Note:

Caps of between dashed green lines and U1000 should be placed under the U1000 package.
Other caps should be placed close to the U1000 package

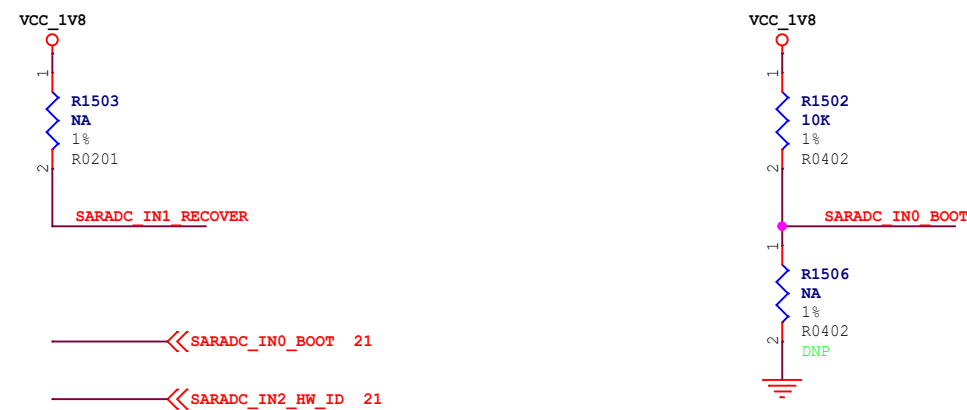
RK3528_C (Saradc/OTP)



Note:

Caps of between dashed green lines and U1000 should be placed under the U1000 package.

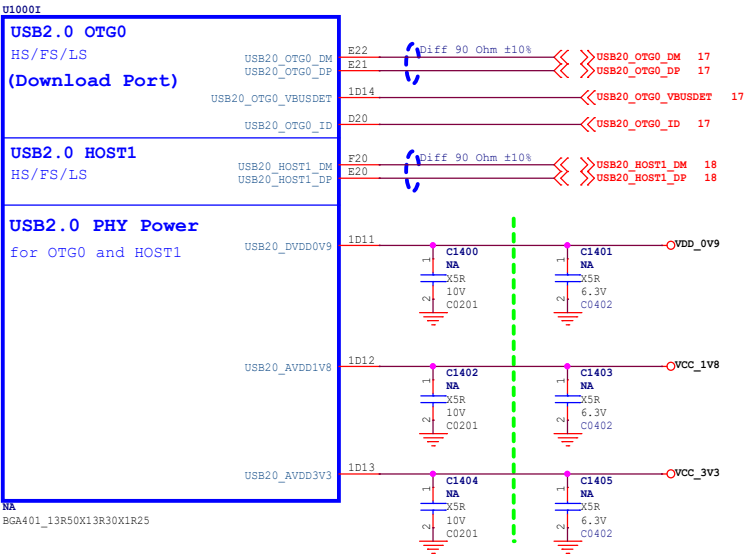
Other caps should be placed close to the U1000 package



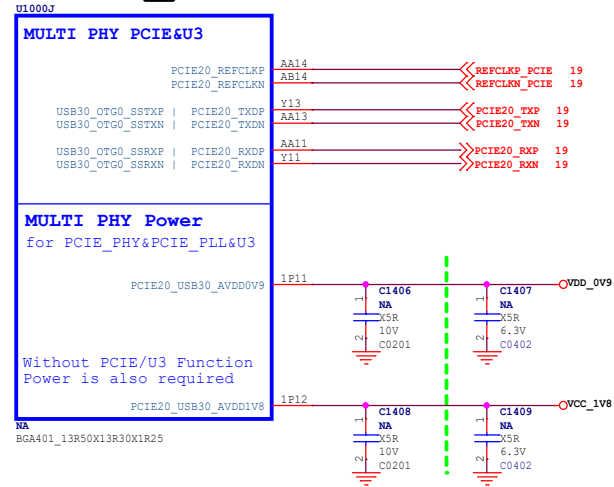
TABLE

Item	Rup	Rdown	ADC	BOOT MODE
LEVEL1	DNP	100K	0	USB (Maskrom mode)
LEVEL2	100K	12K	114	
LEVEL3	100K	27K	228	FSPI--USB
LEVEL4	100K	51K	342	
LEVEL5	100K	82K	456	
LEVEL6	100K	120K	570	EMMC--USB
LEVEL7	100K	200K	683	EMMC--SD Card--USB
LEVEL8	100K	330K	796	SD Card--USB
LEVEL9	100K	820K	910	
LEVEL10	100K	DNP	1023	FSPI--EMMC--SD Card--USB

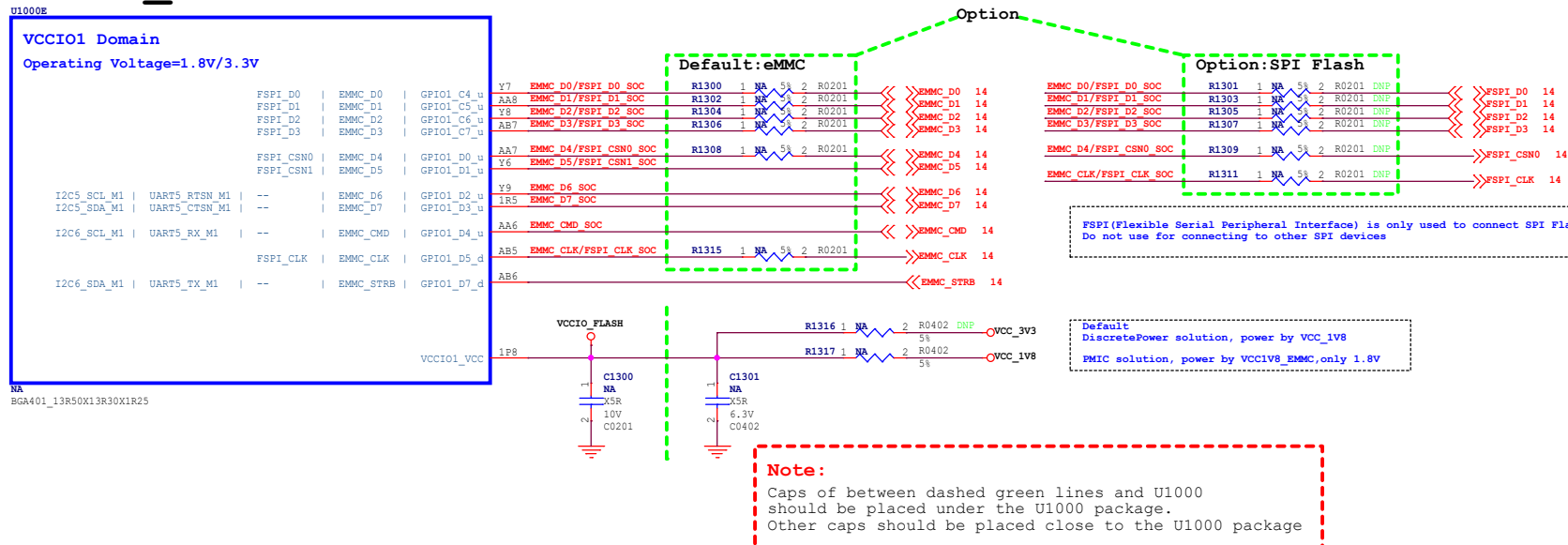
RK3528_I (USB2.0 OTG/HOST)



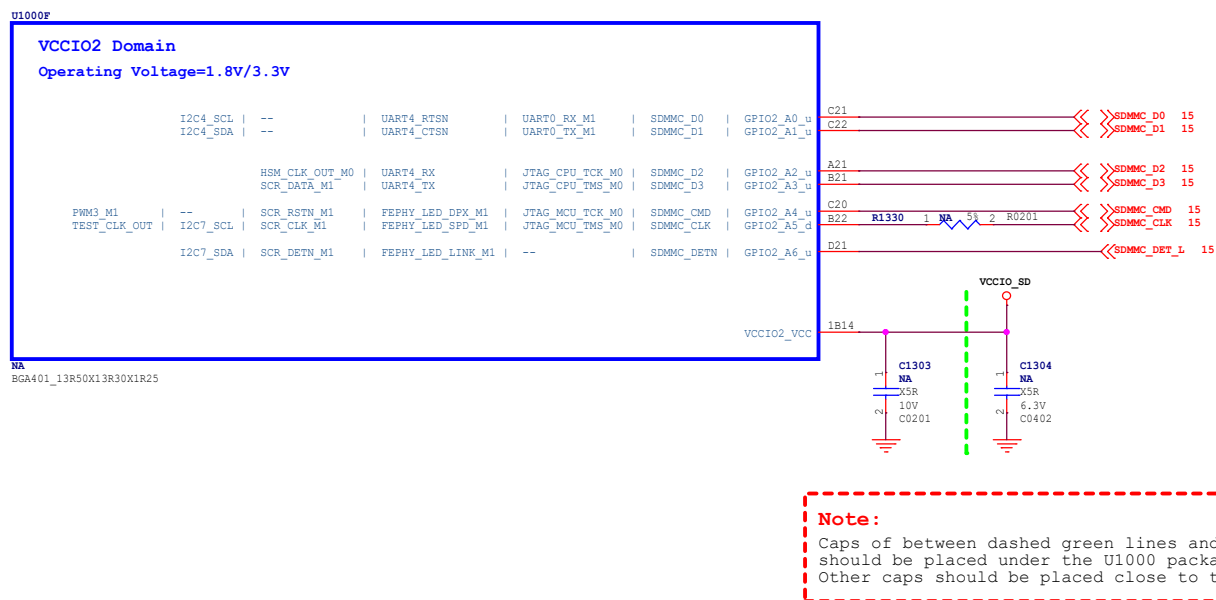
RK3528_J (PCIE2.0/U3 PHY)

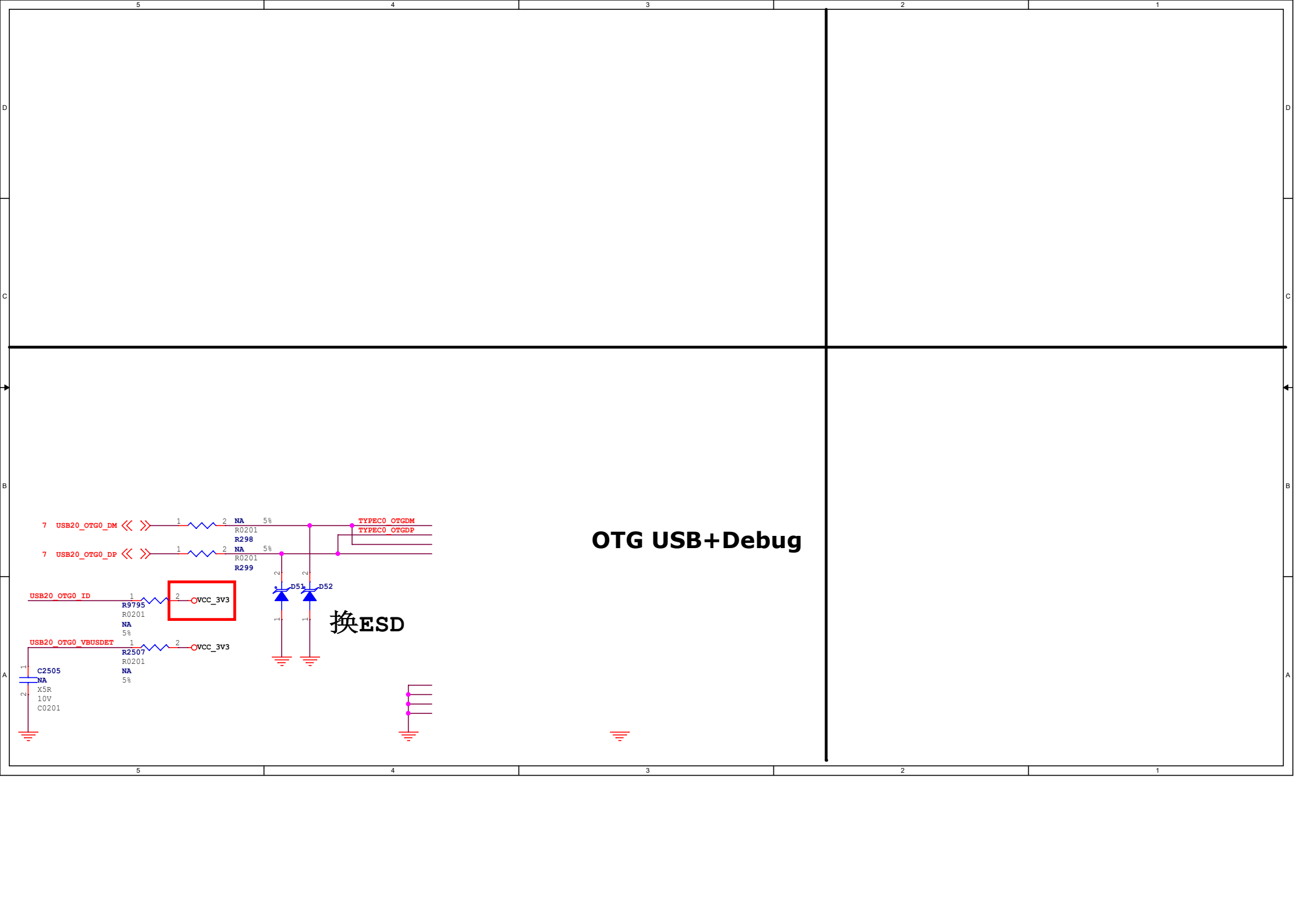


RK3528 E (VCCIO1 Domain)



RK3528 F (VCCIO2 Domain)

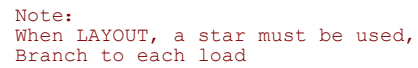




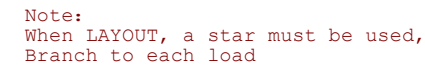
Step 1



Step 1



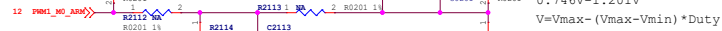
Step 2



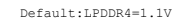
Step 2



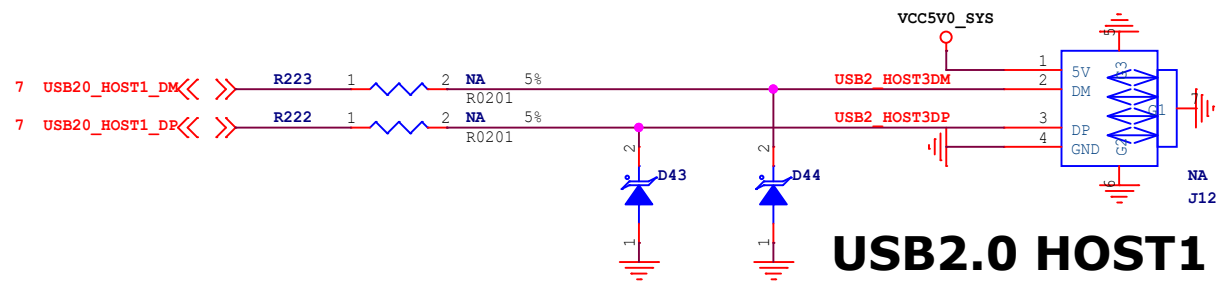
Step 1



Step 2



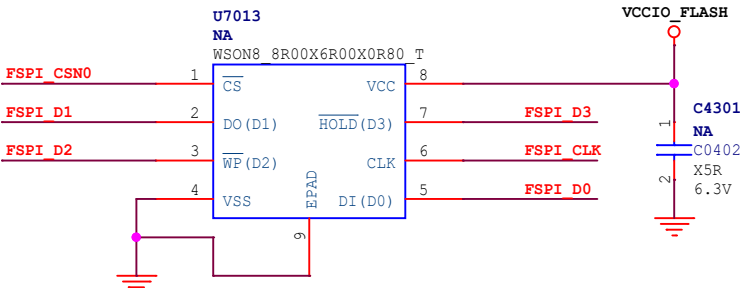
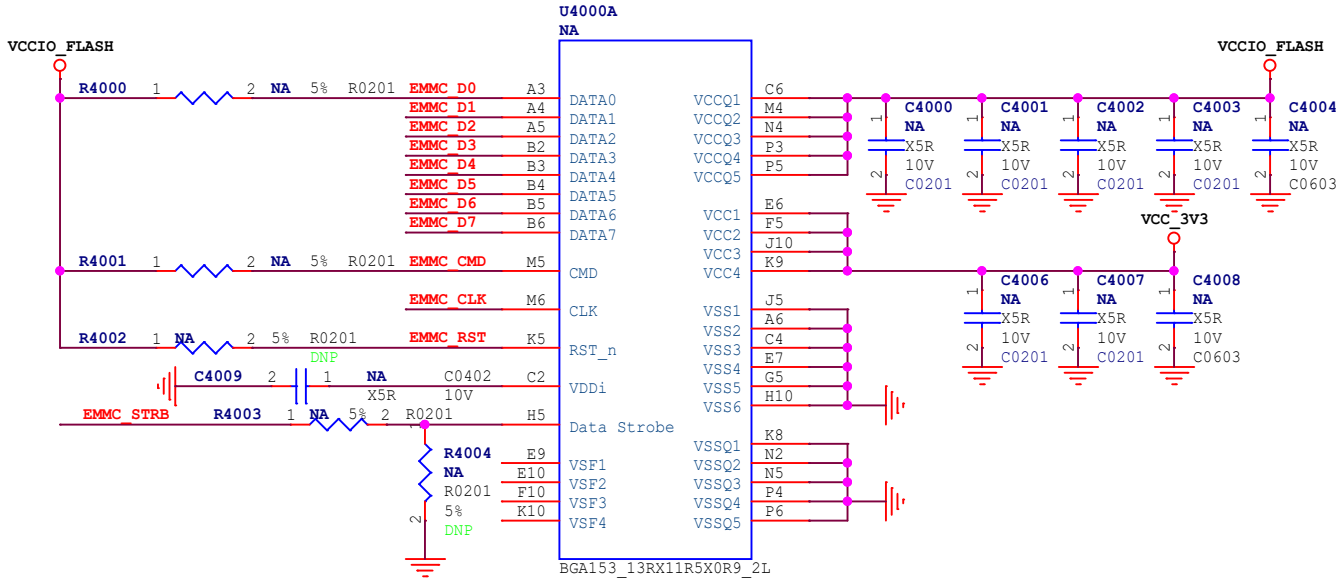
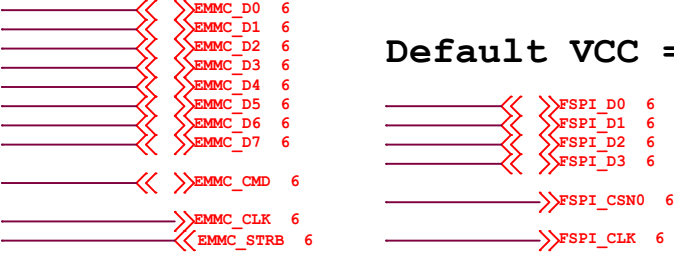
DDR4	1.20V	100K
LPDDR4/4x	1.10V	84.5K
DDR3	1.5V	150K
DDR3L	1.35V	127K
LPDDR3	1.25V	110K



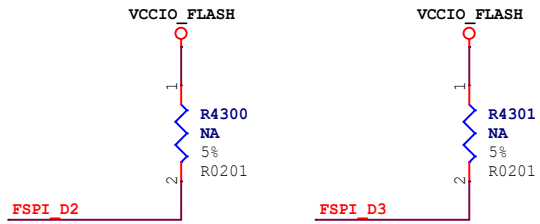
EMMC

FSPI

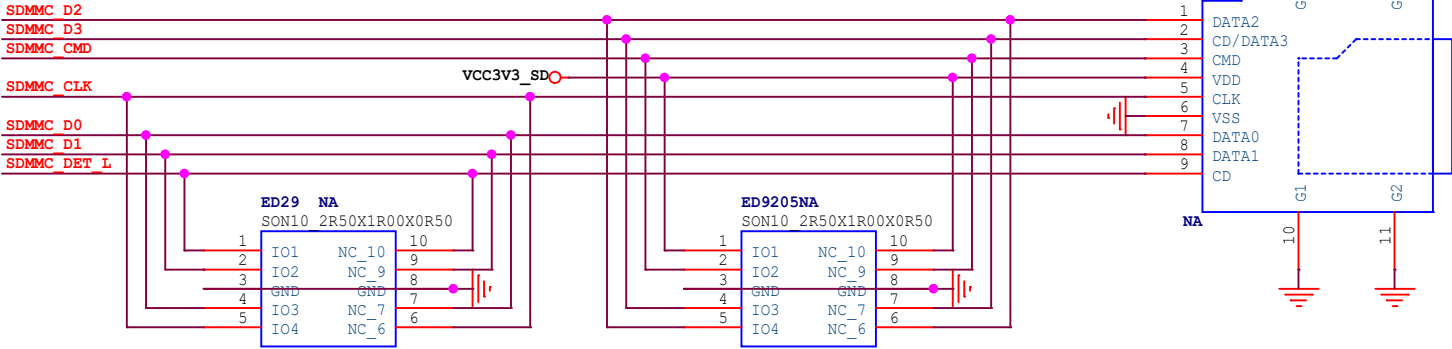
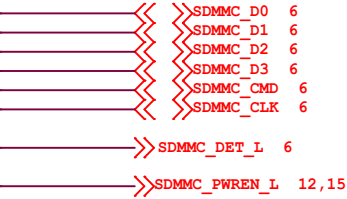
Default VCC = 1.8V



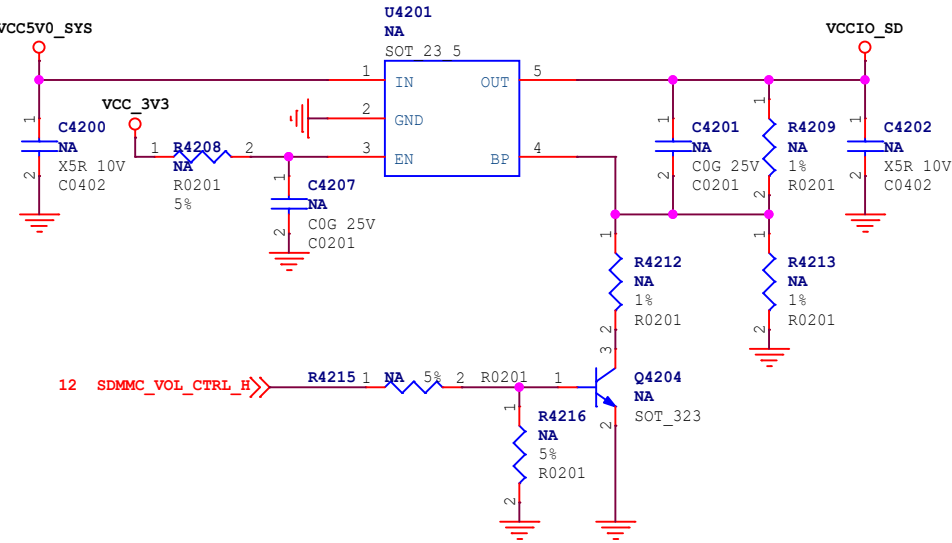
U4000B	NA	A7	E5	E8	G3	G10	K6	K7	P7	P10		
A2	NC2	REF1	REF2	REF3	REF4	REF5	REF6	REF7	REF8	REF9	NC196	P14
A8	NC8										NC195	P13
A9	NC9										NC194	P12
A10	NC10										NC193	P11
A11	NC11										NC191	P8
A12	NC12										NC190	P2
A13	NC13										NC184	P1
A14	NC14										NC183	
B1	NC15										NC182	N14
B7	NC21										NC181	N13
B8	NC22										NC180	N12
B9	NC23										NC179	N11
B10	NC24										NC178	N10
B11	NC25										NC177	N9
B12	NC26										NC176	N8
B13	NC27										NC175	N7
B14	NC28										NC174	N6
C1	NC29										NC171	N3
C3	NC31										NC169	N1
C7	NC35										NC168	M14
C8	NC36										NC167	M13
C9	NC37										NC166	M12
C10	NC38										NC165	M11
C11	NC39										NC164	M10
C12	NC40										NC163	M9
C13	NC41										NC162	M8
C14	NC42										NC161	M7
D1	NC43										NC157	M3
D2	NC44										NC156	M2
D3	NC45										NC155	M1
D4	NC46										NC154	L14
D12	NC54										NC153	L13
D13	NC55										NC152	L12
D14	NC56										NC143	L3
E1	NC57										NC142	L2
E2	NC58										NC141	L1
E3	NC59										NC140	K14
E12	NC68										NC139	K13
E13	NC69										NC138	K12
E14	NC70										NC137	K11
F1	NC71										NC129	K3
F2	NC72										NC128	K2
F3	NC73										NC127	K1
F12	NC82										NC126	J14
F13	NC83										NC125	J13
F14	NC84										NC124	J12
G1	NC85										NC115	J3
G2	NC86										NC114	J2
G12	NC96										NC113	J1
G13	NC97										NC112	H14
G14	NC98										NC111	H13
											NC110	H12
											NC101	H3
											NC100	H2
											NC99	H1



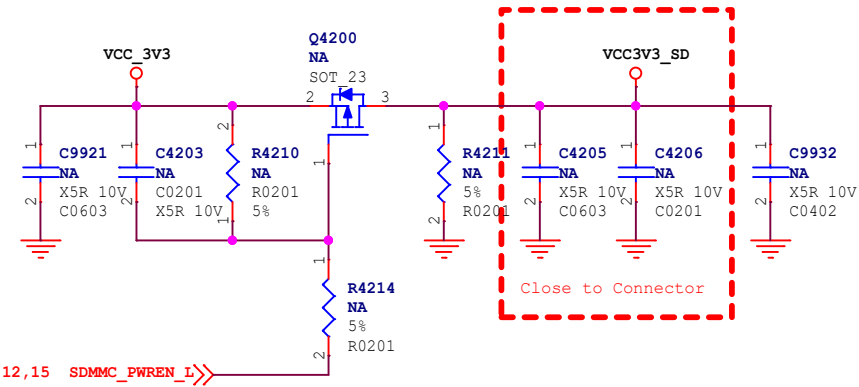
TF Card



VCCIO_SD



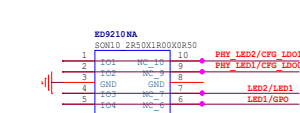
VCC3V3_SD



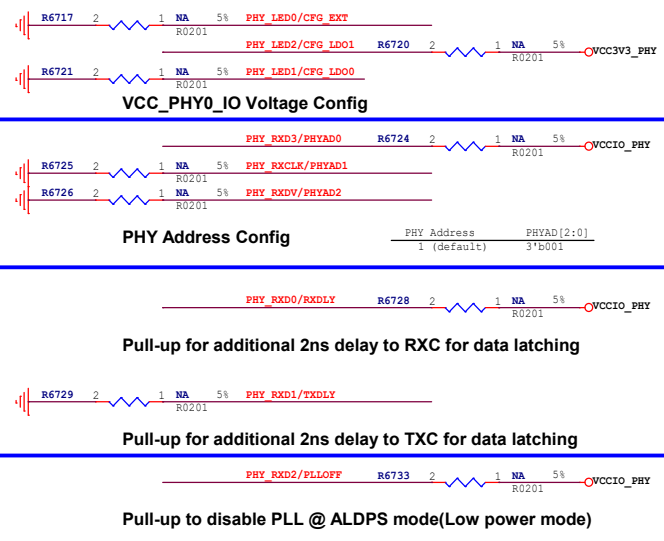
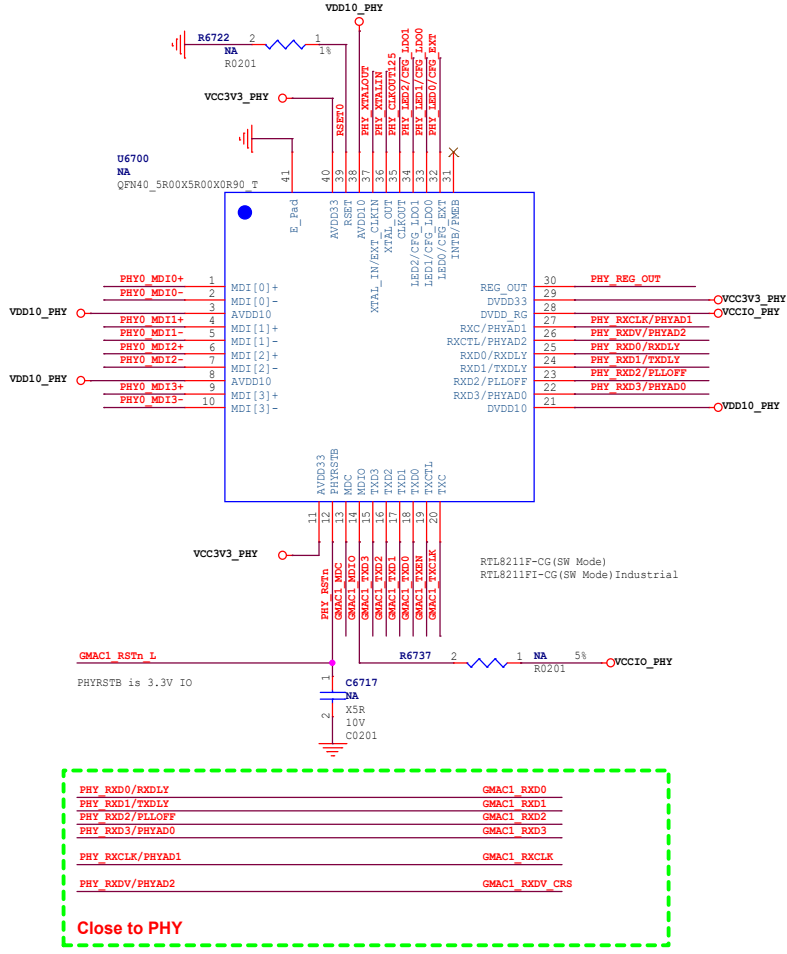
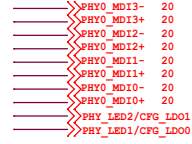
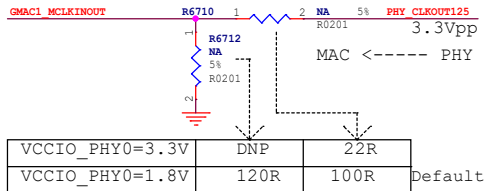
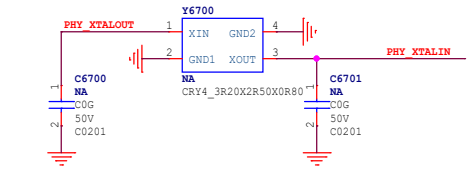
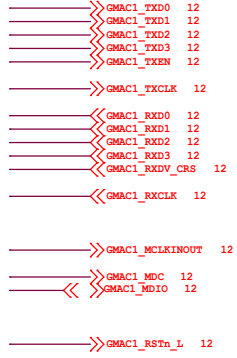
SDMMC_PWREN=L VCC_SD=3.3V(Default)
SDMMC_PWREN=H VCC_SD=0V

SDIO2.0 SDMMC0_VOL_CTRL=H VCCIO_SD=3.34V(Default)
SDIO3.0 SDMMC0_VOL_CTRL=L VCCIO_SD=1.816V

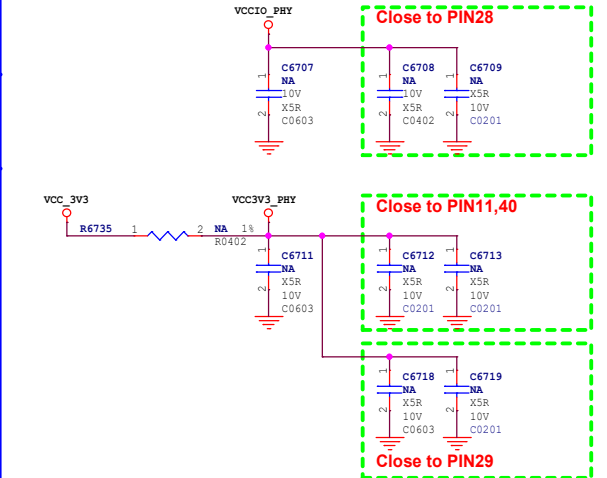
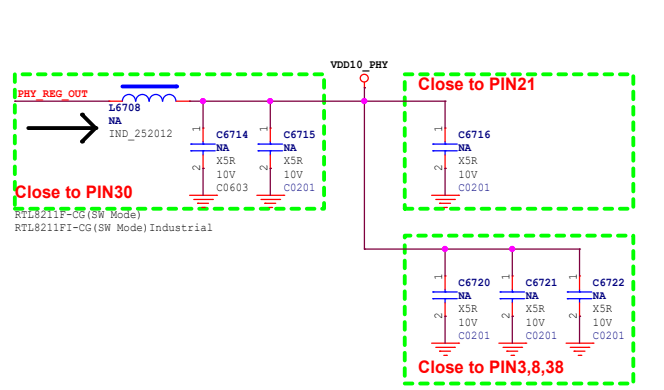
1



RGMII



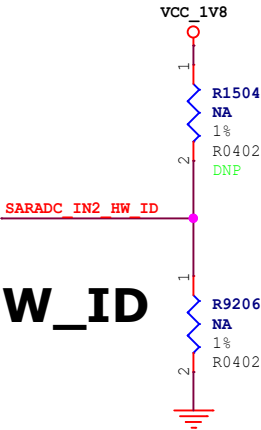
RGMII Power Source	CFG_EXT	CFG_LD0[1:0]	
External 3.3V	1'b1	2'b00	
External 1.8V	1'b1	2'b10	
Internal 1.8V(default)	1'b0	2'b10	



KEY



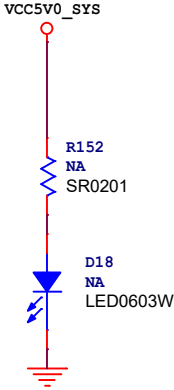
HW_ID



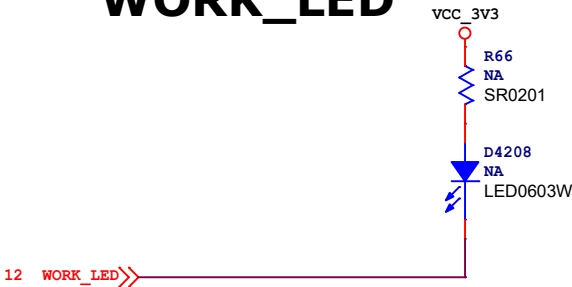
TABLE

SARADC_IN2	Rup	Rdown	ADC
HW_ID0	DNP	100K	0
HW_ID1	100K	12K	114
HW_ID2	100K	27K	228
HW_ID3	100K	51K	342
HW_ID4	100K	82K	456
HW_ID5	100K	120K	570
HW_ID6	100K	200K	683
HW_ID7	100K	330K	796
HW_ID8	100K	820K	910
HW_ID9	100K	DNP	1023

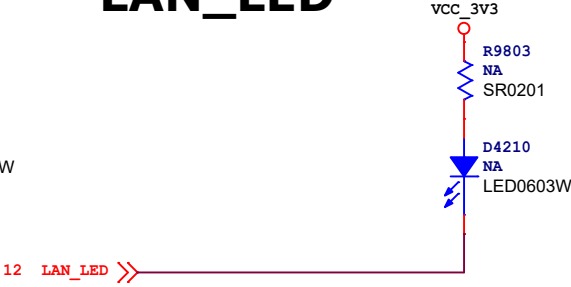
POWER_LED



WORK_LED



LAN_LED



WAN_LED

