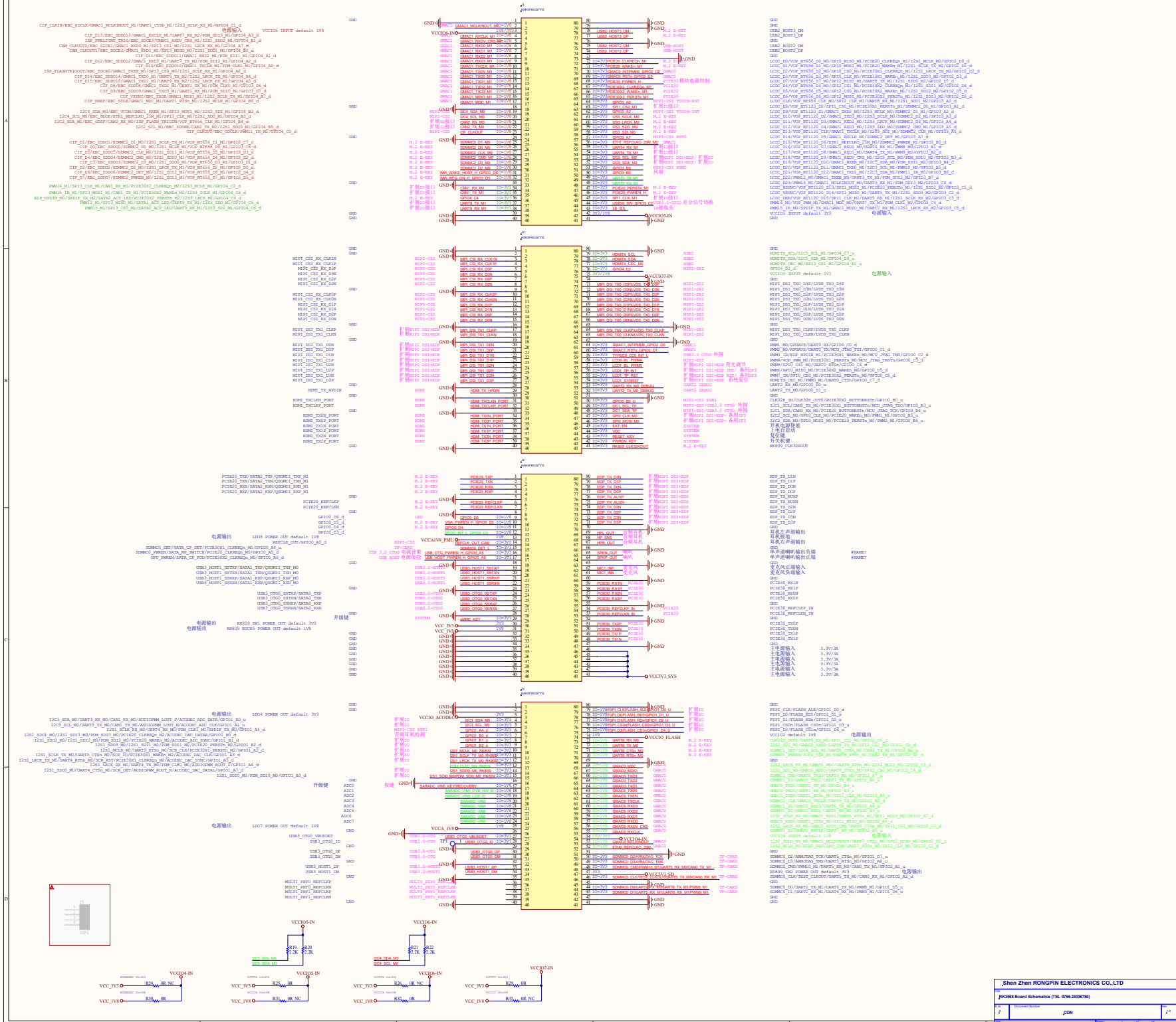
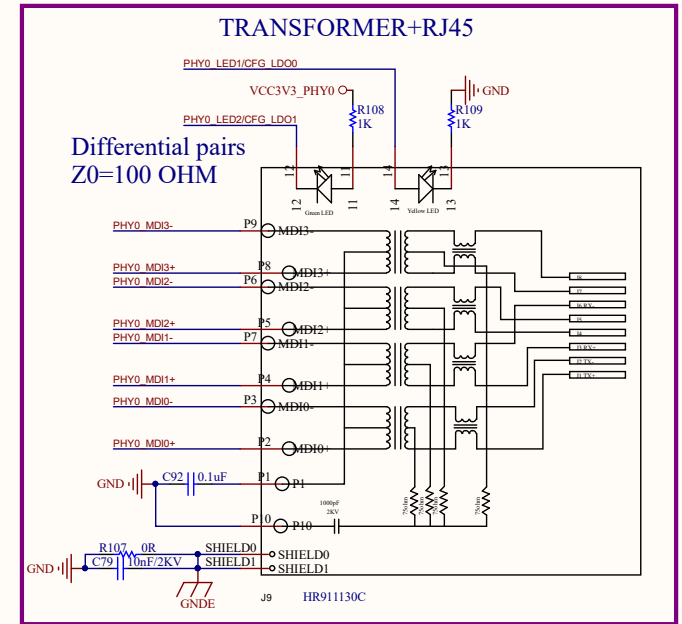
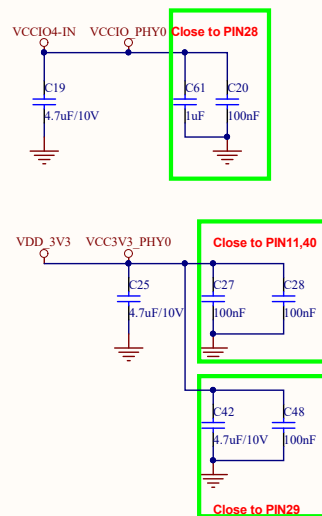
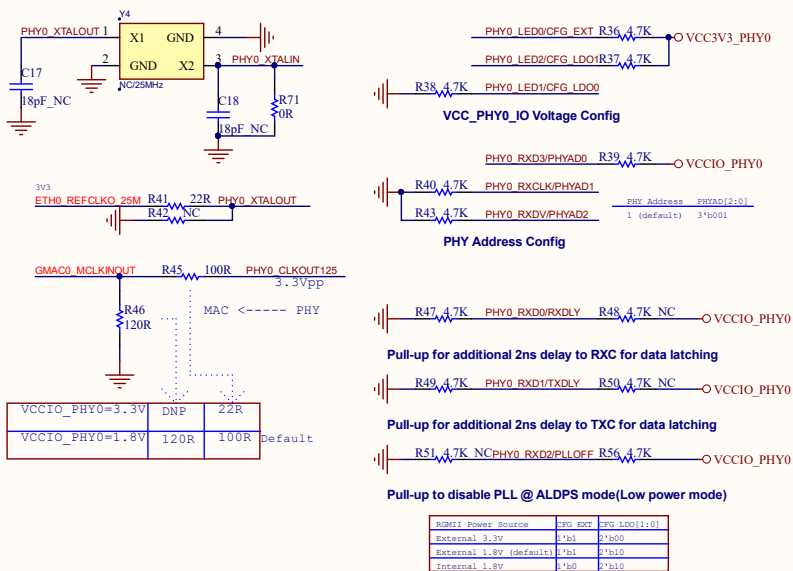
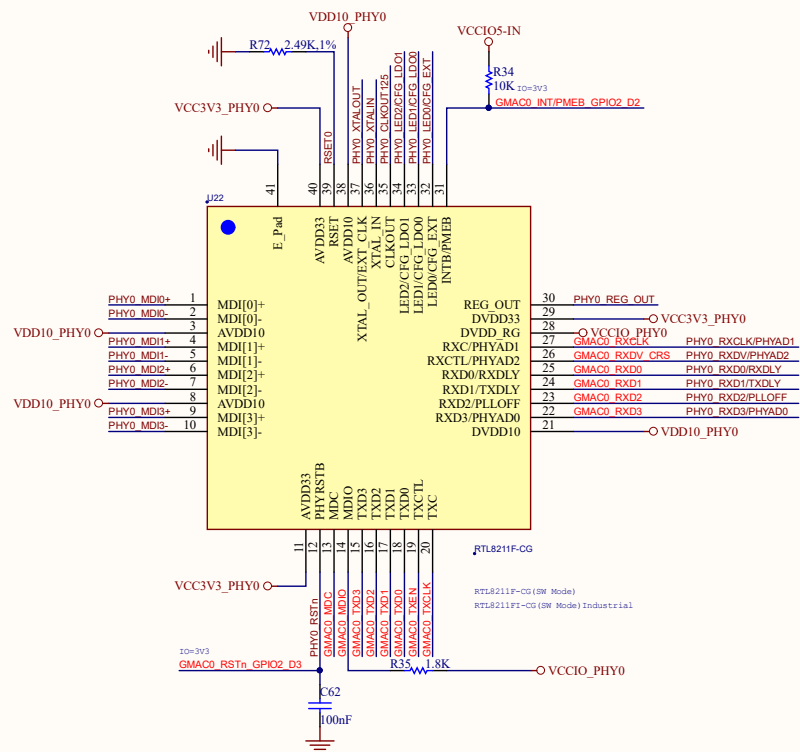


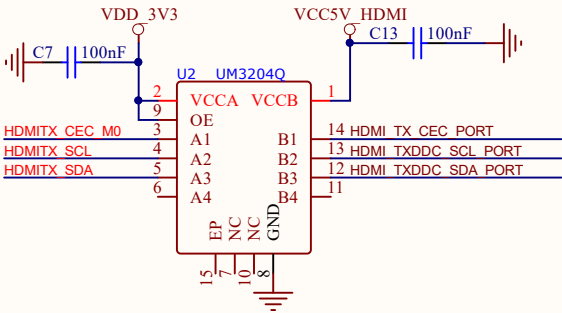
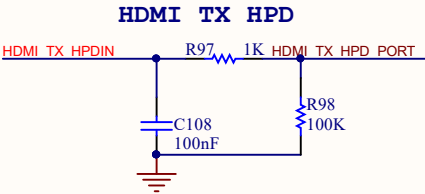
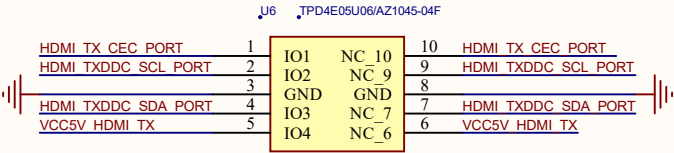
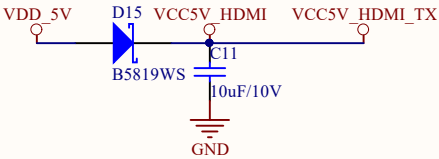
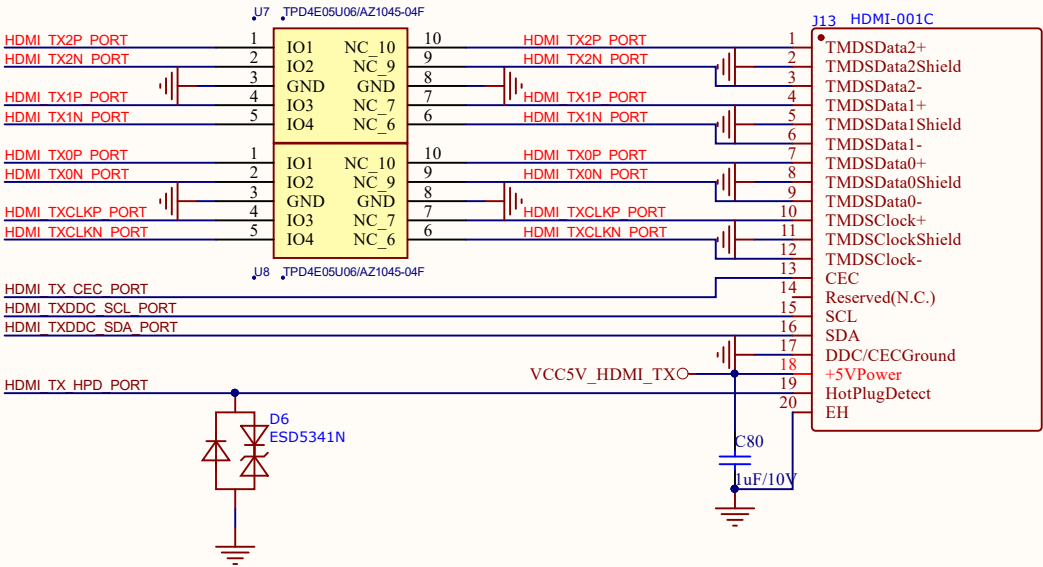




RGMI Power Source	CFG EXT	CFG LDO[1:0]
External 3.3V	1'b1	2'b00
External 1.8V (default)	1'b1	2'b10
Internal 1.8V	1'b0	2'b10

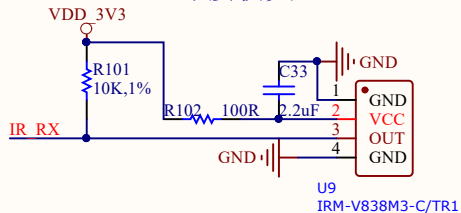
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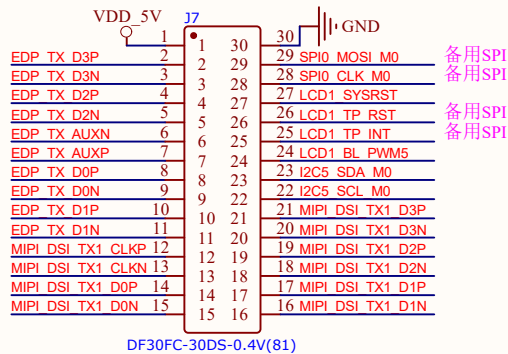


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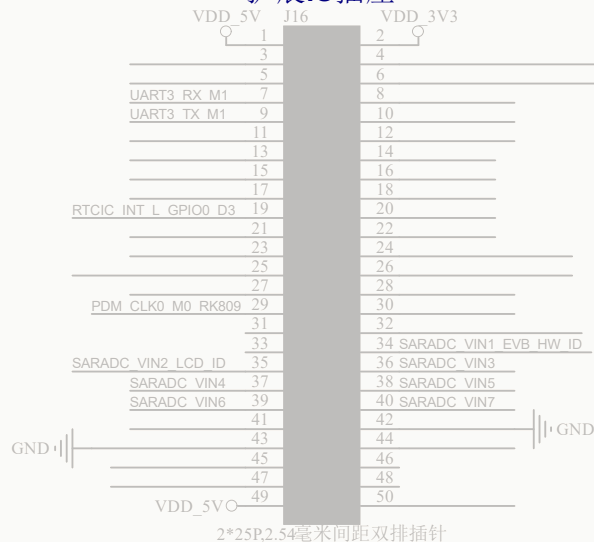
红外接收头



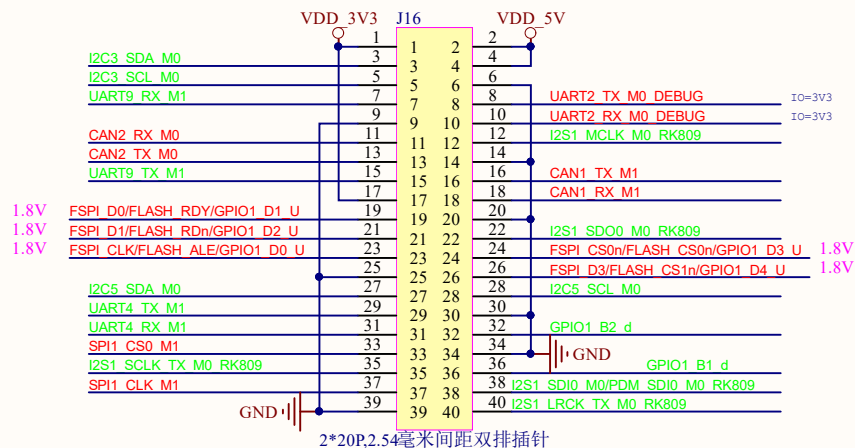
扩展MIPI DSI+EDP视频接口



扩展IO插座



扩展IO插座



Title

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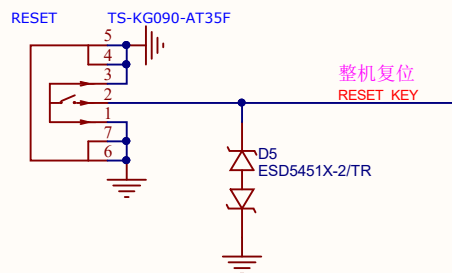
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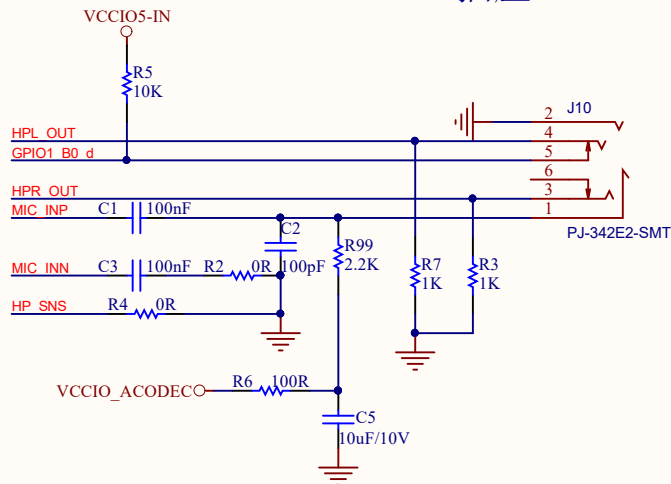
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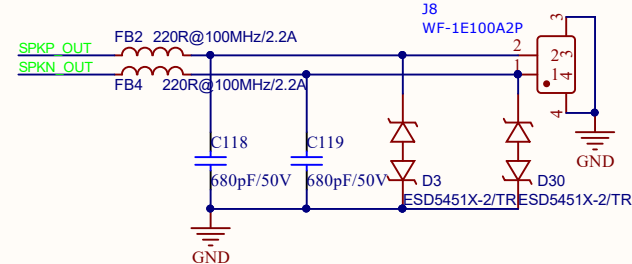
RESET



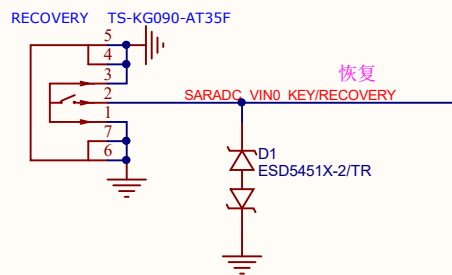
AUDIO 3.5mm插座



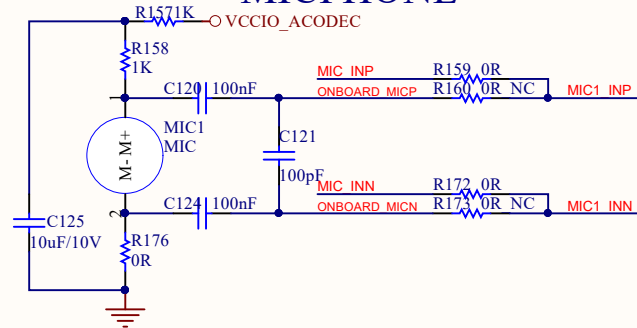
SPEAKER



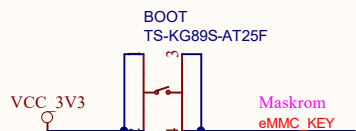
RECOVERY



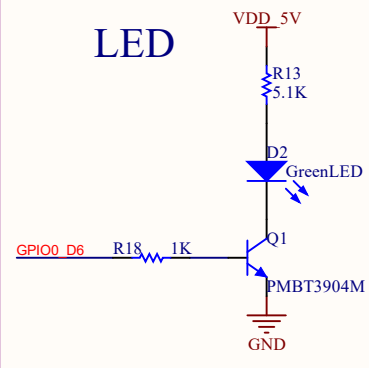
MICPHONE



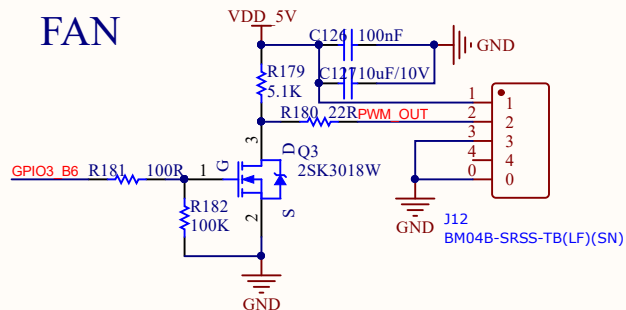
MaskRom



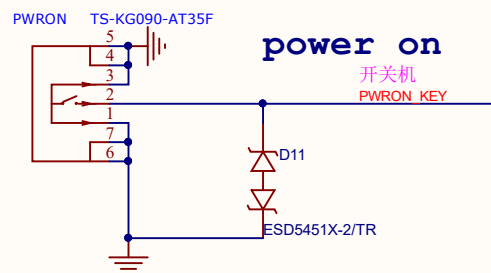
LED



FAN

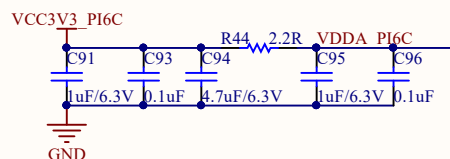
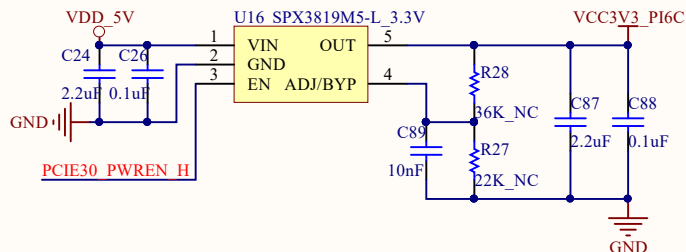


power on

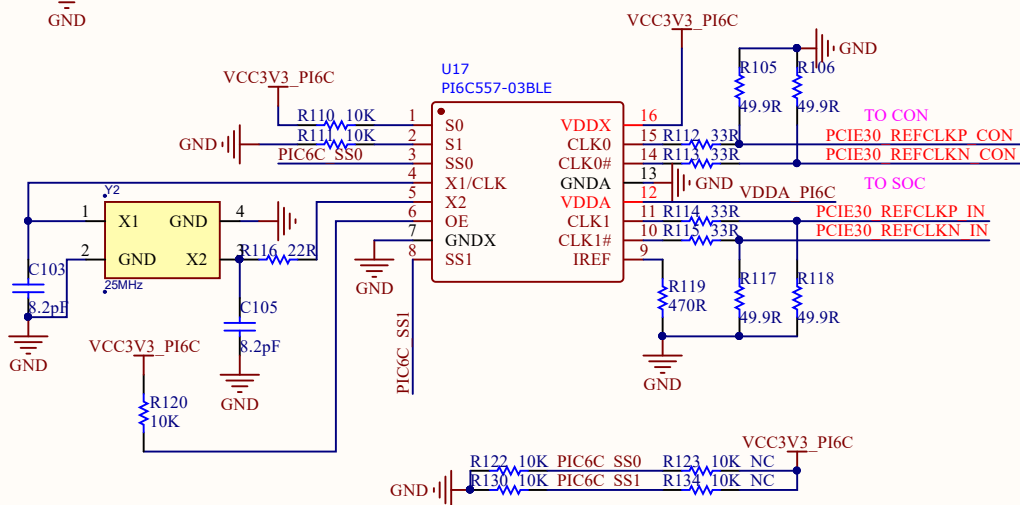


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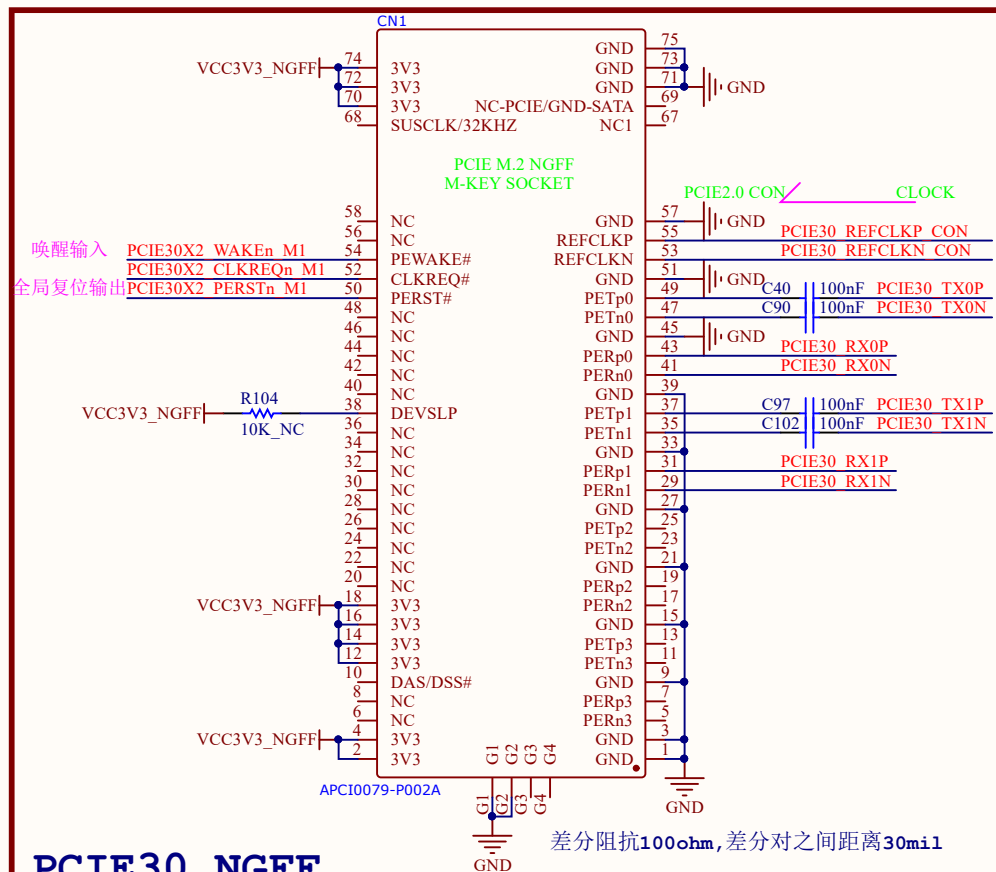
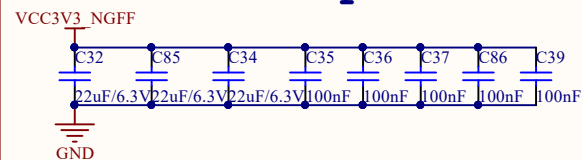
时钟芯片电源



Out Select			Spread Select		
SI	SO	CLK(MHz)	SSI	SSO	Spread Select
0	0	25	0	0	No Spread
0	1	100	0	1	Down-0.5
1	0	125	1	0	Down-0.75
1	1	200	1	1	No Spread



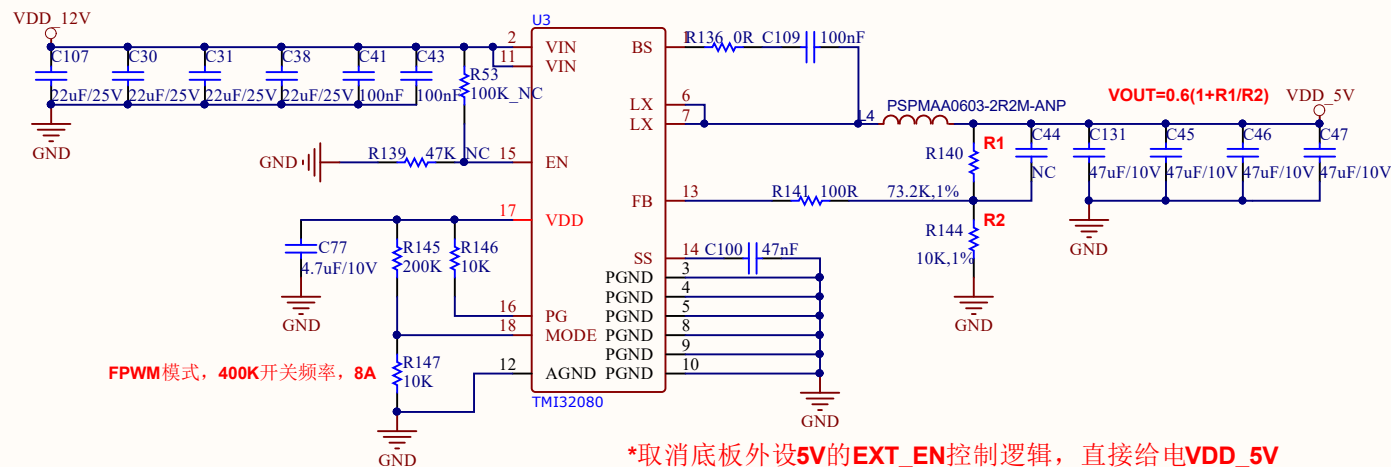
Decouple



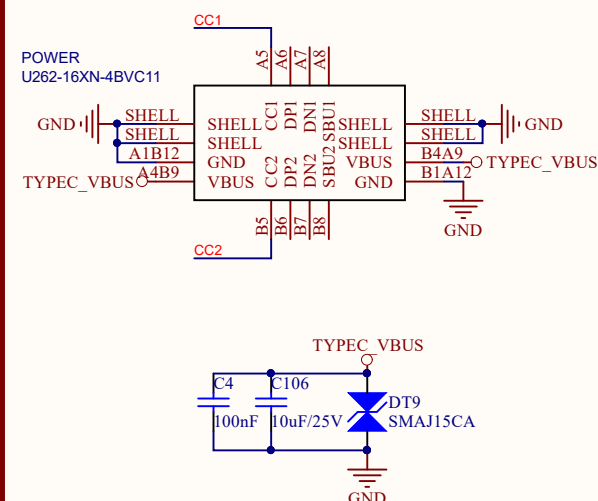
PCIE30 NGFF

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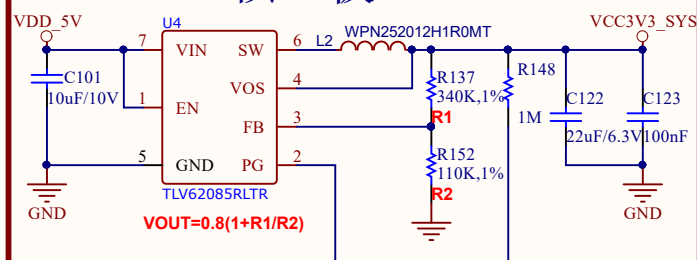
DC12V-DC5V



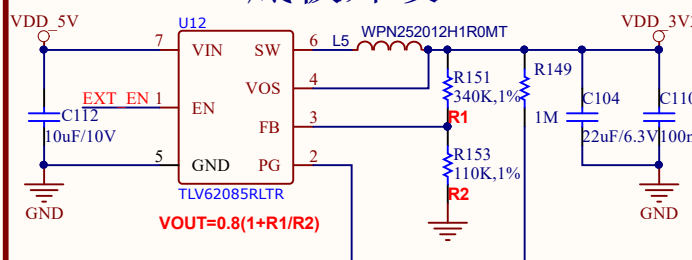
POWER_PD



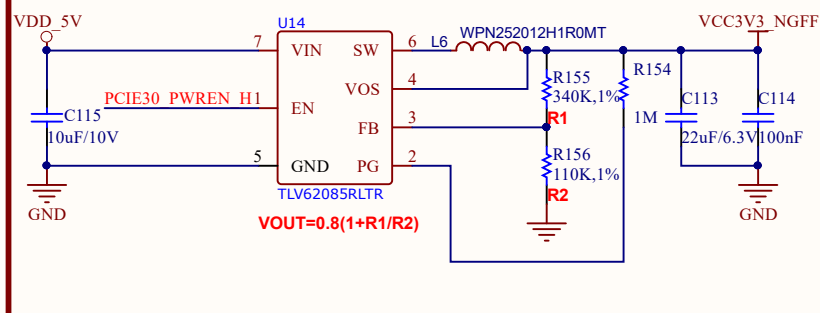
核心板3.3V



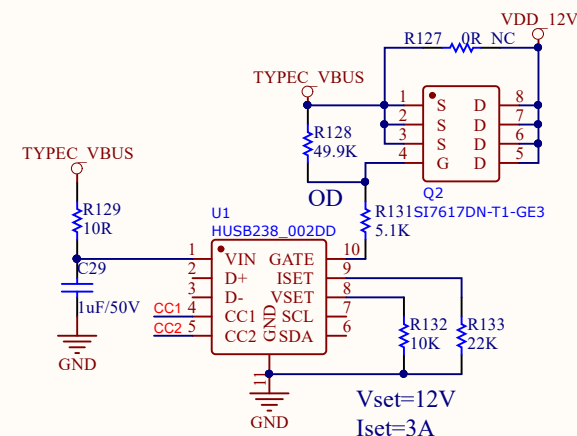
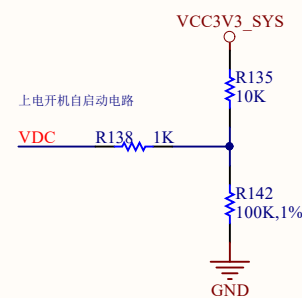
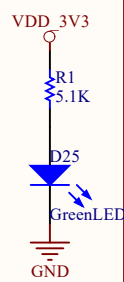
底板外设3.3V



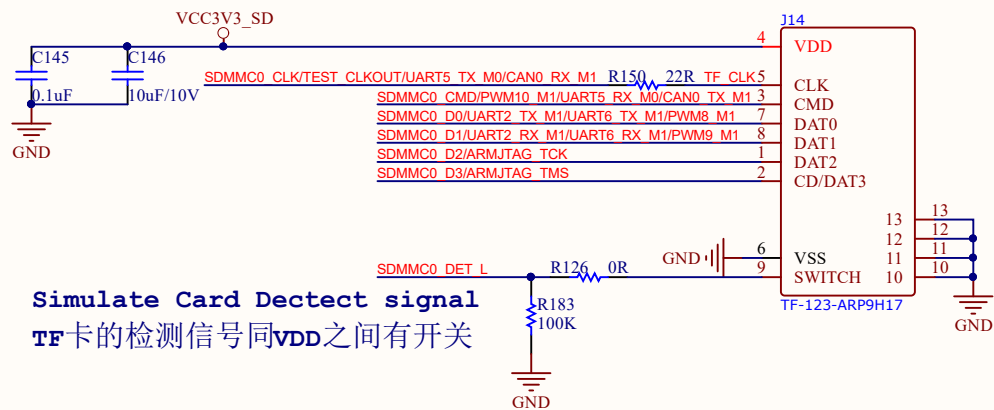
PCIE NGFF POWER



LED指示

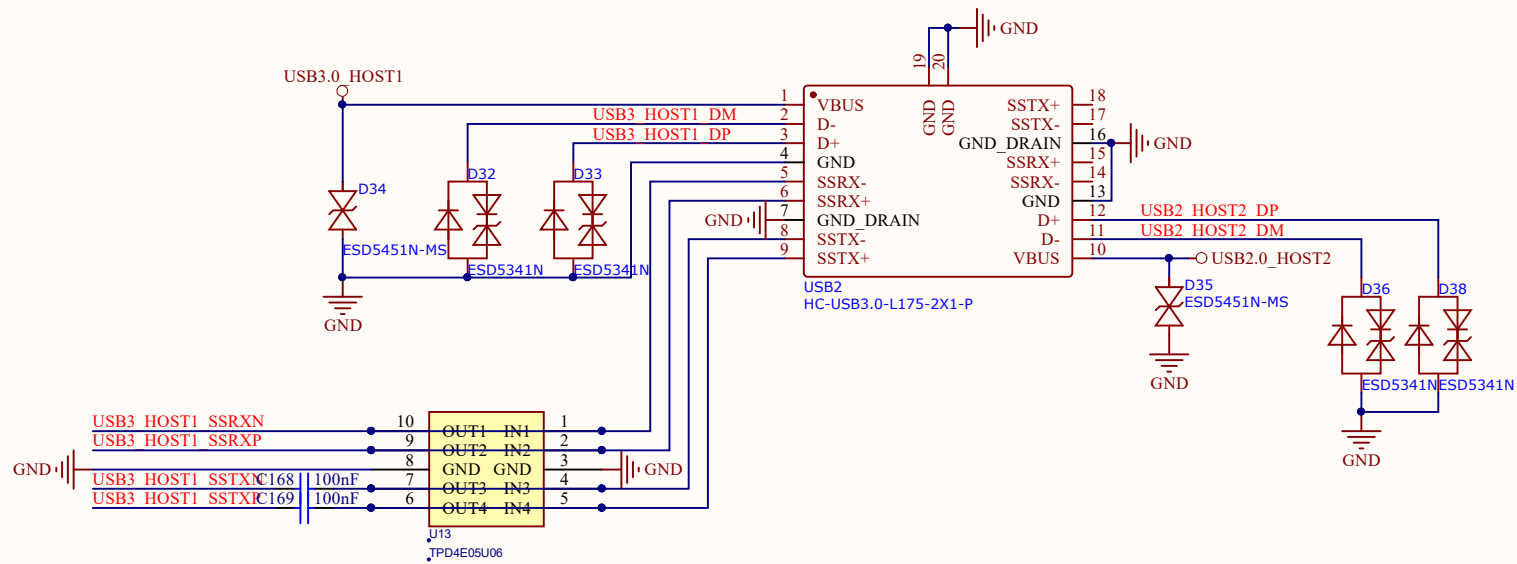
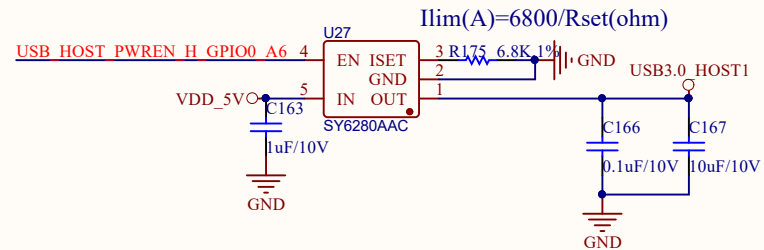
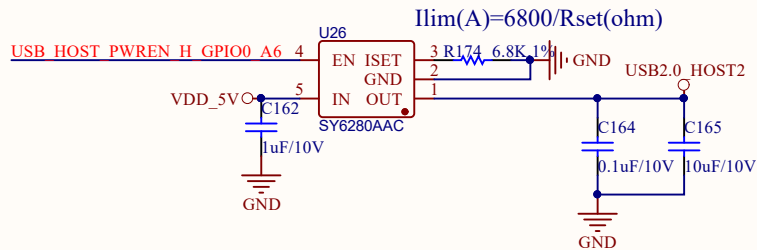


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Simulate Card Dectect signal
TF卡的检测信号同VDD之间有开关

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