

To Extract BOM:

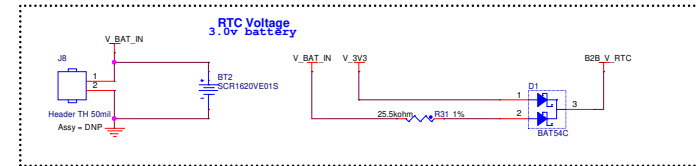
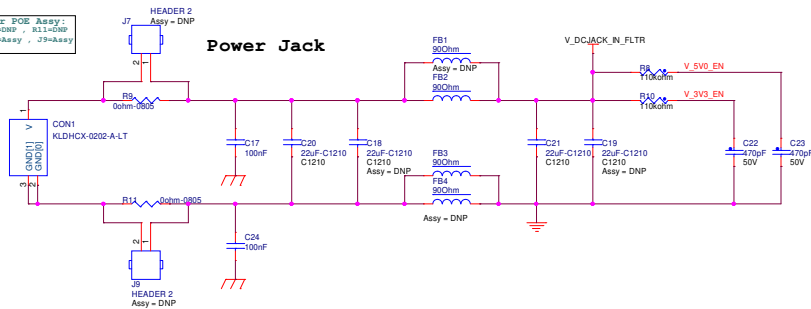
Item\tQuantity\tAssemblyOption\tPart\tPCB Footprint\tDescription\tDataSheet\tManufacturer\tManufacturer P/N\tSolidRun P/N\tReference
{Item}\t{Quantity}\t{ASSY}\t{Value}\t{PCB Footprint}\t{DESCRIPTION}\t{Datasheet}\t{Manufacturer}\t{Manufacturer P/N}\t{SolidRun P/N}\t{Reference}

To Extract BOM:

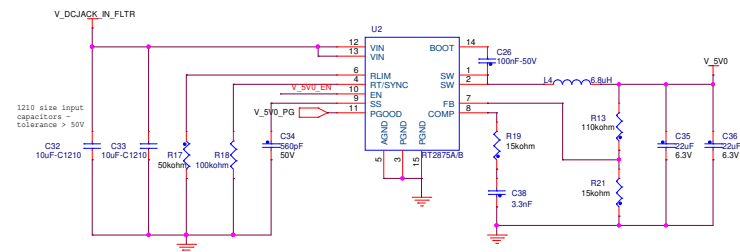
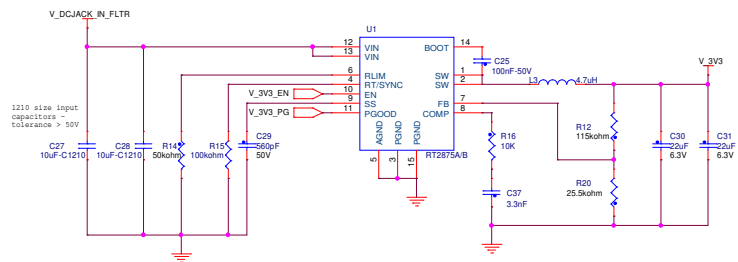
Item\tQuantity\tAssemblyOption\tValue\tDescription\tManufacturer Name\tManufacturer P/N\tSolidRun P/N\tPCB Footprint\tReferer
{Item}\t{Quantity}\t{Assy}\t{Value}\t{DESCRIPTION}\t{Manufacturer Name}\t{Manufacturer P/N}\t{SolidRun P/N}\t{PCB Footprint}\t{Referer}

For POE Assy:
R3=DNP, R11=DNP
J7=Assy, J9=Assy

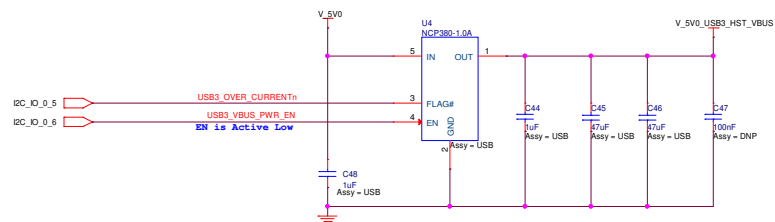
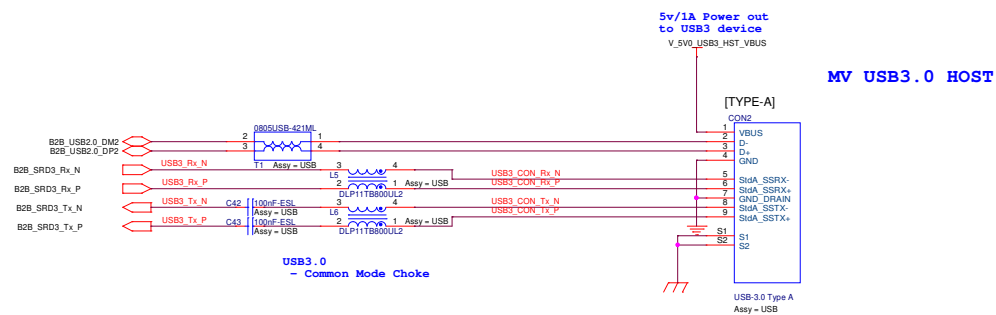
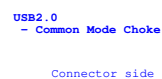
Power Jack



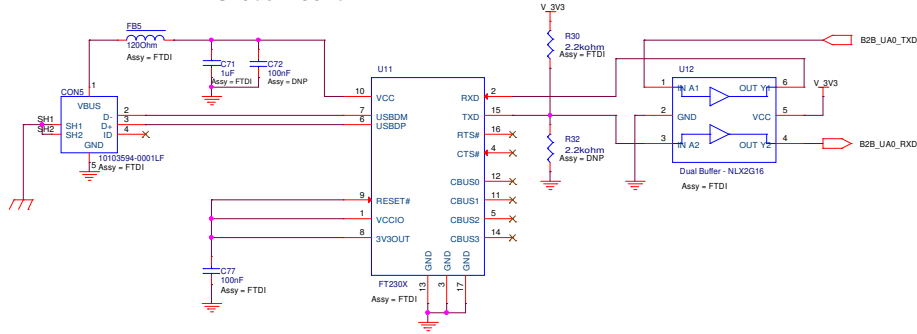
Rev-1.3: D2 replaced to R31 (Bug 56)



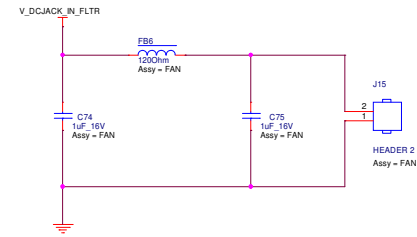
The schematic diagram illustrates the internal components and connections of the B2B_MPP20 module. The module is a Micro SD push-push interface. It includes a V3V3 power supply, a 1uF capacitor (C40) and a 100uF capacitor (C39) for decoupling. The module is connected to a B2B_MPP40 module via a 10K resistor (R22). The module is also connected to a B2B_MPP38 module via a 33uF capacitor (C41). The module is labeled 'Micro SD push-push' and 'Assy = uSD'.



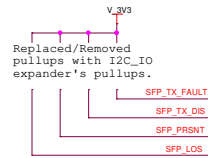
microUSB to UART



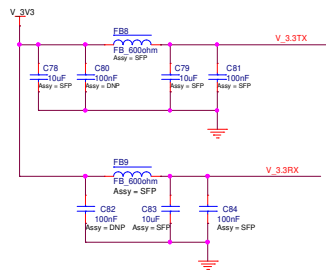
FAN Power



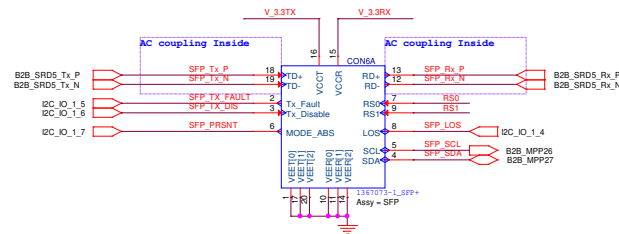
SFP + defines maximum current withdraw from V_3.3TX and V_3.3RX as 300mA each.



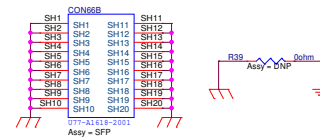
SFP TRANSCEIVER Power



SFP TRANSCEIVER

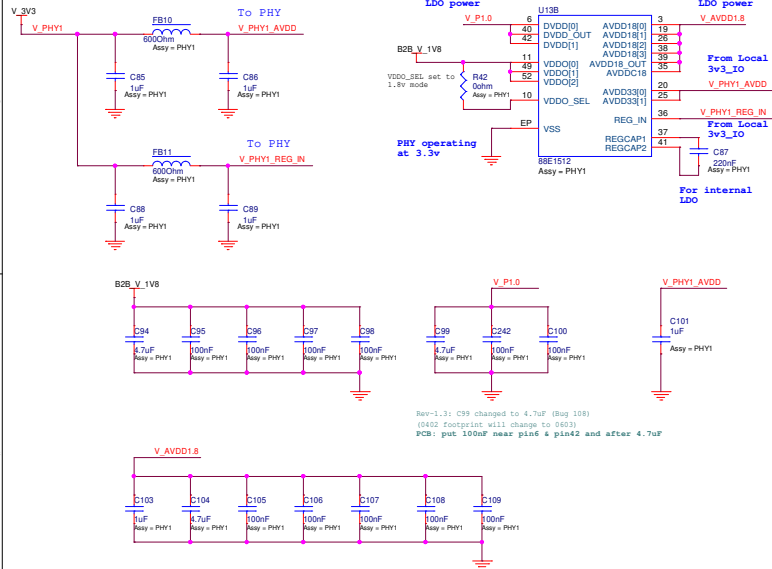


SFP Shield



PHY Power configurations

- 88E1512 power configurations:
1. using internal 1v8 and 1v0 regulators.
2. VDDO_SEL config VDDO to operate at 2v5/3v3.



PHYADD configuration

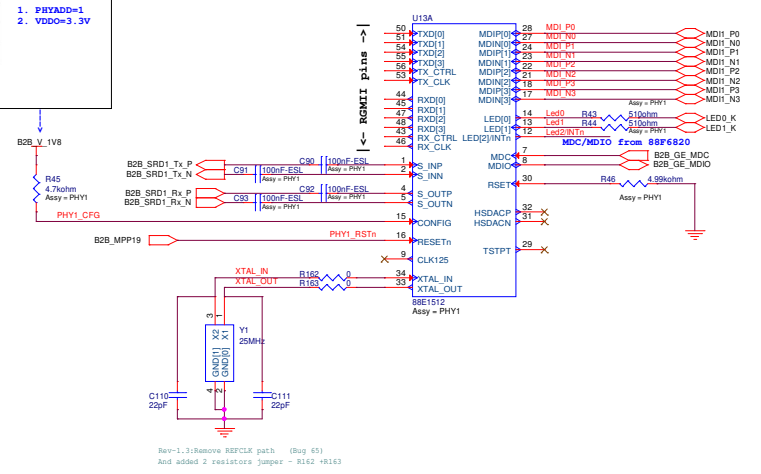
PHY1 - SGMII Function as GE1 Port

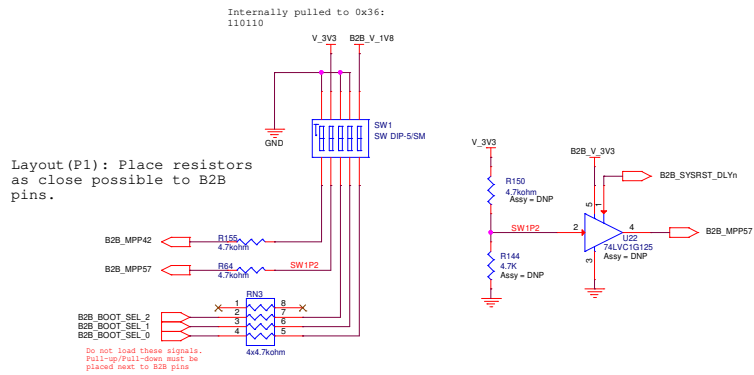
88E1512 PHY configuration options:
PHY_address=1

Pin	CONFIG Bit 1	CONFIG Bit 6	Value Assignment
CONFIG 0	0	0	PHYADDR=0 VDDO_LEVEL=3.3V
CONFIG 1	1	1	PHYADDR=1 VDDO_LEVEL=3.3V
CONFIG 1	0	0	PHYADDR=0 VDDO_LEVEL=2.5V
CONFIG 0	1	1	PHYADDR=1 VDDO_LEVEL=2.5V

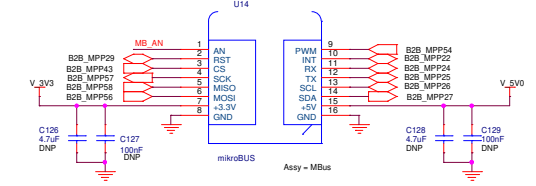
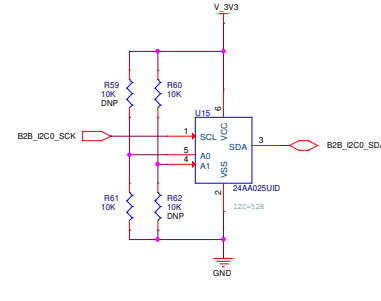
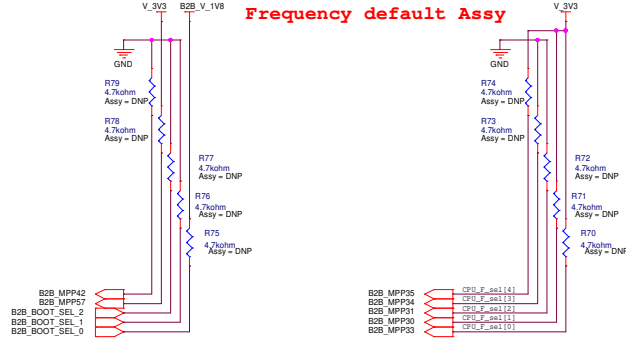
1. PHYADD=1
2. VDDO=3.3V

* This is valid only for 88E1512/88E1512C. For 88E1512, the VDDO_LEVEL is forced to 1V8, hence the 01 mapping for VDDO_LEVEL is ignored.





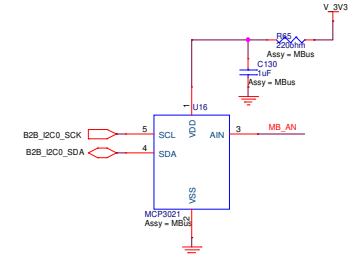
Frequency default Assy



PART NO.	XX	X	/XX
Device	Address Options	Temperature Range	Package

Device:	MCP3021T: 10-Bit 2-Wire Serial A/D Converter (Tape and Reel)																																													
Temperature Range:	E = -40°C to +125°C																																													
Address Options:	<table><tr><th></th><th>XX</th><th>A2</th><th>A1</th><th>A0</th></tr><tr><td>A0</td><td>=</td><td>0</td><td>0</td><td>0</td></tr><tr><td>A1</td><td>=</td><td>0</td><td>0</td><td>1</td></tr><tr><td>A2</td><td>=</td><td>0</td><td>1</td><td>0</td></tr><tr><td>A3</td><td>=</td><td>0</td><td>1</td><td>1</td></tr><tr><td>A4</td><td>=</td><td>1</td><td>0</td><td>0</td></tr><tr><td>A5 *</td><td>=</td><td>1</td><td>0</td><td>1</td></tr><tr><td>A6</td><td>=</td><td>1</td><td>1</td><td>0</td></tr><tr><td>A7</td><td>=</td><td>1</td><td>1</td><td>1</td></tr></table>		XX	A2	A1	A0	A0	=	0	0	0	A1	=	0	0	1	A2	=	0	1	0	A3	=	0	1	1	A4	=	1	0	0	A5 *	=	1	0	1	A6	=	1	1	0	A7	=	1	1	1
	XX	A2	A1	A0																																										
A0	=	0	0	0																																										
A1	=	0	0	1																																										
A2	=	0	1	0																																										
A3	=	0	1	1																																										
A4	=	1	0	0																																										
A5 *	=	1	0	1																																										
A6	=	1	1	0																																										
A7	=	1	1	1																																										
	* Default option. Contact Microchip factory for other address options																																													
Package:	OT = SOT-23, 5-lead (Tape and Reel)																																													

- Examples:
- MCP3021A0T-EJOT: Extended, A0 Address, Tape and Reel
 - MCP3021A1T-EJOT: Extended, A1 Address, Tape and Reel
 - MCP3021A2T-EJOT: Extended, A2 Address, Tape and Reel
 - MCP3021A3T-EJOT: Extended, A3 Address, Tape and Reel
 - MCP3021A4T-EJOT: Extended, A4 Address, Tape and Reel
 - MCP3021A5T-EJOT: Extended, A5 Address, Tape and Reel
 - MCP3021A6T-EJOT: Extended, A6 Address, Tape and Reel
 - MCP3021A7T-EJOT: Extended, A7 Address, Tape and Reel

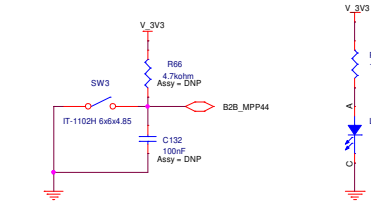
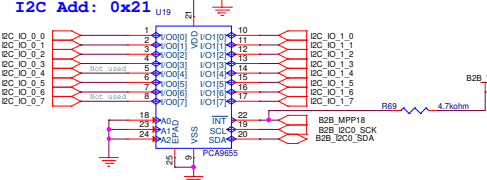


PCA9555 16-bit I2C and SMBus I/O port with interrupt 5V tolerant I/Os

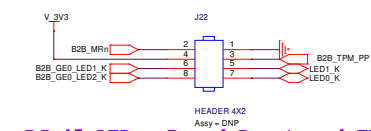
I/O port: When an I/O is configured as an input, PTEs Q1 and Q2 are off, creating a high impedance input with a weak pull-up to VDD. The input voltage may be raised above VDD to a maximum of 5.5 V.

I/O Table

I2C Add: 0x21
102_0
102_1
102_2
102_3
102_4
102_5
102_6
102_7
103_0
103_1
103_2
103_3
103_4
103_5
103_6
103_7



AP Reset Switch (GPIO)



RJ-45 LEDs, Board Reset and TPM_PP