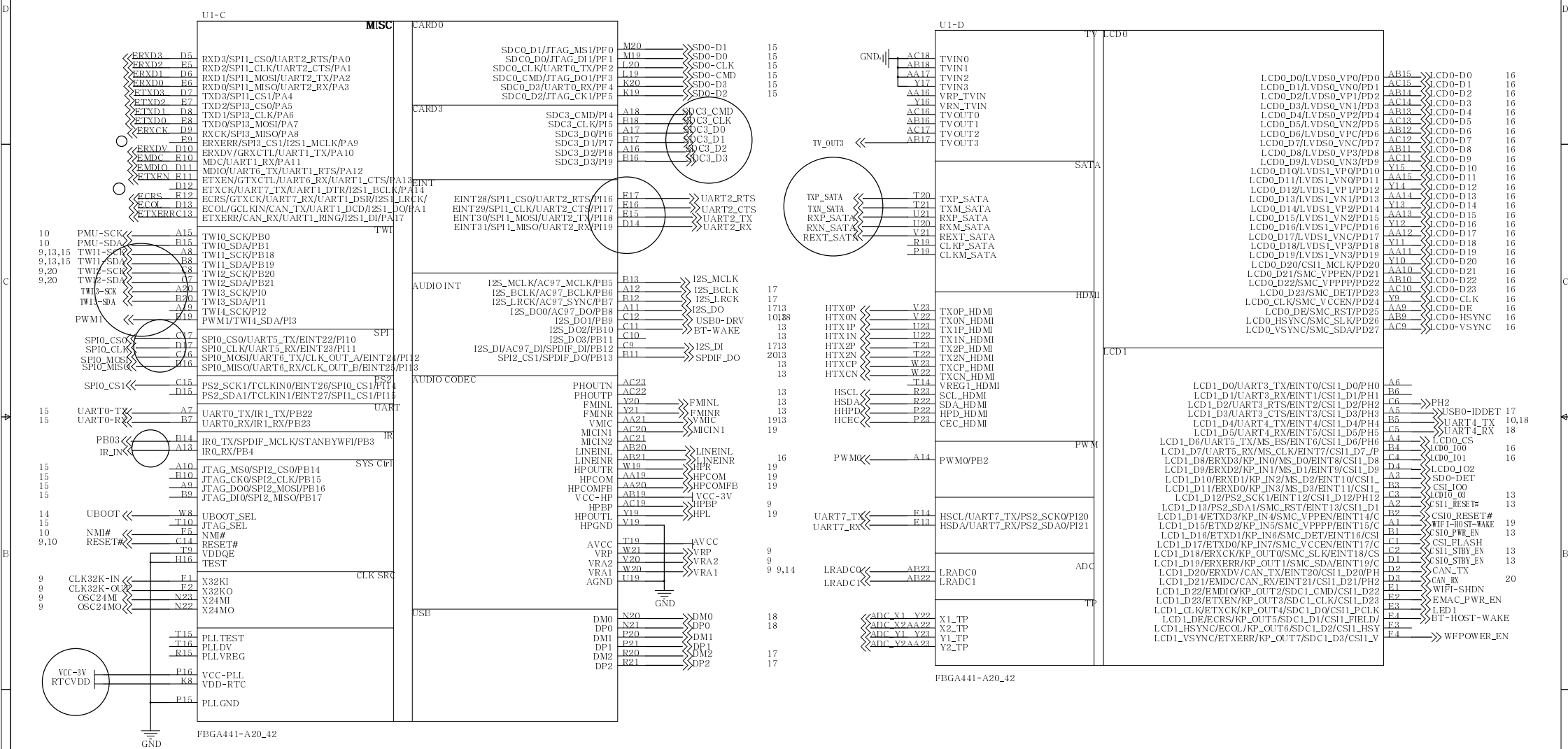
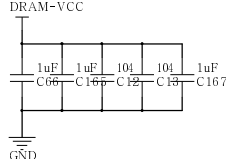
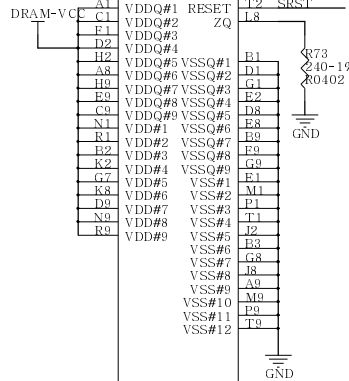
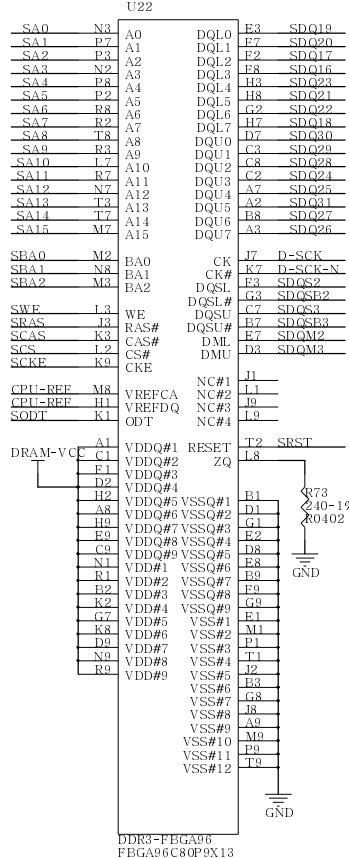
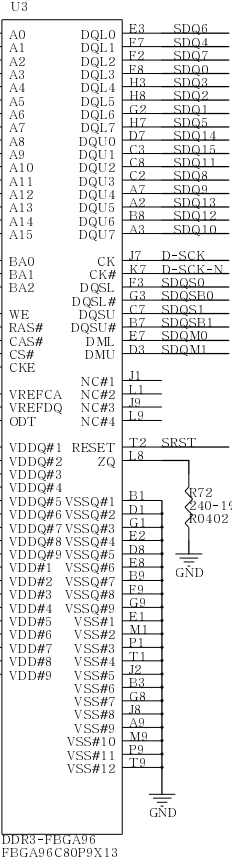
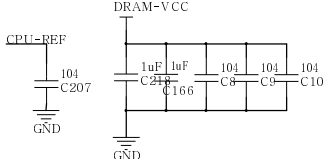
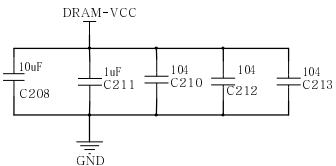
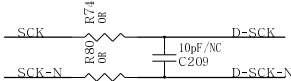
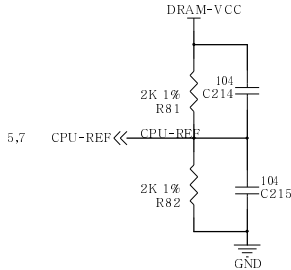
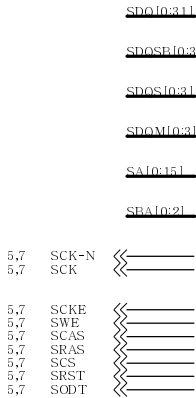


CPU2

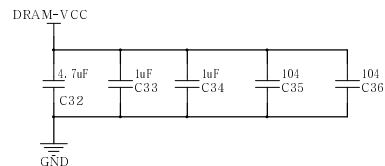


DDR3-16BITX2

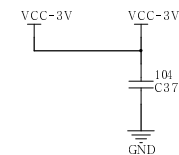


BESIDE CPU

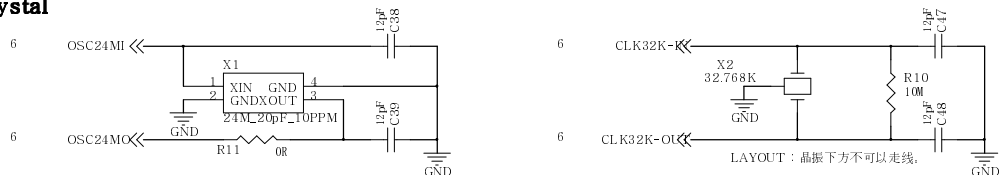
DRAM



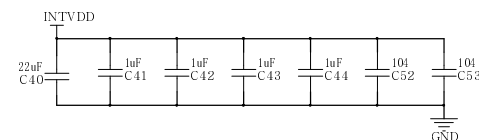
PLL



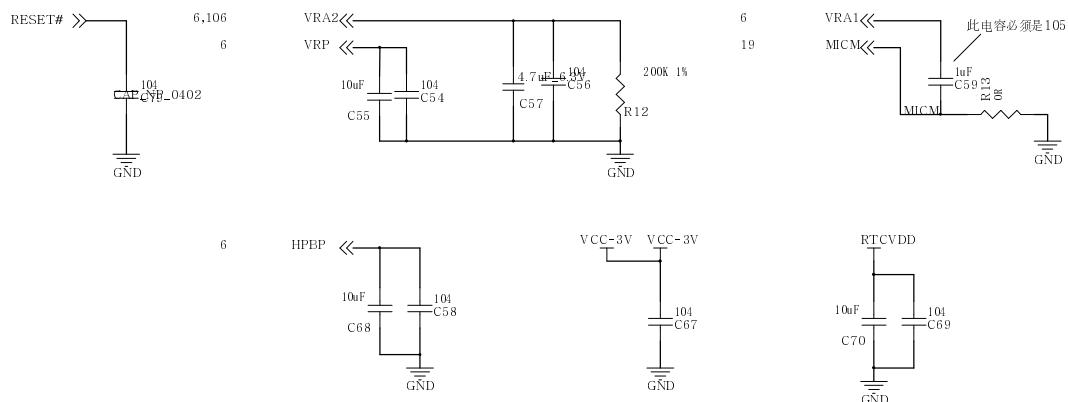
Crystal



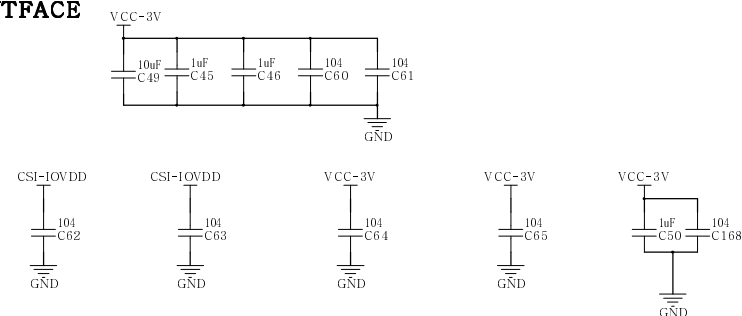
CORE



AUDIO&SYS&TP

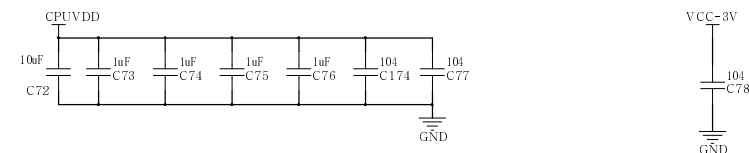


PIO-INTFACE

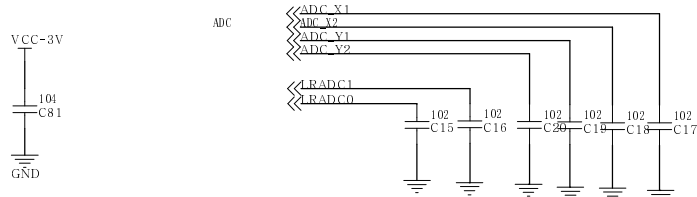


CPU&TV

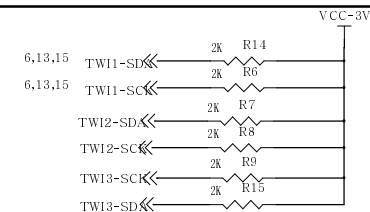
LAYOUT: 按照一个PIN, 放一个电容, 并且尽量靠近PIN摆放.



USB



TWI-PULLUP



SATA



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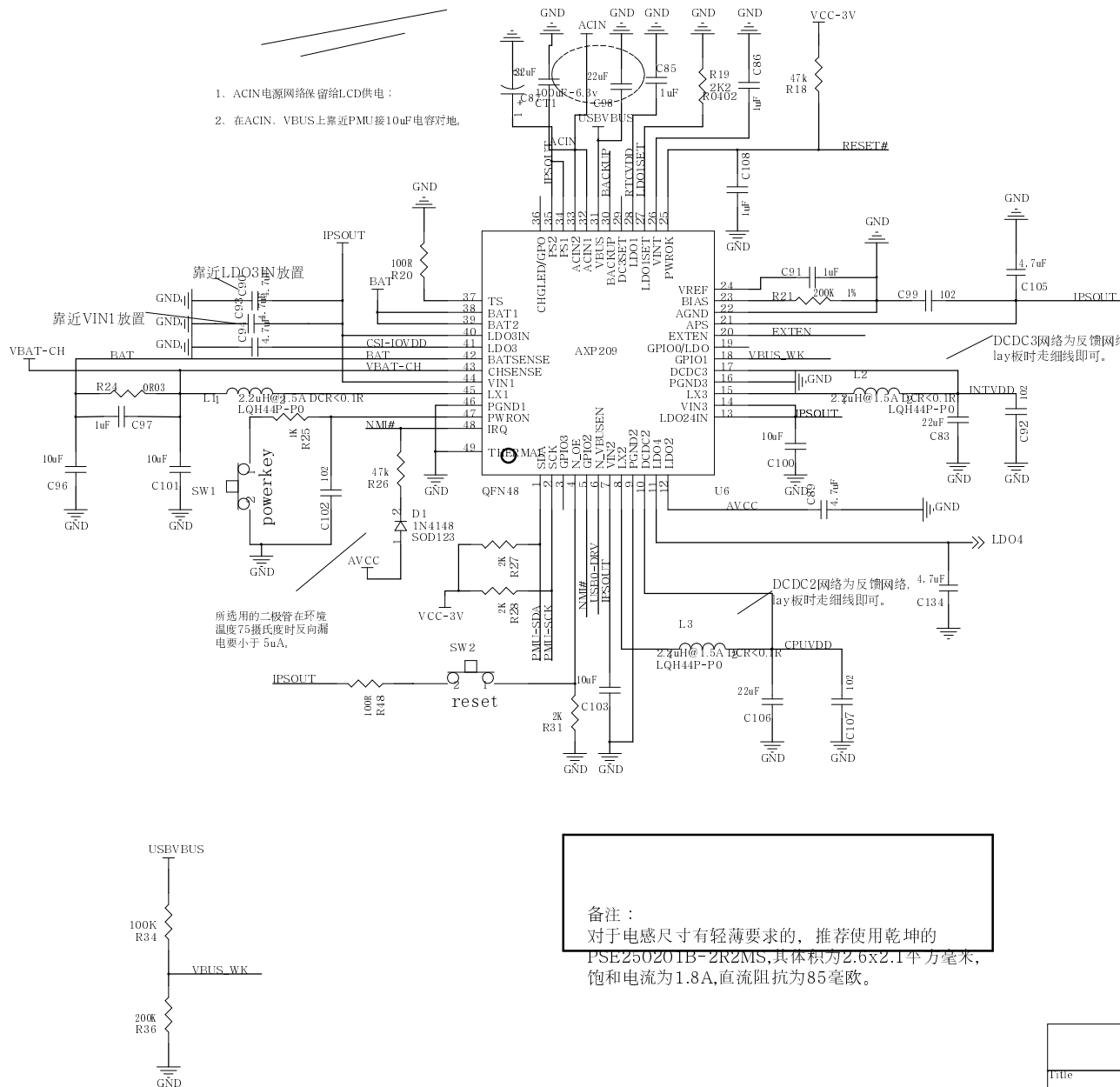
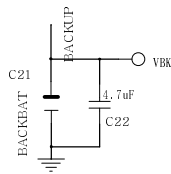
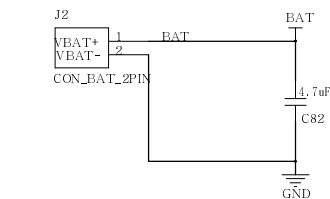
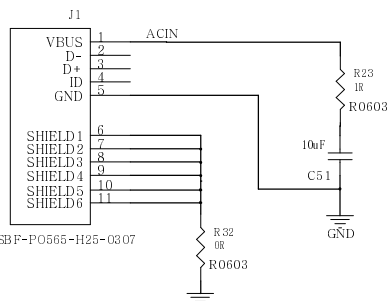


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POWER-PMU

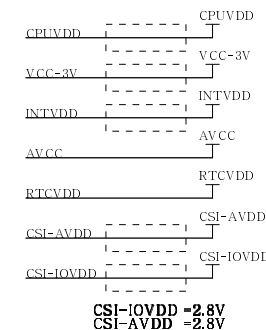
6	NMI#	PMU-SDA
6	PMU-SDA	PMU-SCK
6	PMU-SCK	
6,18	USB0-DR	USB0-DRV
11	EXTEN	EXTEN
6,9	RESET#	RESET#
18	USBVBUS	USBVBUS

POWER INPUT



POWER LINE:Width>=80mil

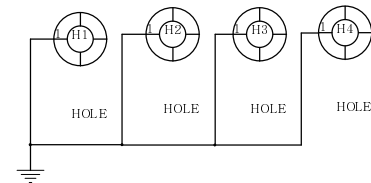
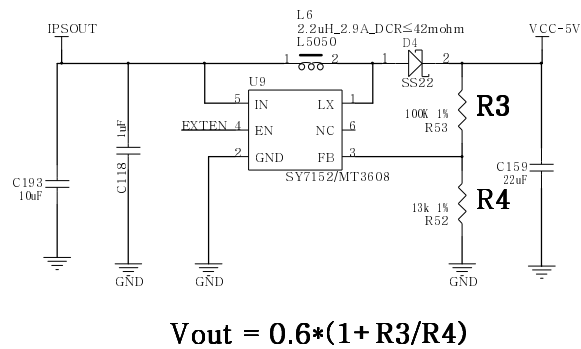
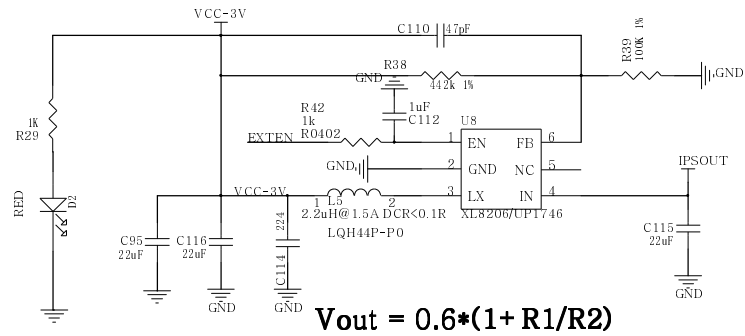
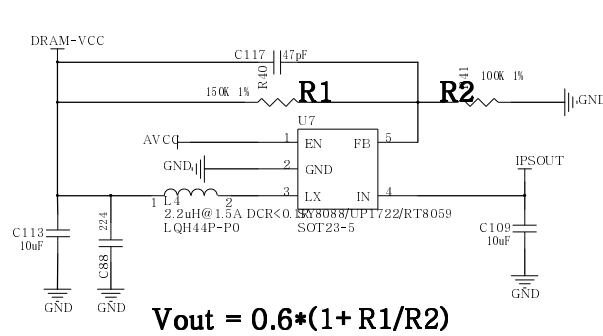
POWER LINE:Width>=40mil



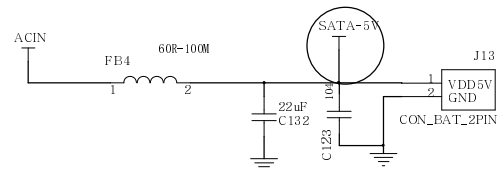
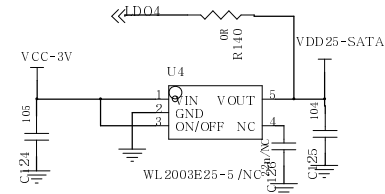
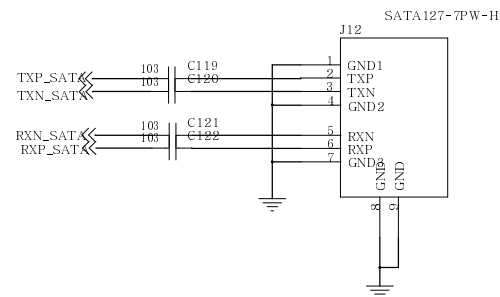
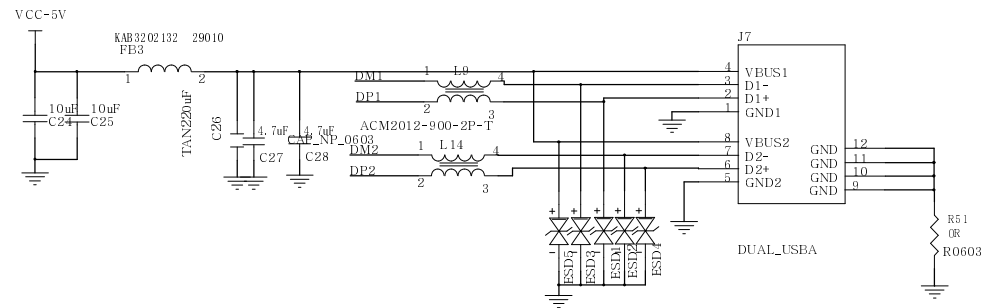
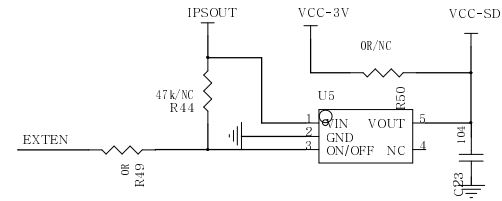
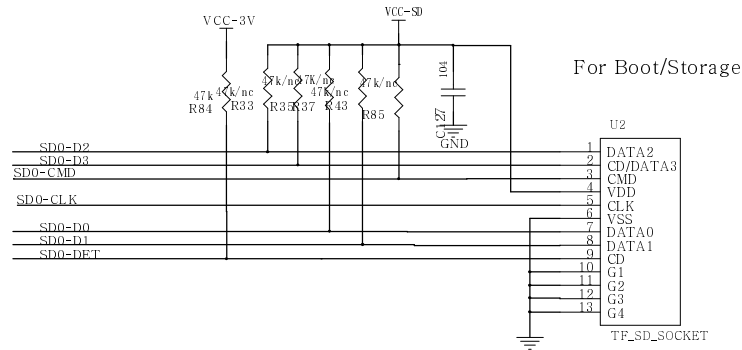
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Banana Pro		
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A3	POWER1	1.1
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POWER-DC/DC

EXTEN» EXTEN 10

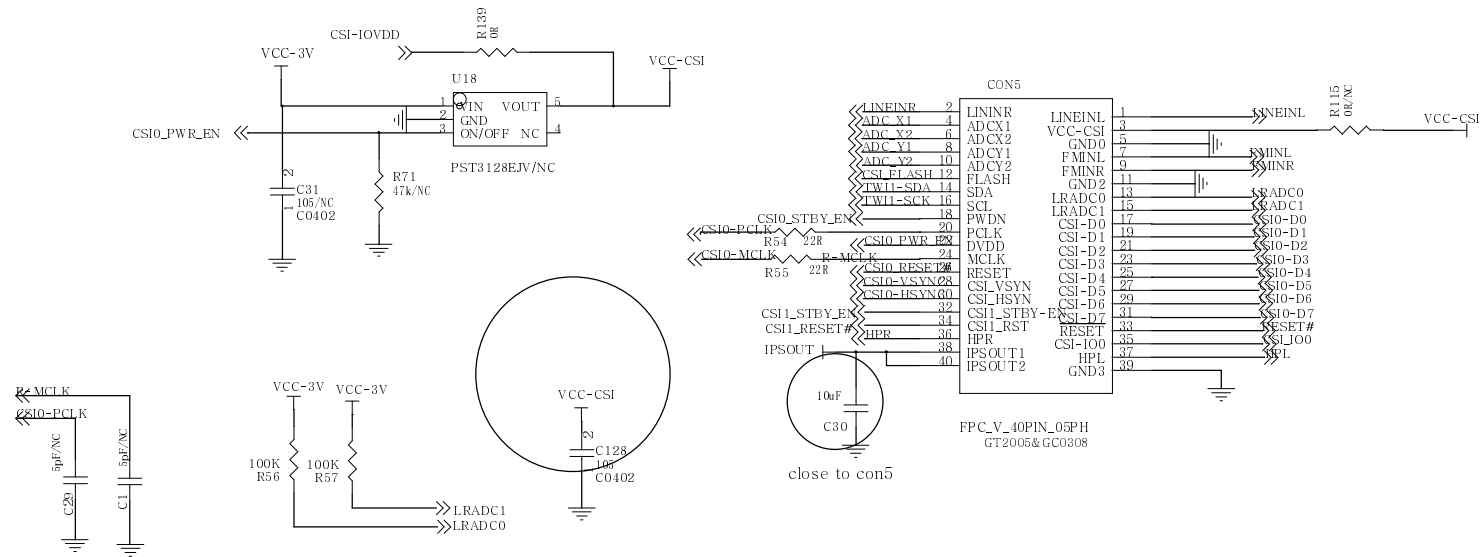
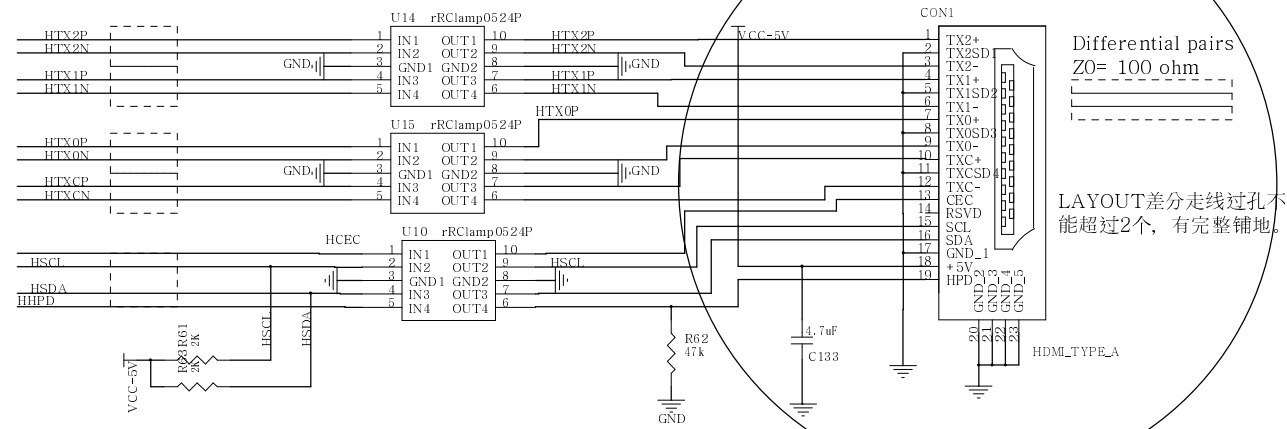
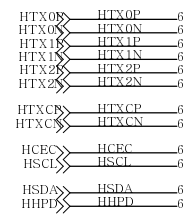


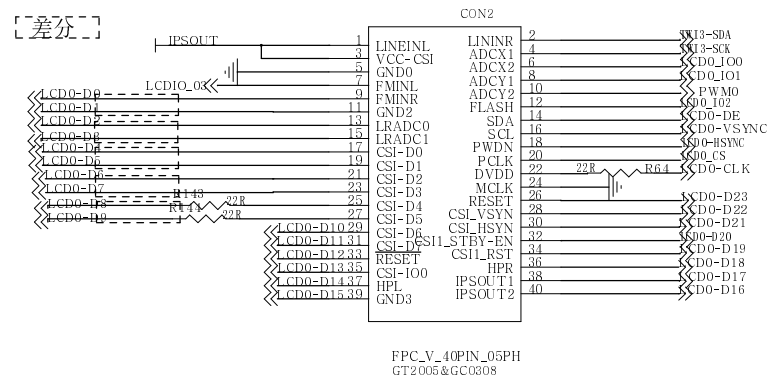
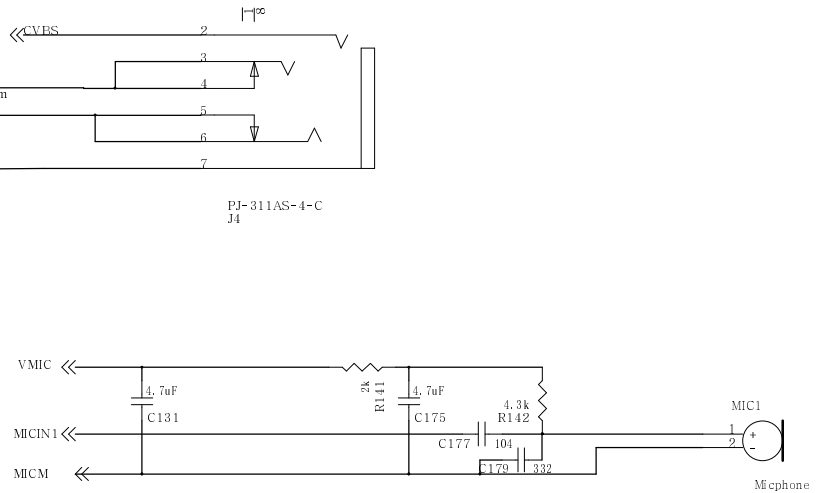
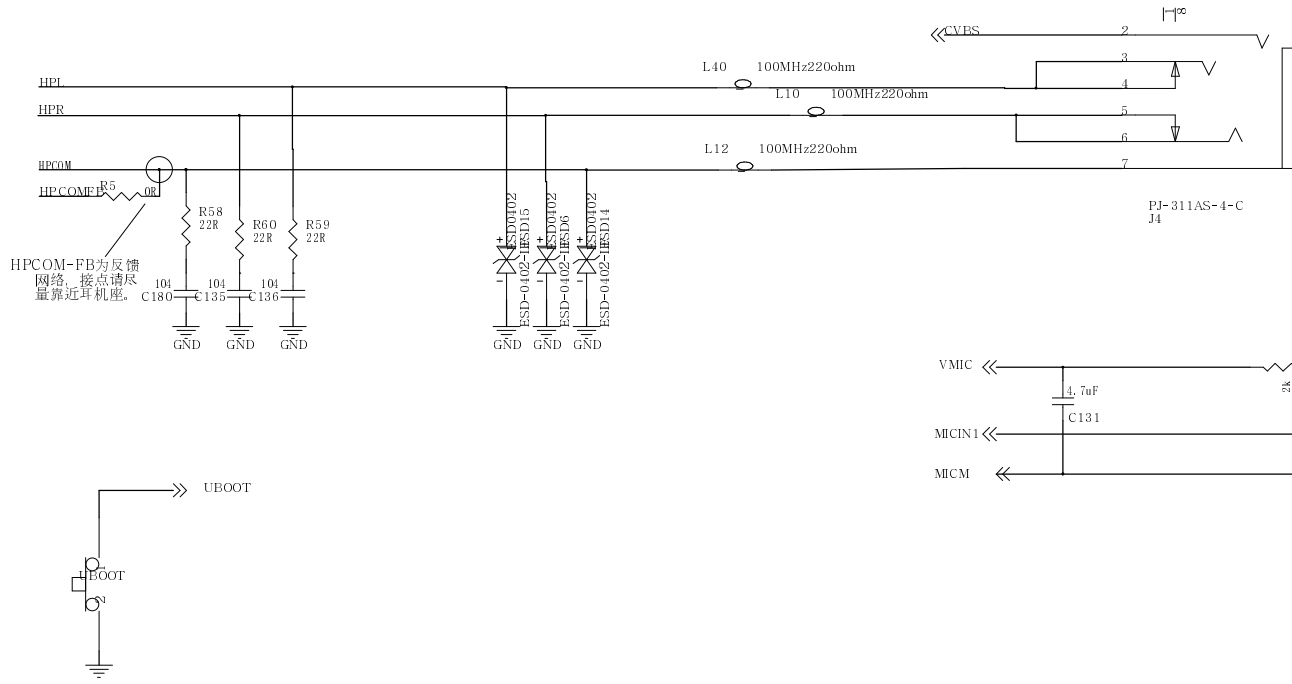
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A3		Rev 1.1	
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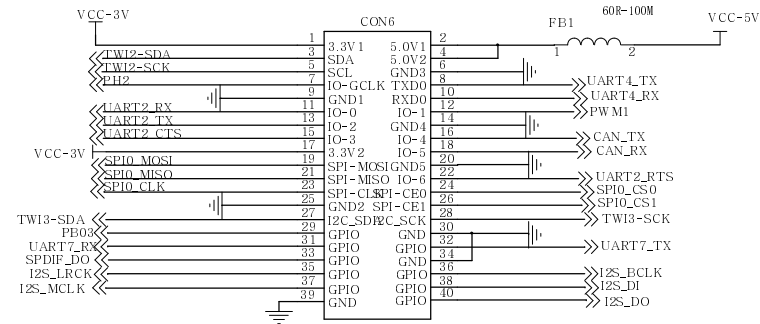
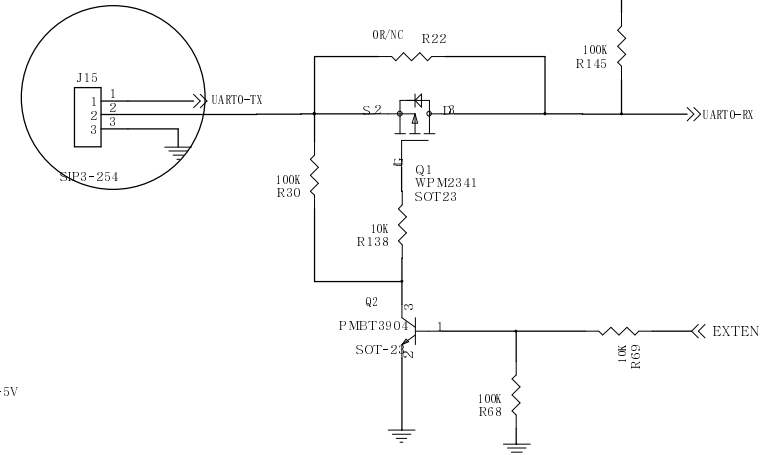
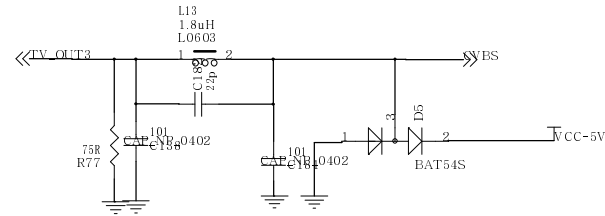
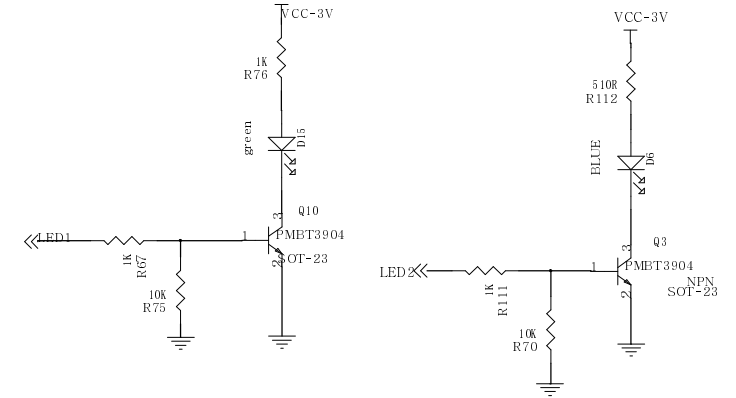
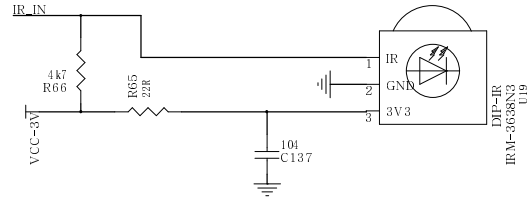


HDMI-CSI

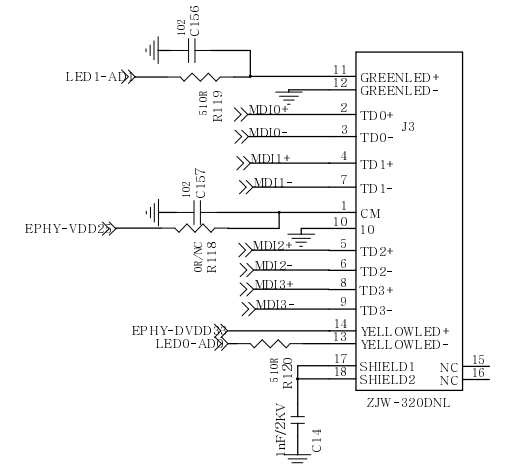
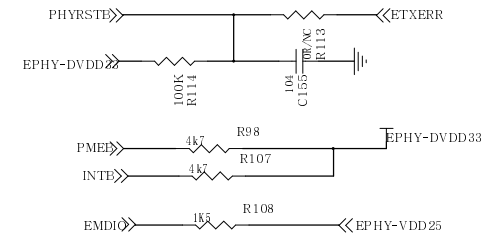
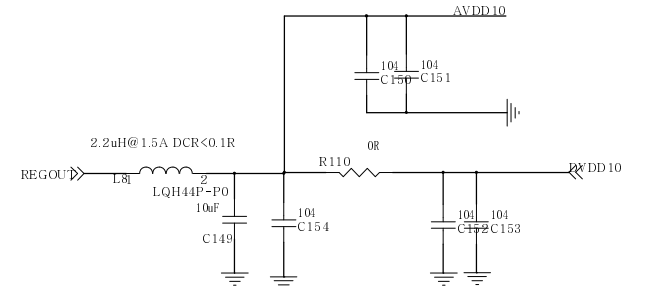
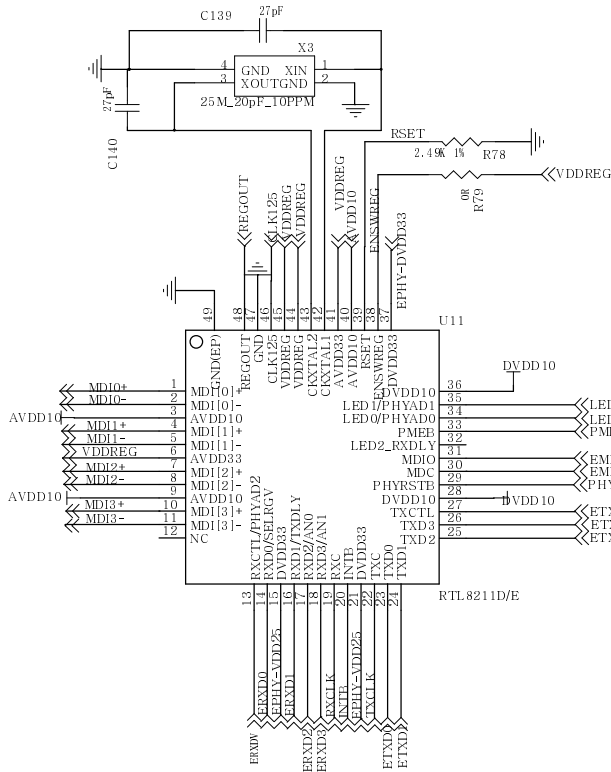
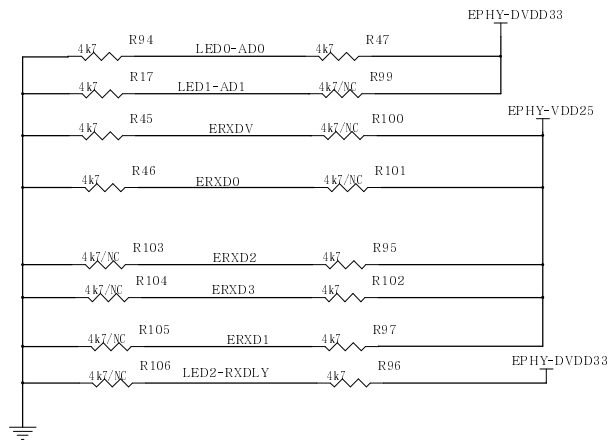
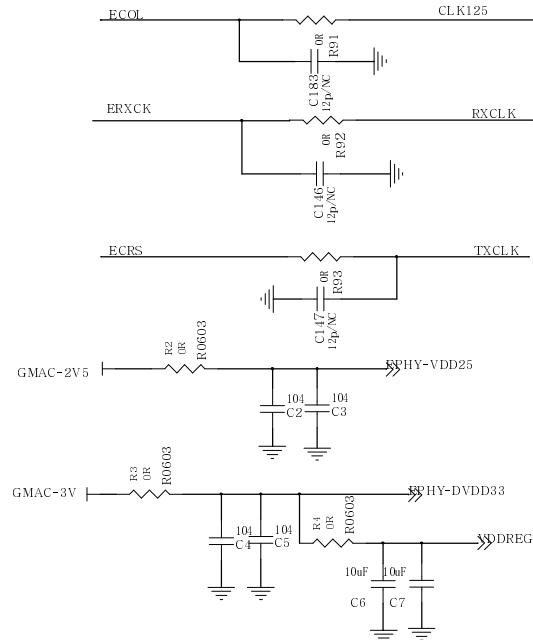
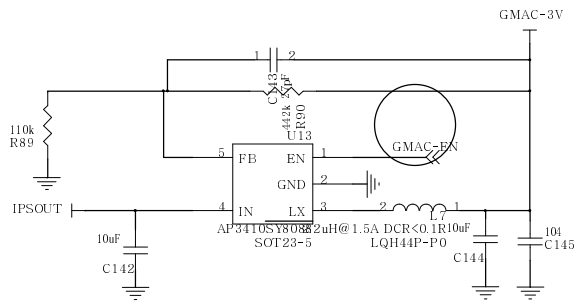
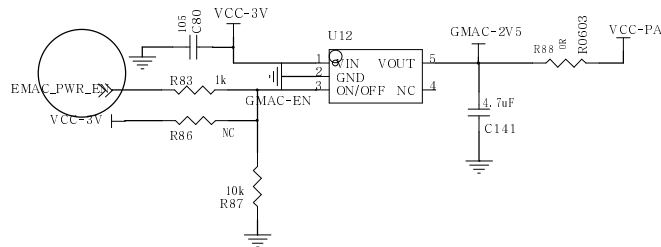
HDMI



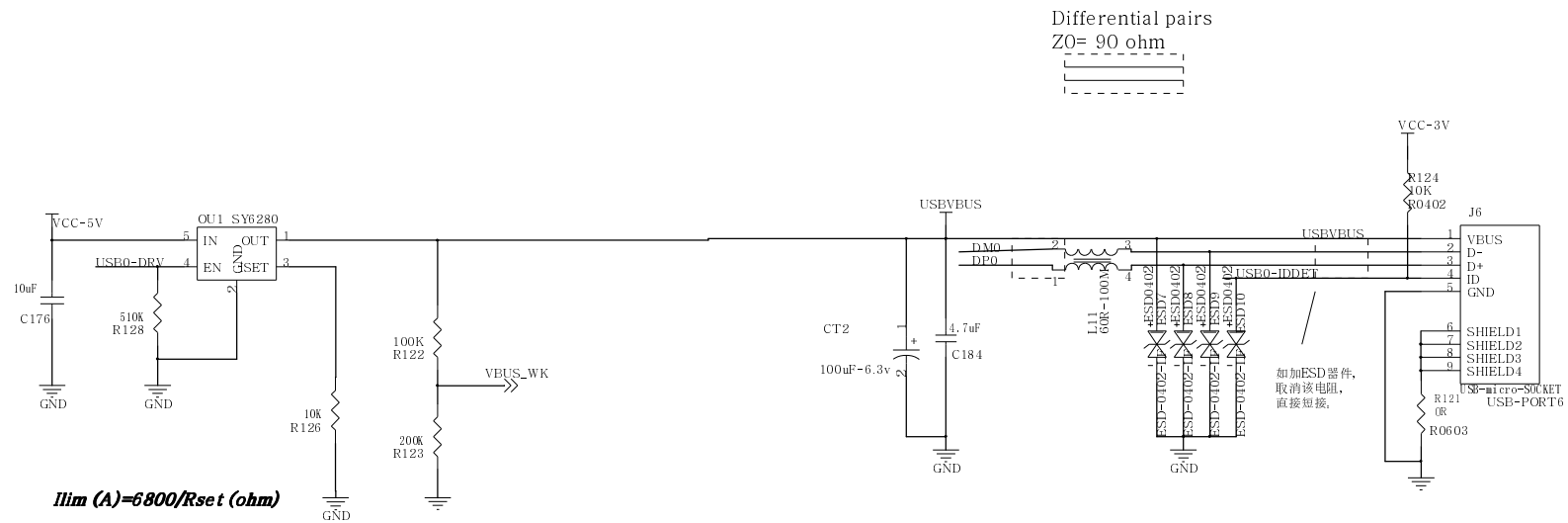




DIP40-254



USB-USB OTG

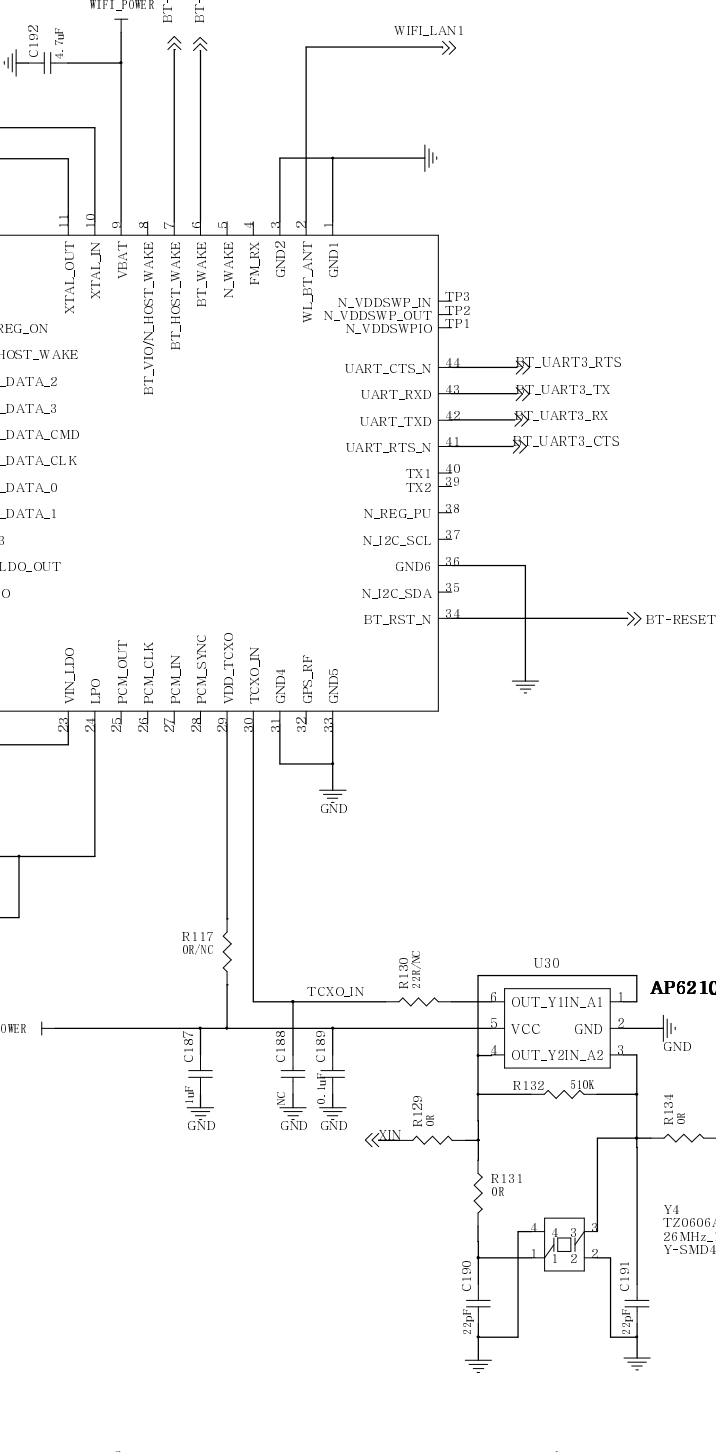
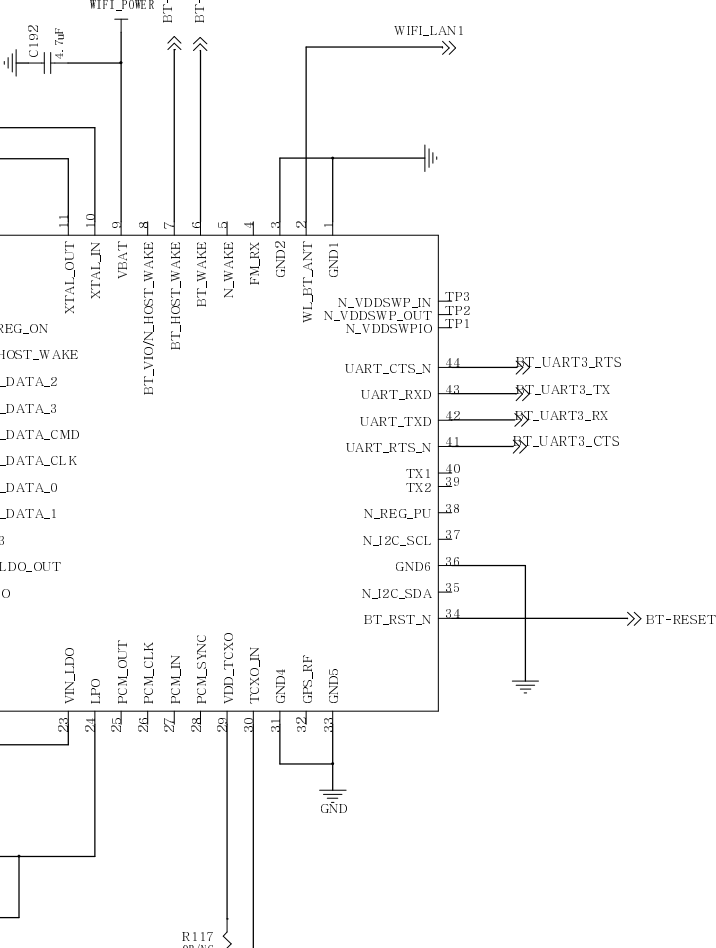
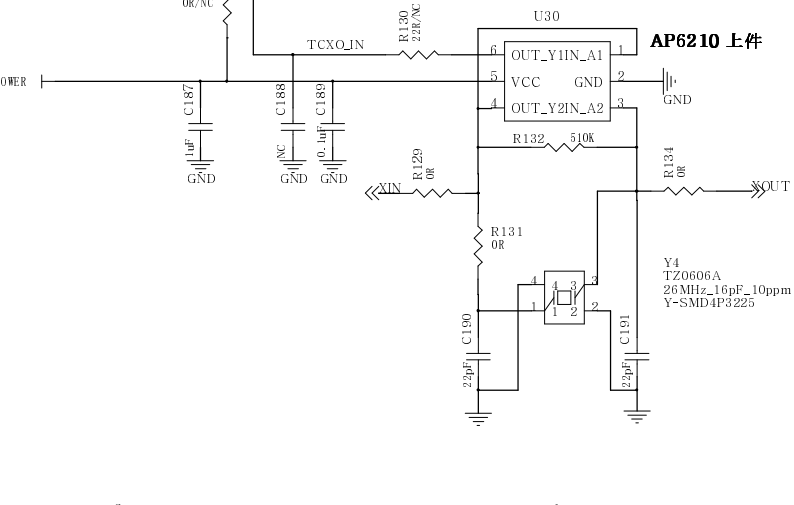
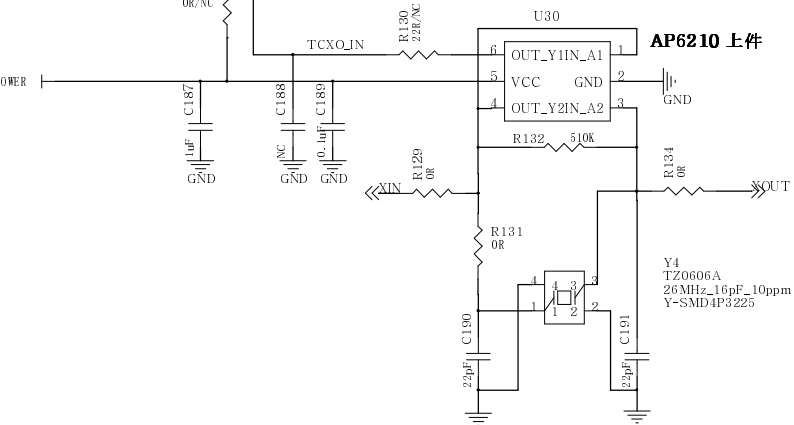
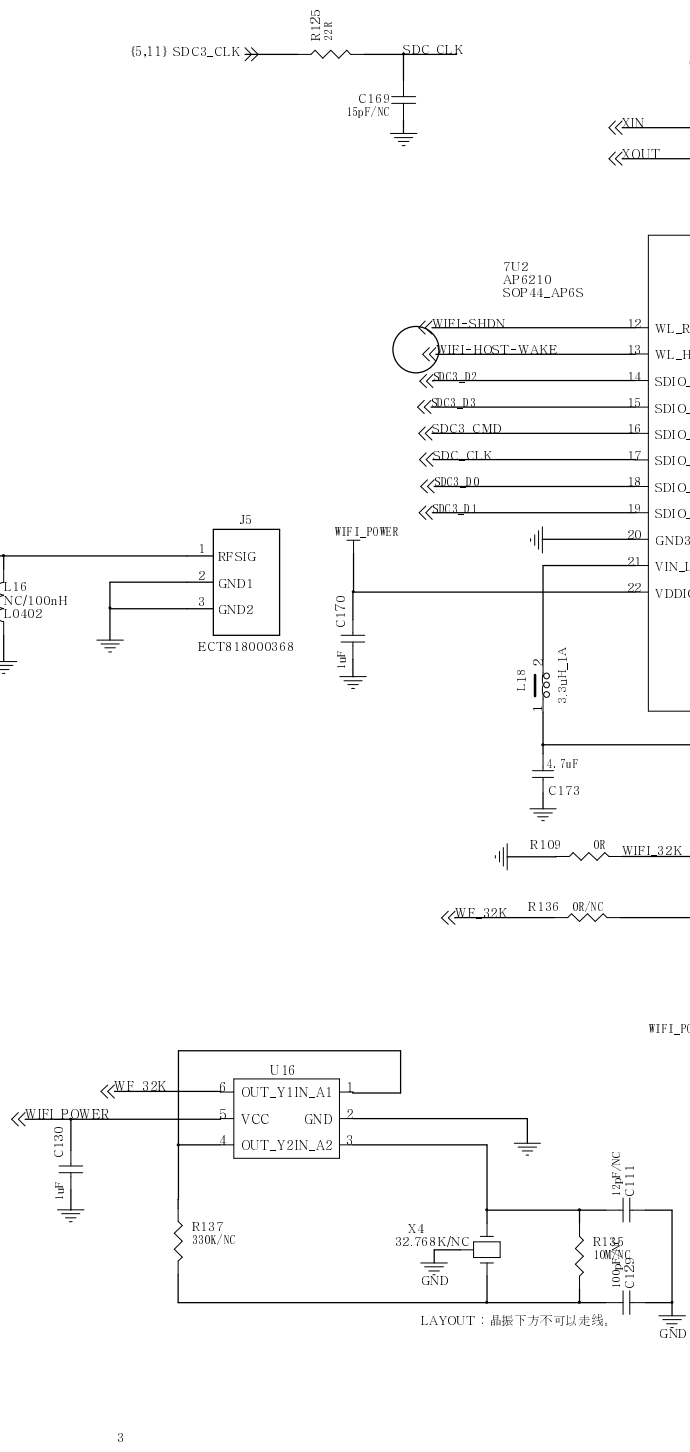
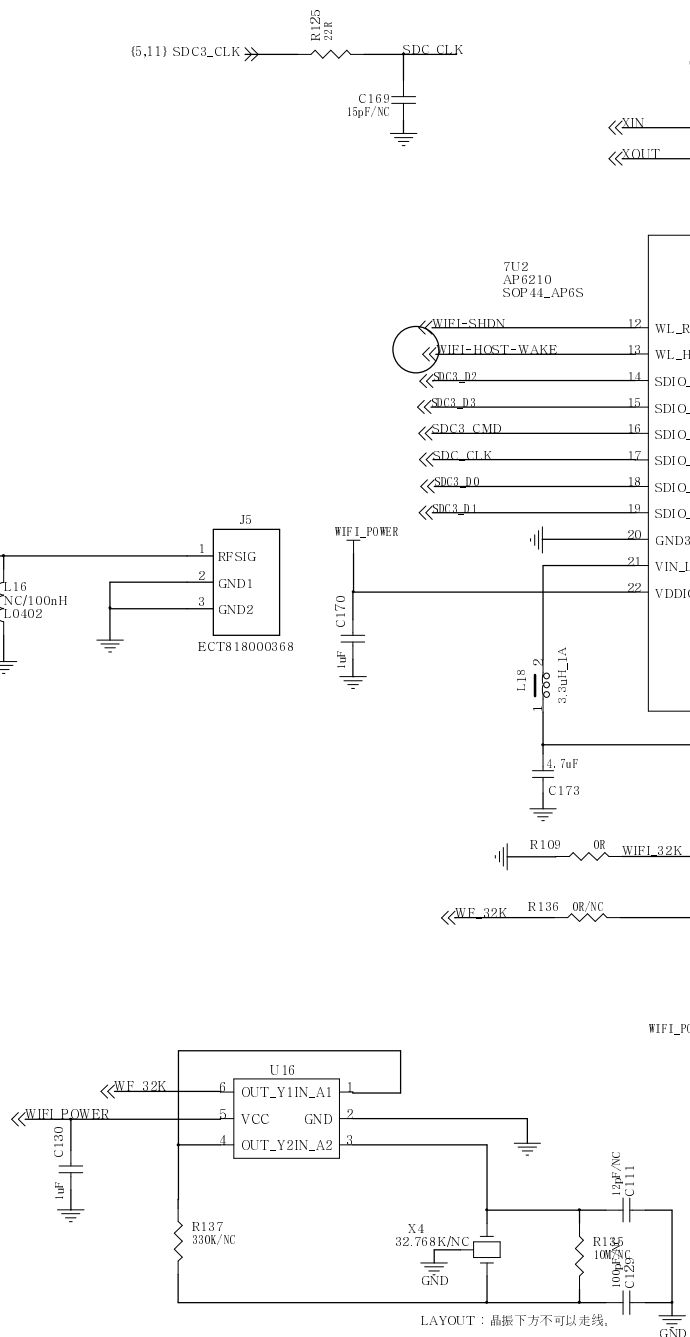
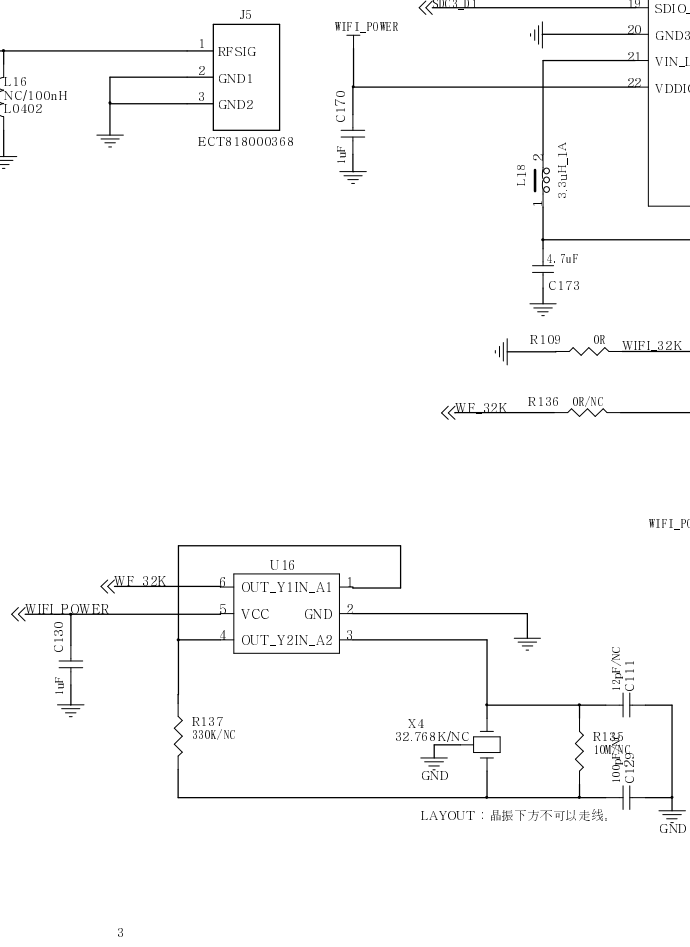
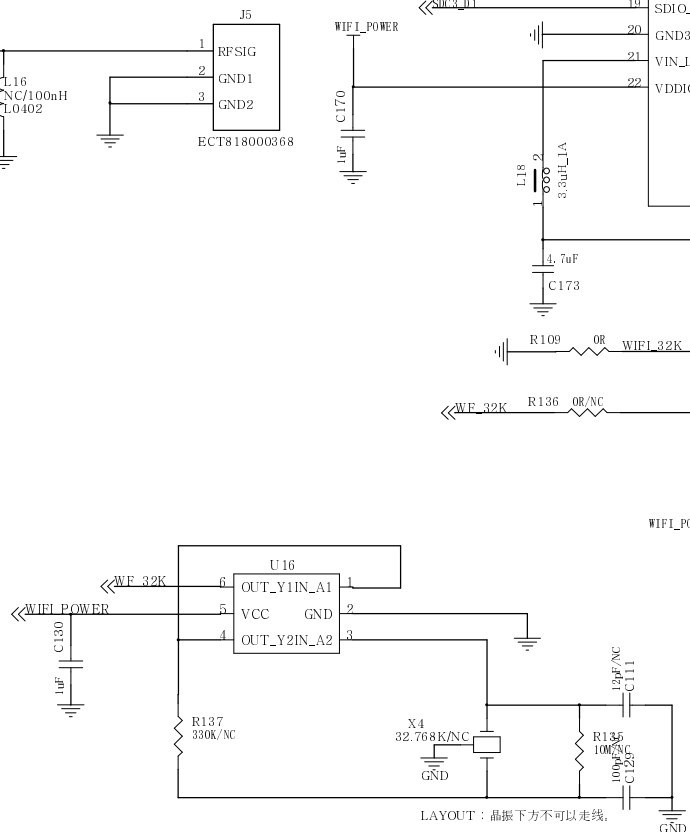
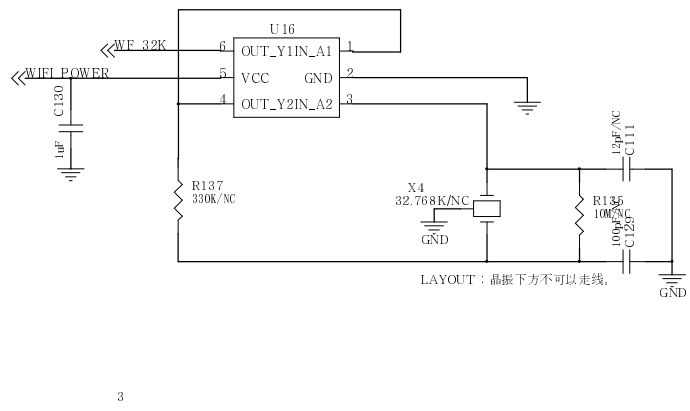
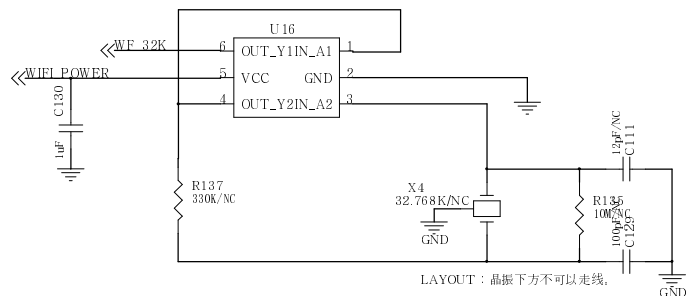
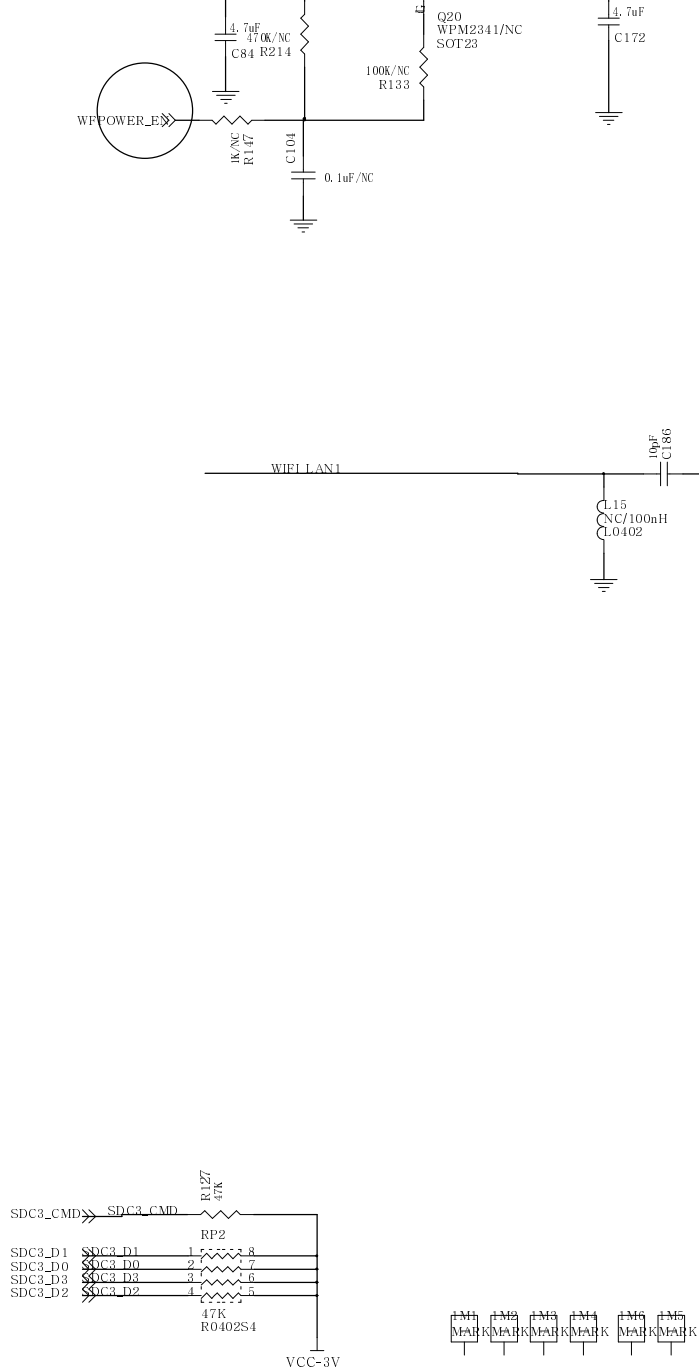
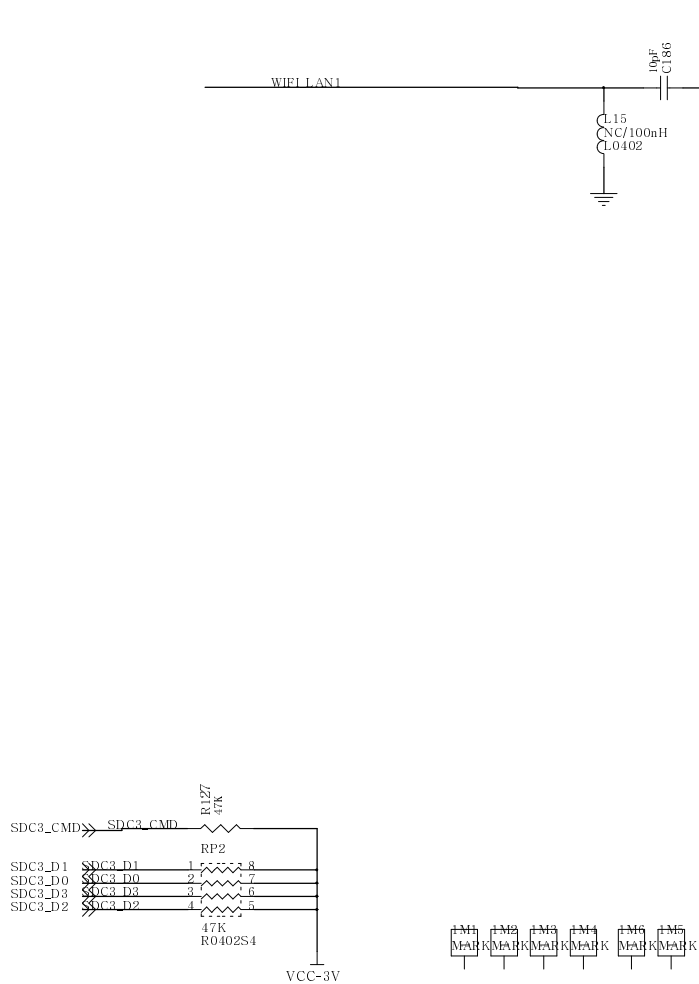
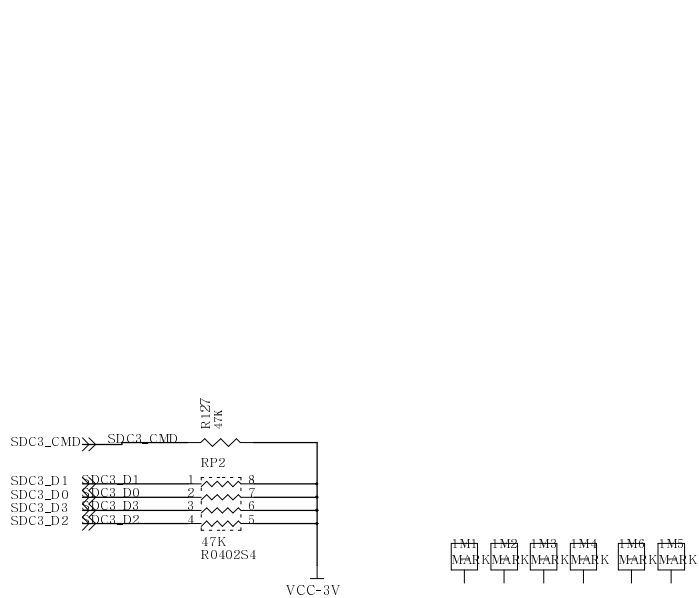
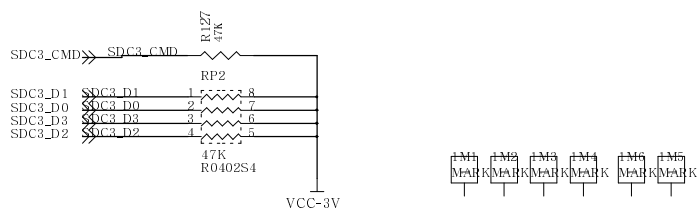


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Banana Pro

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20140916:change Version to V1.1

20140916: change HDMI to Type A

20140916: Add a GND pin to J15

20140917: change RP2 to 47K R127 to 47K change wifi SDIO to SDC3

20140917: change C1,C29 to NC

20140917: change R83 to 1K U12 power supply changed to VCC-3V

20140917: change CON26 PIN29 to PB03 D4 changed to SS22

20140917: change U8 to XL8206 2ADCDC

20141013: Change version to 1.2

20141013: add Q1 Q2 R30 R68 R69

20141013:change FB3 from 60R@100MHz to KAB3202132 29010 1.25A fuse

20141013:change R111 frpm 0R to 1K;R112 from 510R to 1K add R70 10K add Q3

20141013: add C30

20141013: add R71 R115 U18 C31

20141014: add C80 C84 R116

20141014: change C115 C116 C159 to 22uF, add c95

20141014:Change J4 add C104

20141014:Del E3,U16 R22;Add J5

20141014:Del ESP8089 SDIO wifi

20141014:change J1 to 创粤USBF-PO565-H25-0307

20141022: Add 32KHz signal circuit for broadcom wifi

20141024: change BT uart from uart4 to uart3(with flow control)

20141024: change BT uart from uart4 to uart3(with flow control)

20141028: Add C132 C133 change C128 to 105

20141028: Add R139 C134 R140

20141030: Add FB1 change L 6 to 5*5

20141022: Add 32KHz signal circuit for broadcom wifi