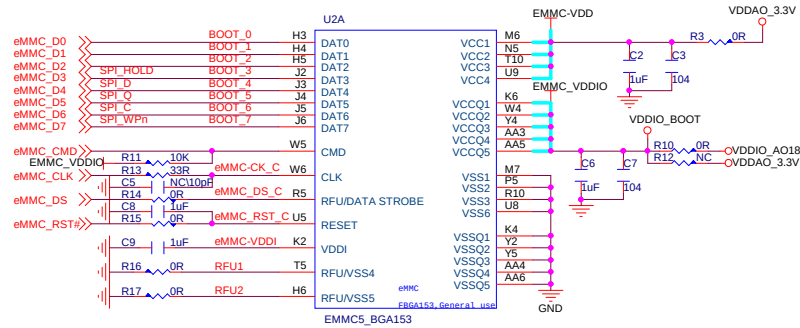
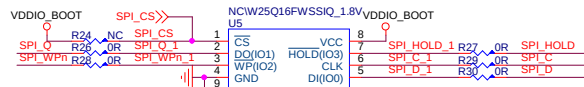


eMMC

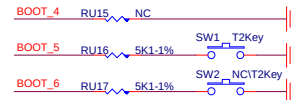


SPI Nor flash

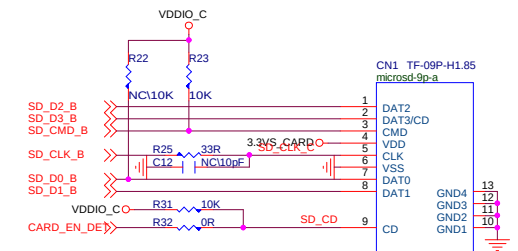
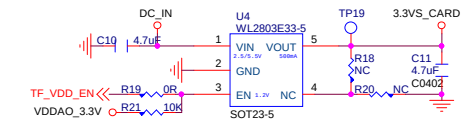
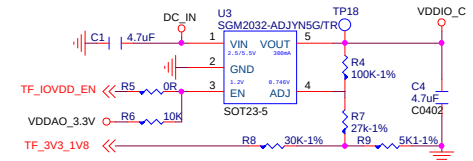


Power on config

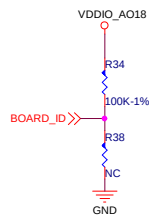
BOOT_4[NAND_D4]: 0= SPI NAND Boot first
 BOOT_5[NAND_D5]: 0= USB Boot first
 BOOT_6[NAND_D6]: 0= SPI NOR Boot first



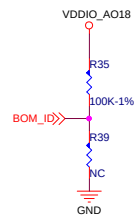
MicroSD



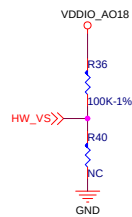
BOARD ID



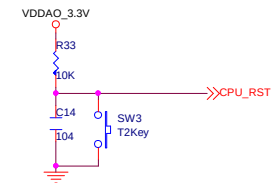
BOM ID



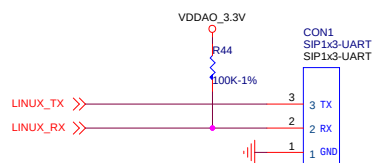
HW Ver



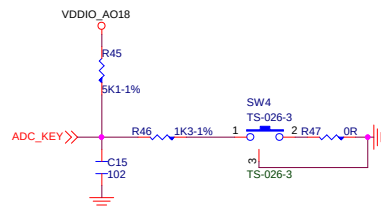
Reset



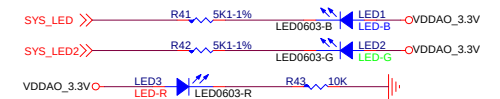
debug



ADC Key

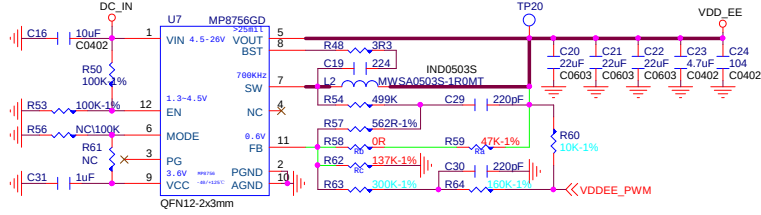


LEDs

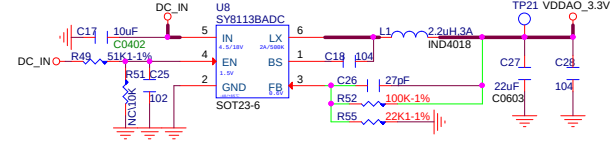


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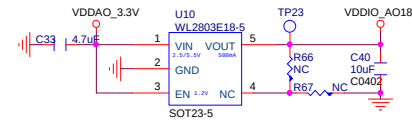
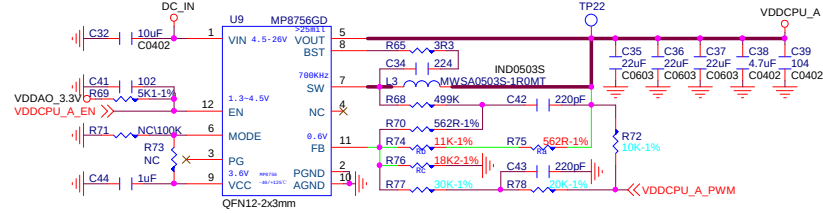
5V to VDD_EE: Max 5A



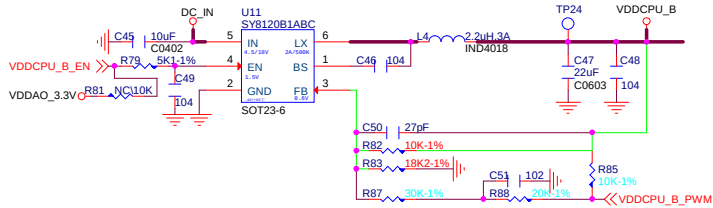
5V to VDDAO_3.3V: Max 3A



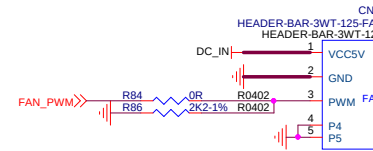
5V to VDDCPU_A: Max 5A



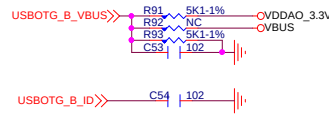
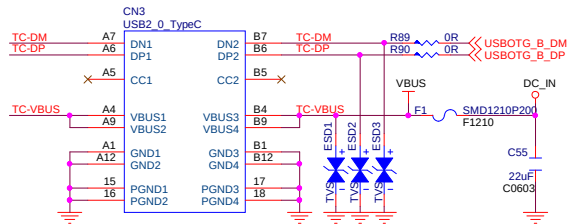
5V to VDDCPU_B: Max 2A



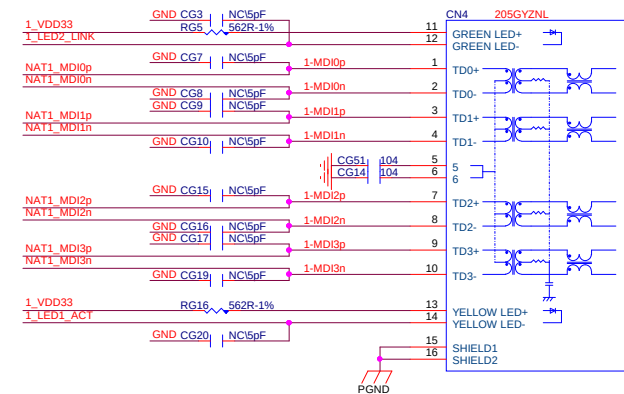
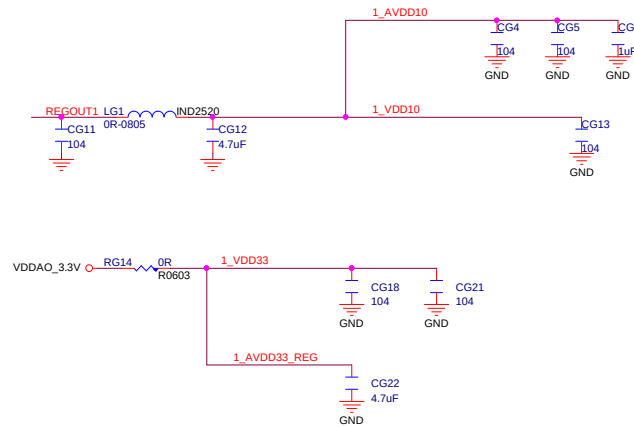
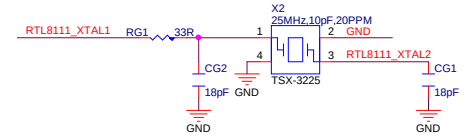
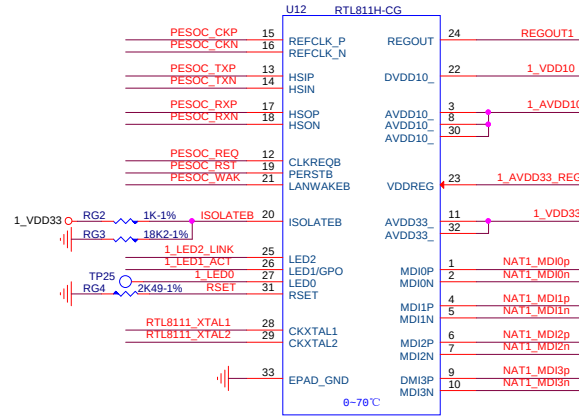
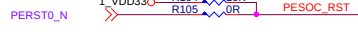
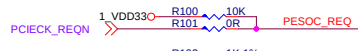
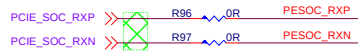
FAN



TypeC



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HDMI_SDA	↔	HDMI_SDA
HDMI_SCL	↔	HDMI_SCL
HDMI_HPD	↔	HDMI_HPD
HDMI_CEC	↔	HDMI_CEC
HDMI_TXCP	↔	TX_CK
HDMI_TXCN	↔	TX_CKN
HDMI_TXOP	↔	TX_DOP
HDMI_TXON	↔	TX_DON
HDMI_TX1P	↔	TX_D1P
HDMI_TX1N	↔	TX_D1N
HDMI_TX2P	↔	TX_D2P
HDMI_TX2N	↔	TX_D2N

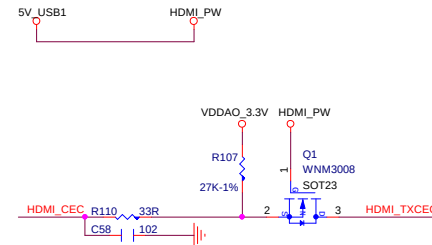
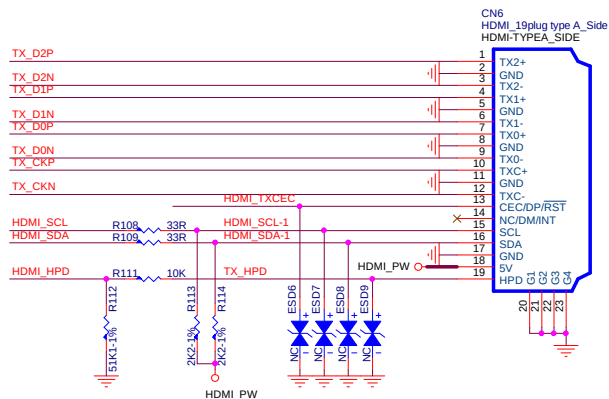
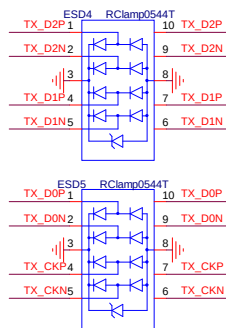
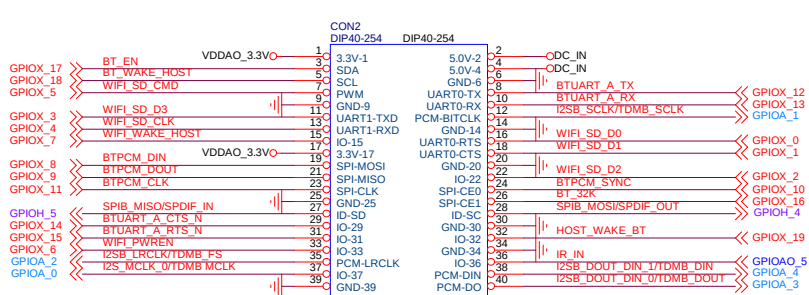
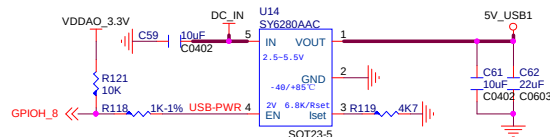
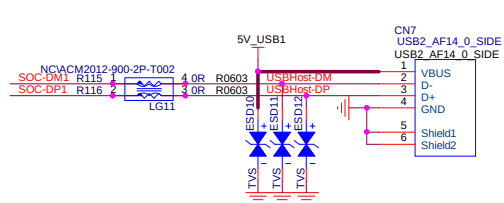


Diagram showing the connection of USB signals to the SOC:

- USB_A_DP is connected to SOC-DP1.
- USB_A_DM is connected to SOC-DM1.



MIPI TX

