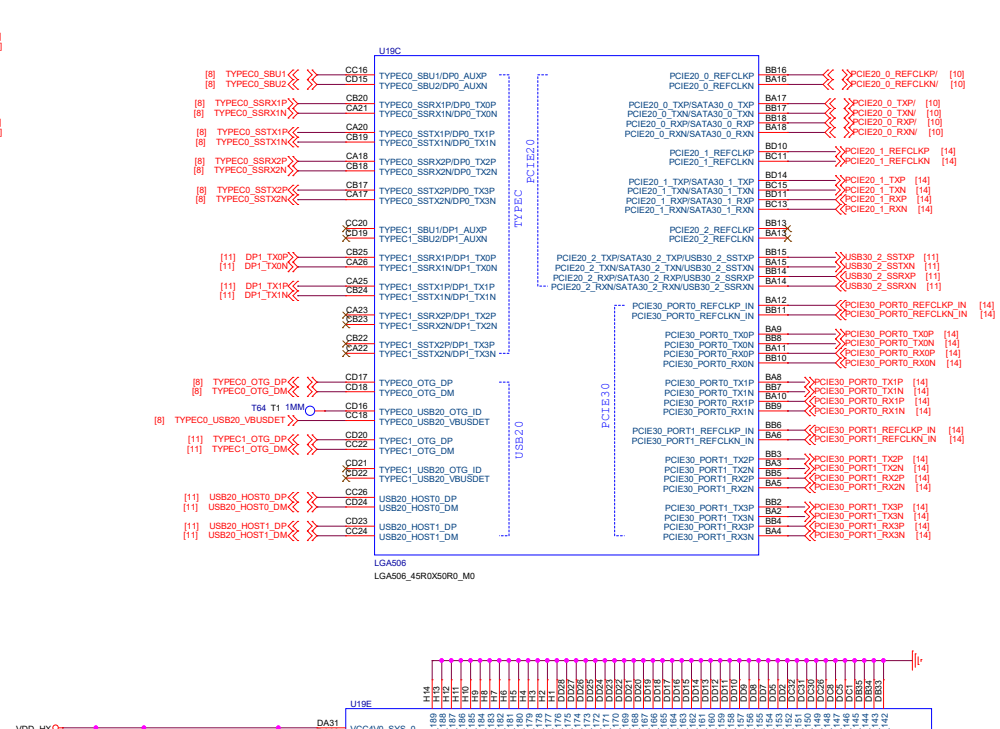
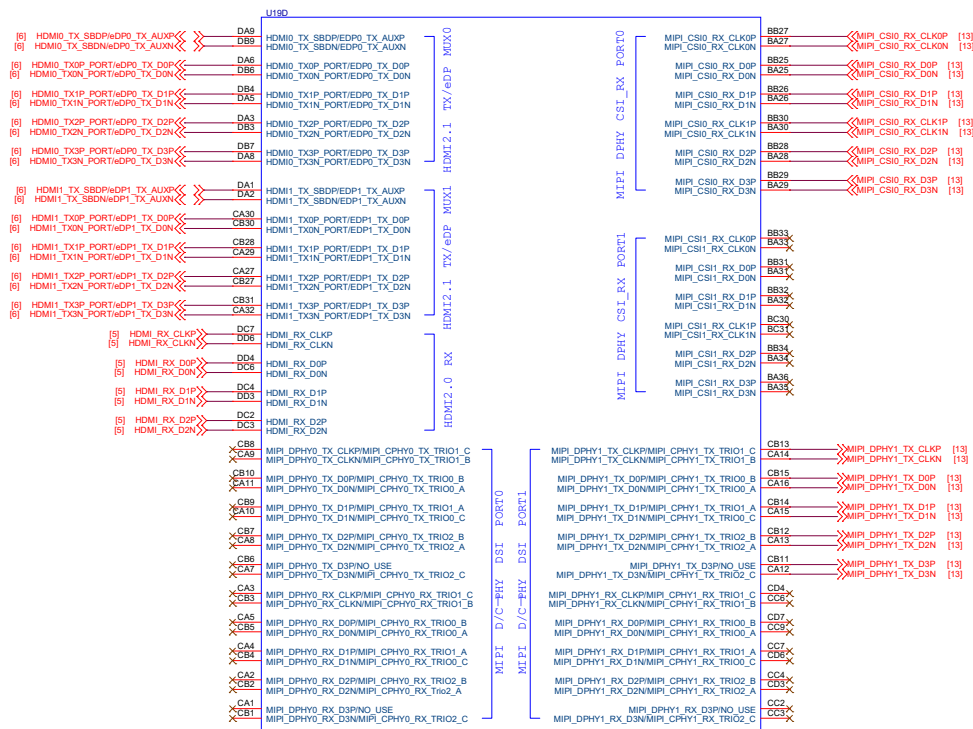


Revision V1.1:

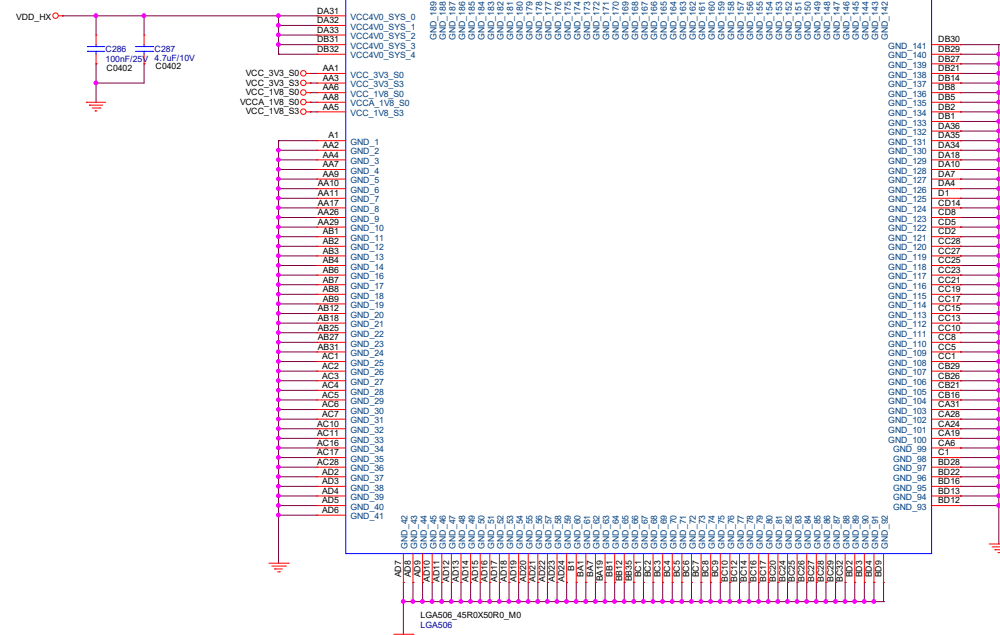
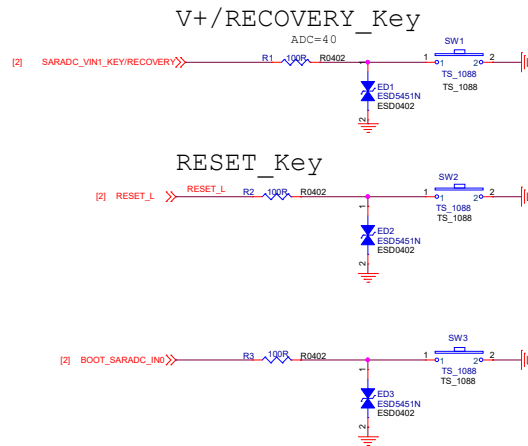
- 1.PCBPackage change: USBx2=J8, touch switch=SW1/SW2/SW3, TF Card=J16,Typec=J20;
- 2.PCBPackage change: Debug CON=J10, 32.768KHz=X1; Shield=J13;
- 3.Fan =J11 change to 3 line;

Revsion V1.2:

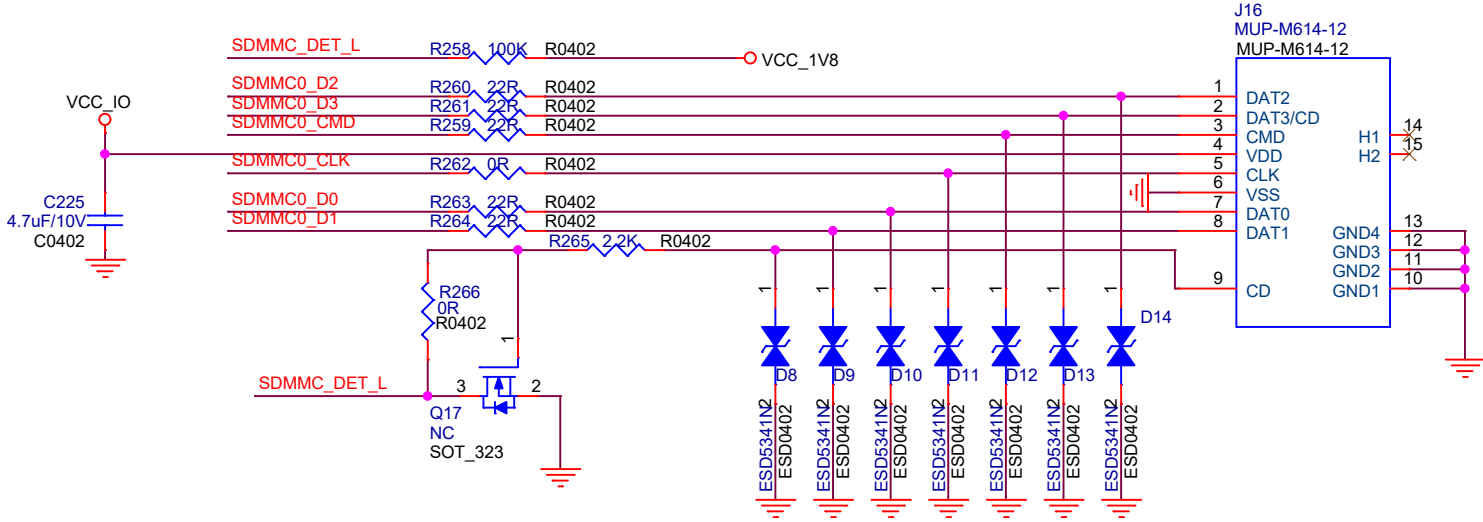
- 1.add uart2 protect circuit; R410=51K change to NC;



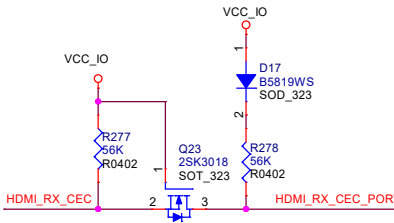
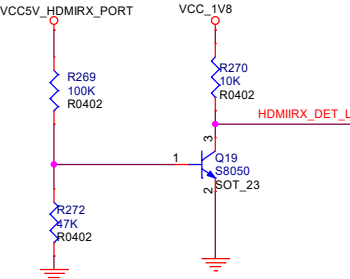
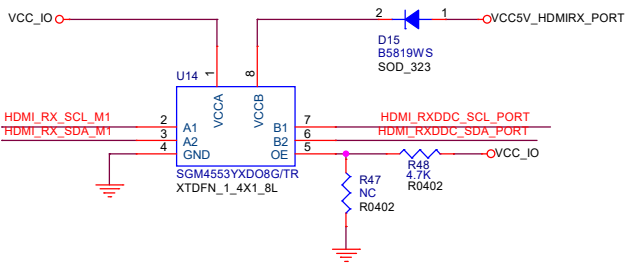
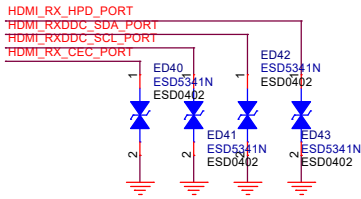
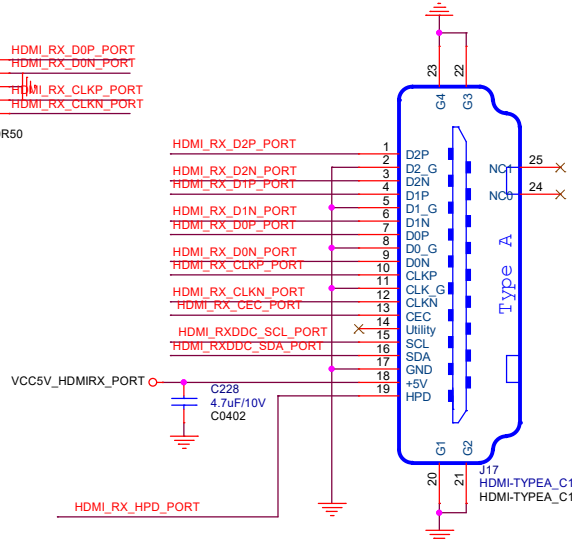
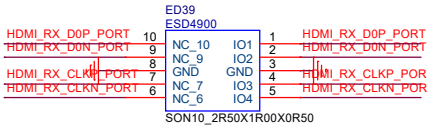
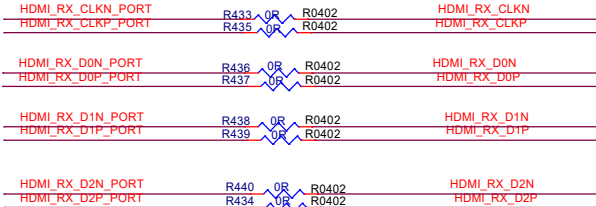
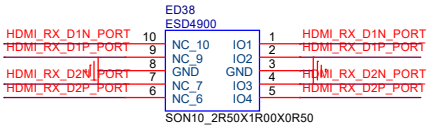
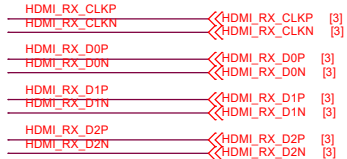
LGA506
LGA506_45R0X50R0_M0



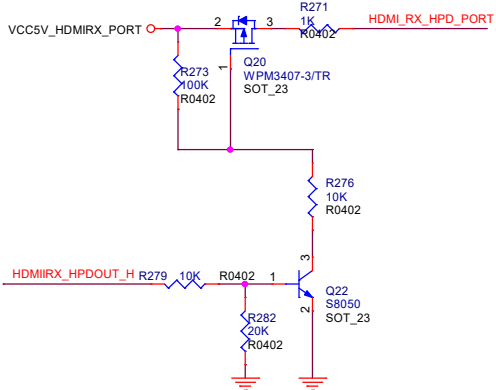
TF CARD



HDMI2.0 RX

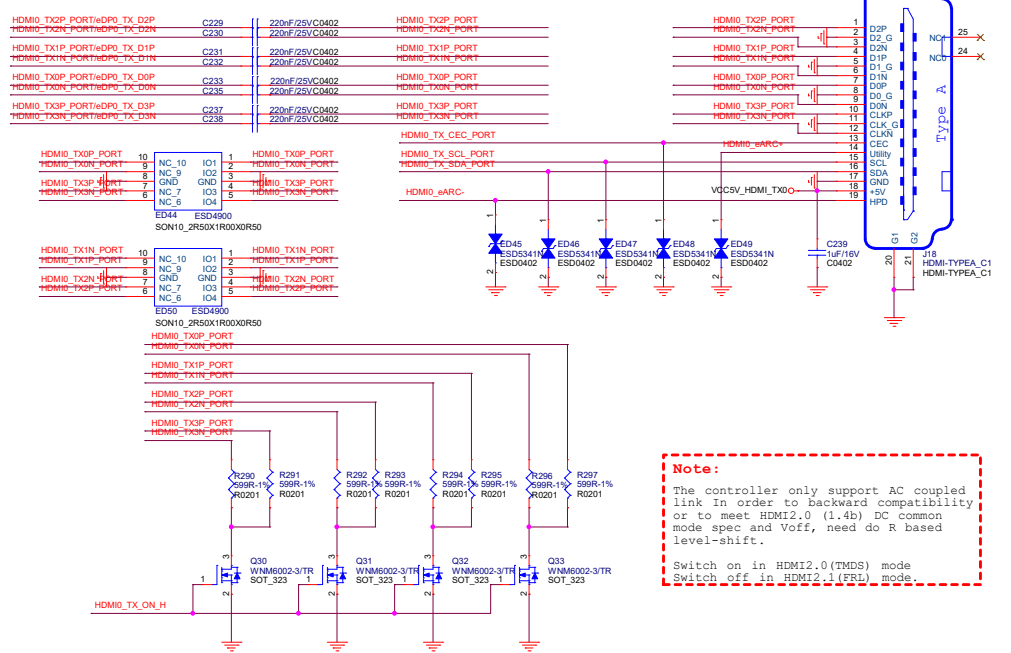
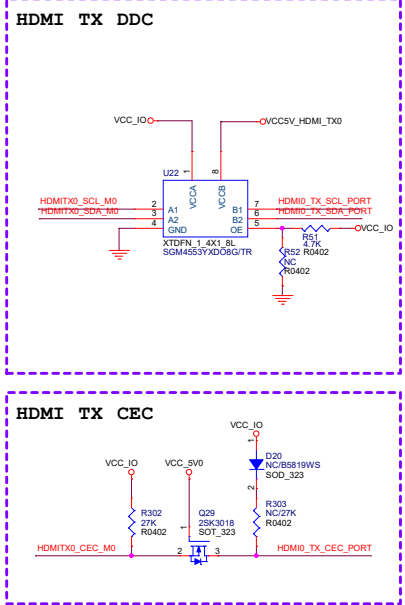
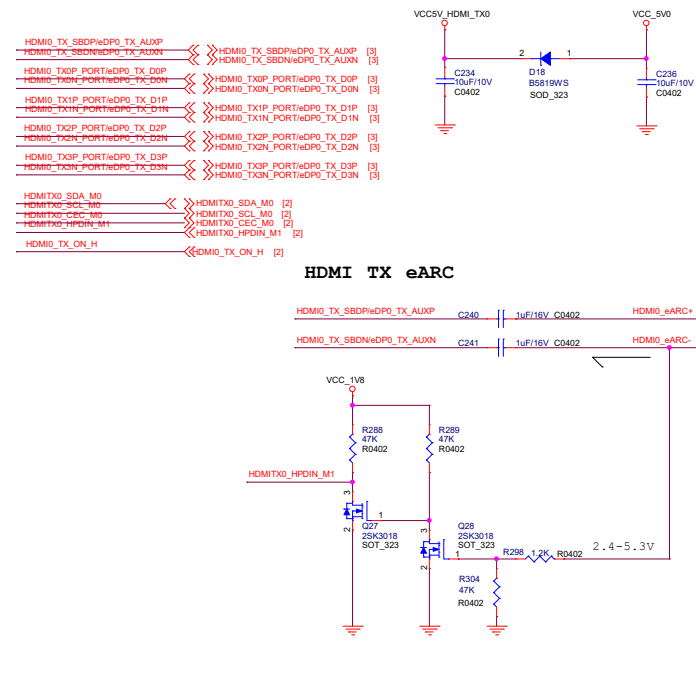


HDMI RX DDC

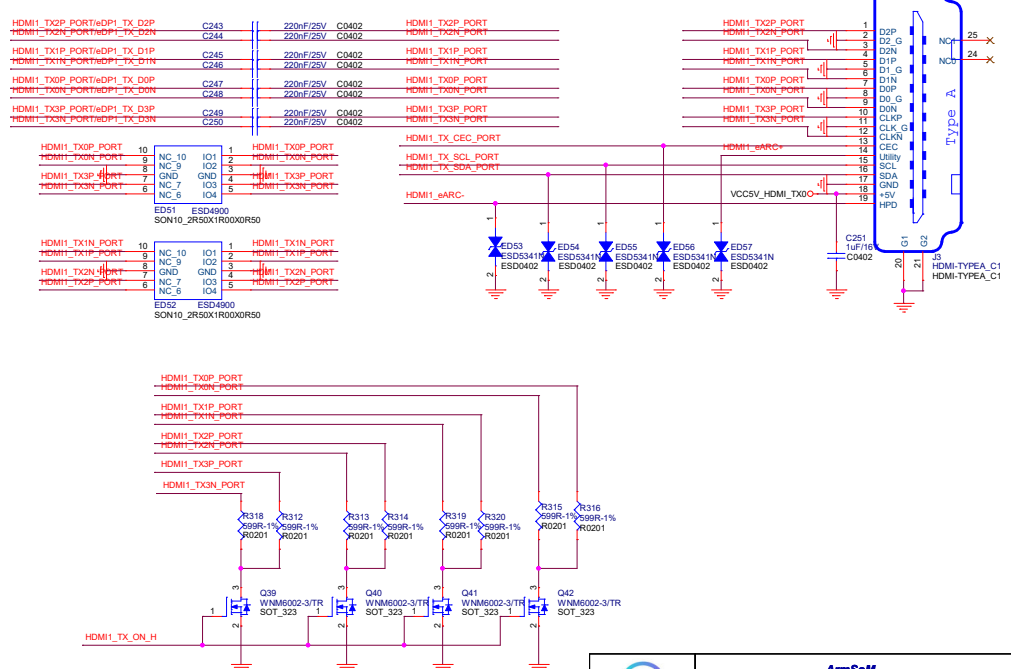
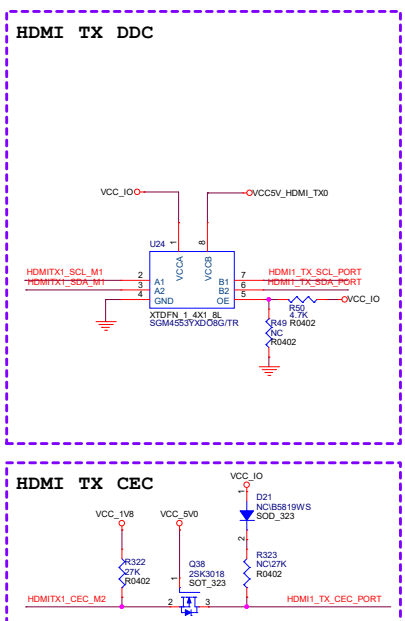
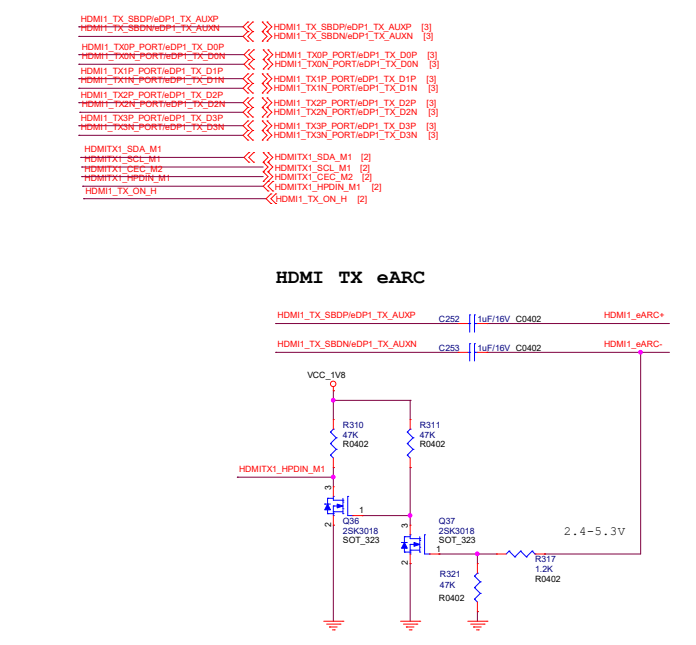


HPD:
Sink Side: Require output, Min 2.4V; Max 5.3V
Source Side: Require input and Detection.
Min 2.0V, Max: 5.3V

HDMI TX0

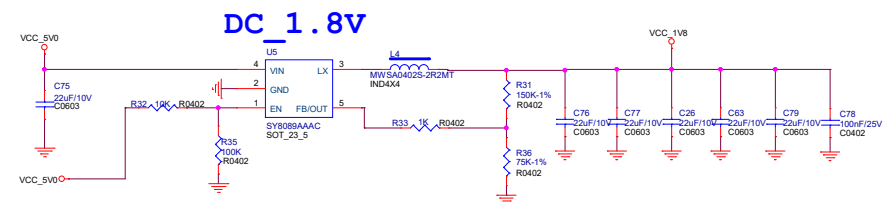
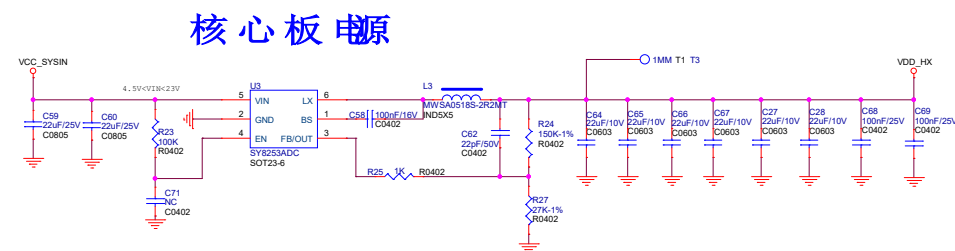
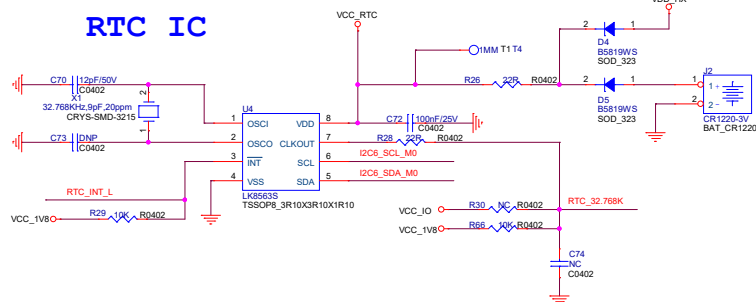
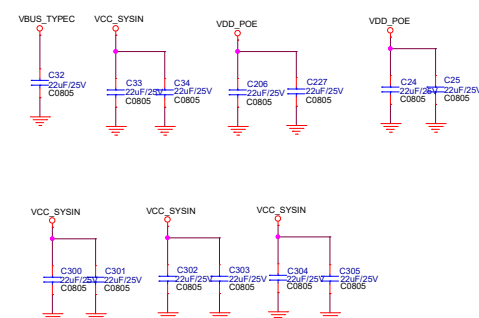
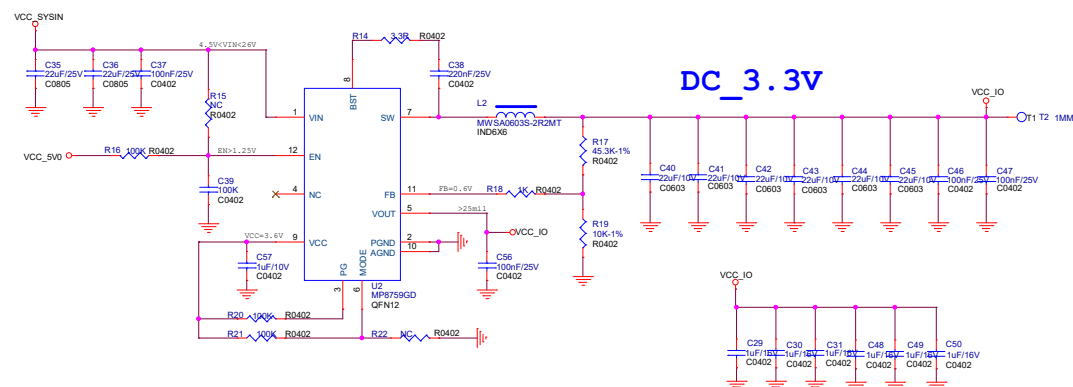
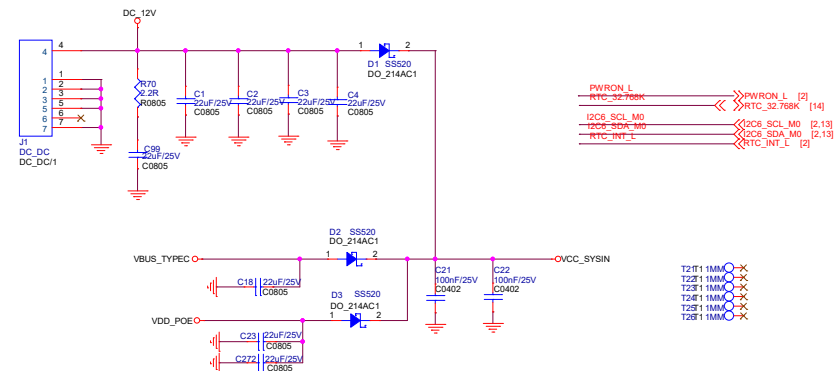
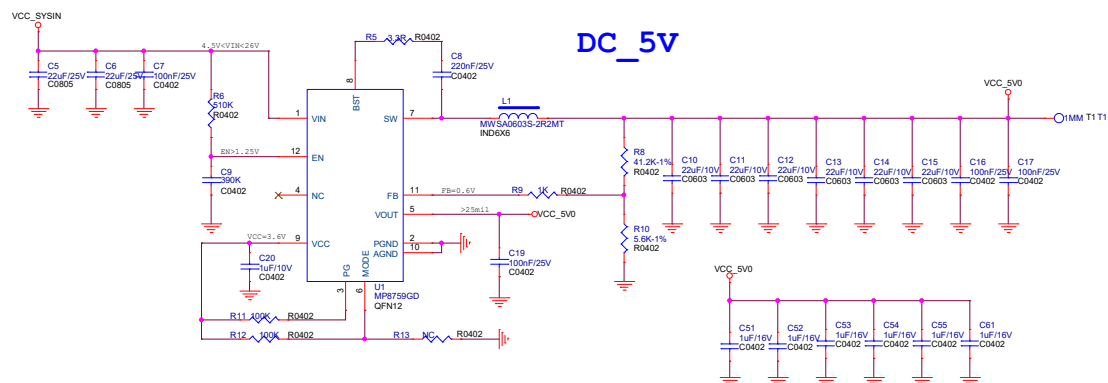


HDMI TX1

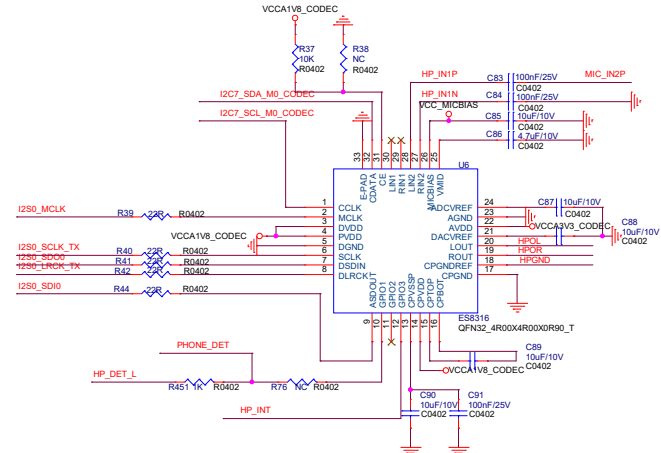
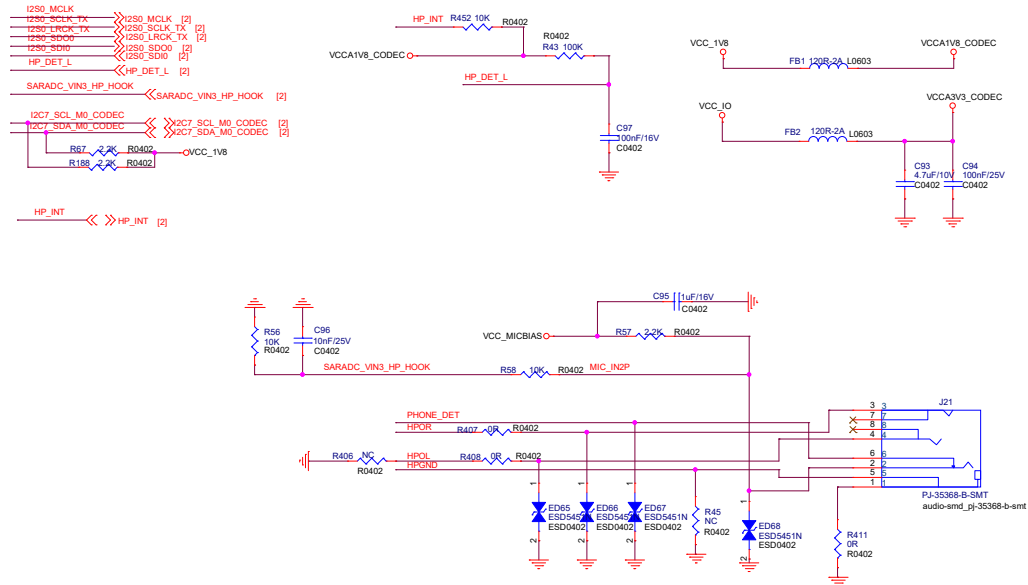


Cj<=0.2pF

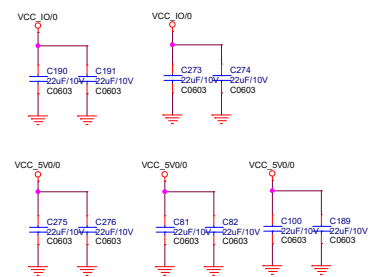
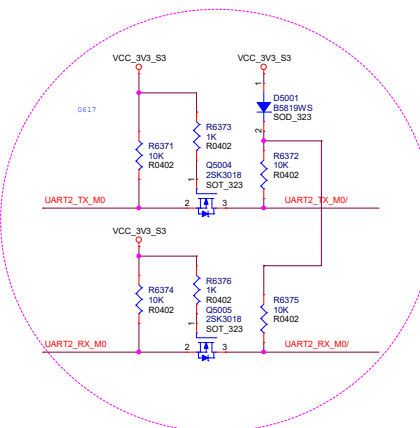
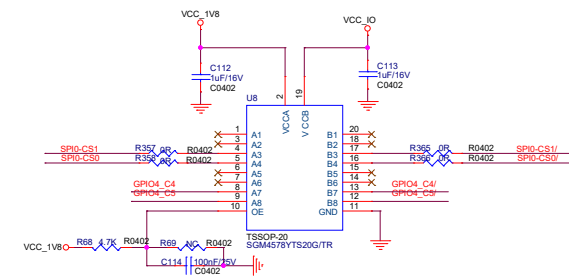
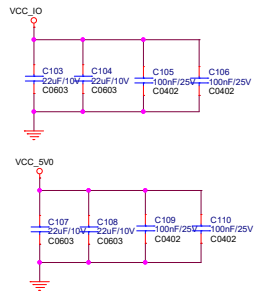
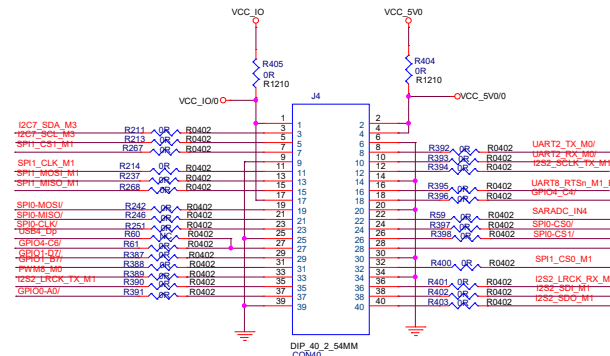
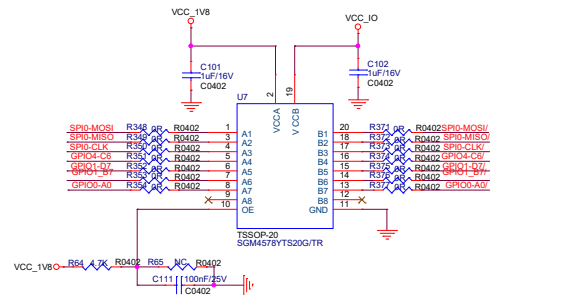
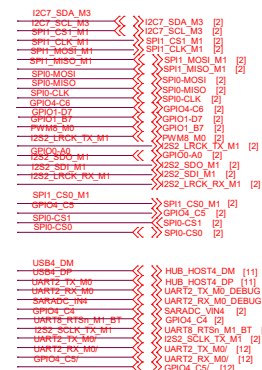
Note:
The controller only support AC coupled link in order to backward compatibility or to meet HDMI2.0 (1.4b) DC common mode spec and Voff, need do R based level-shift.
Switch on in HDMI2.0(TMDs) mode
Switch off in HDMI2.1(FRL) mode



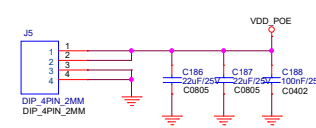
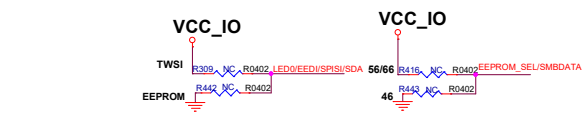
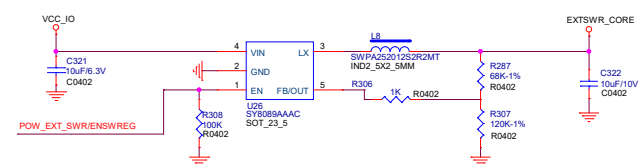
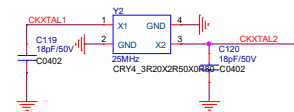
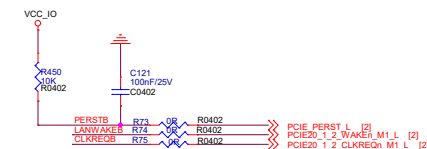
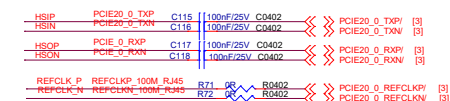
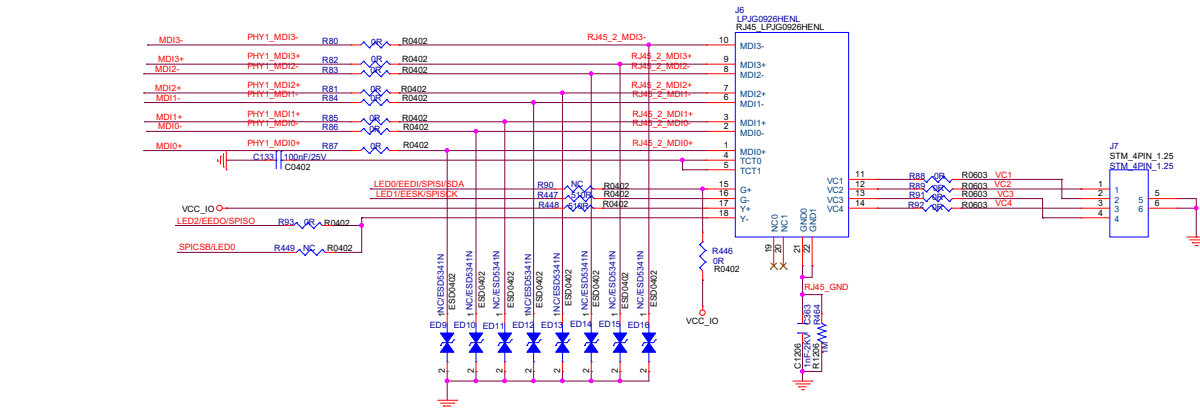
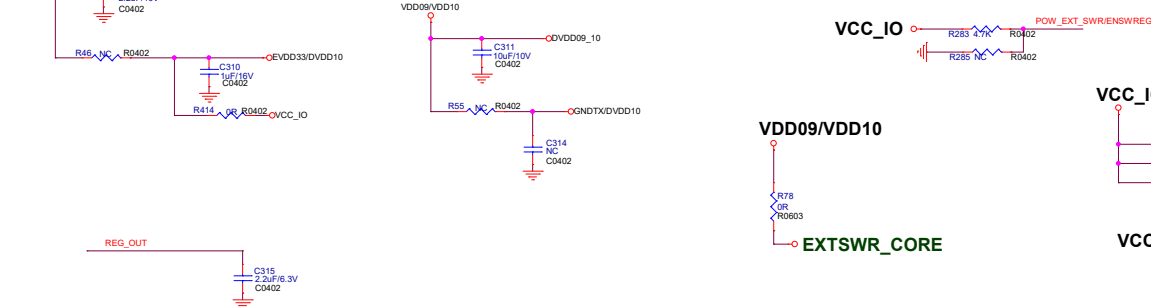
CODEC ES8316



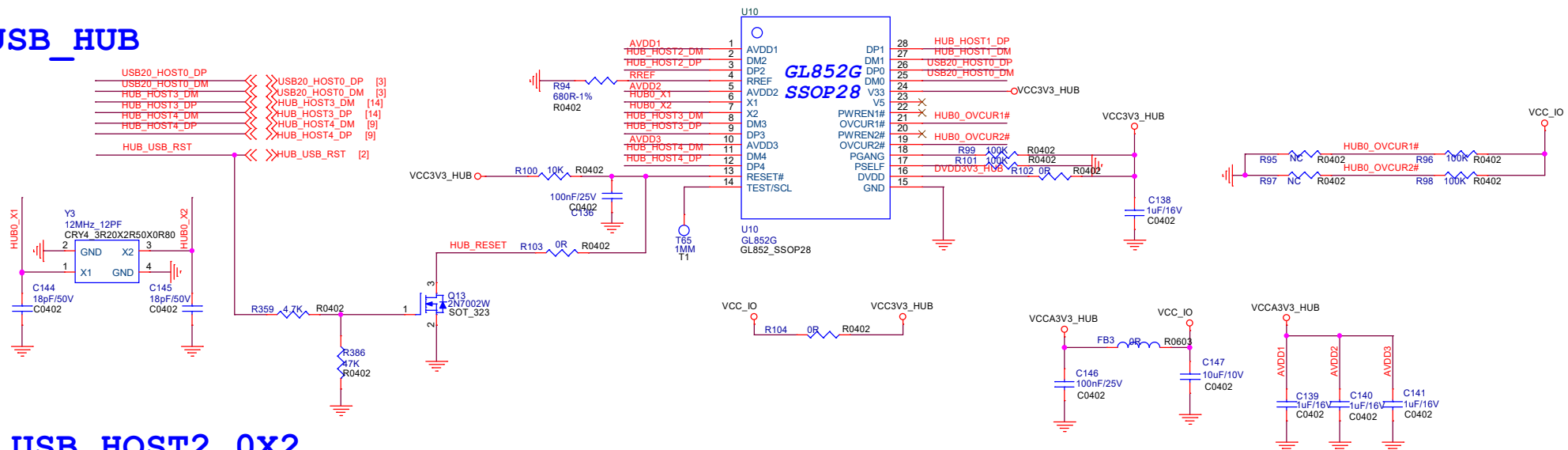
40PIN GPIO



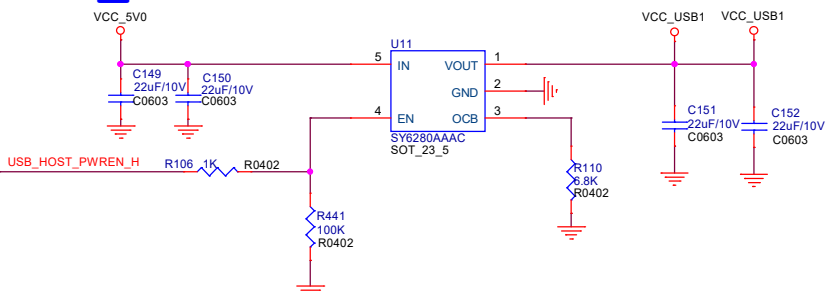
PCIE TO RJ45



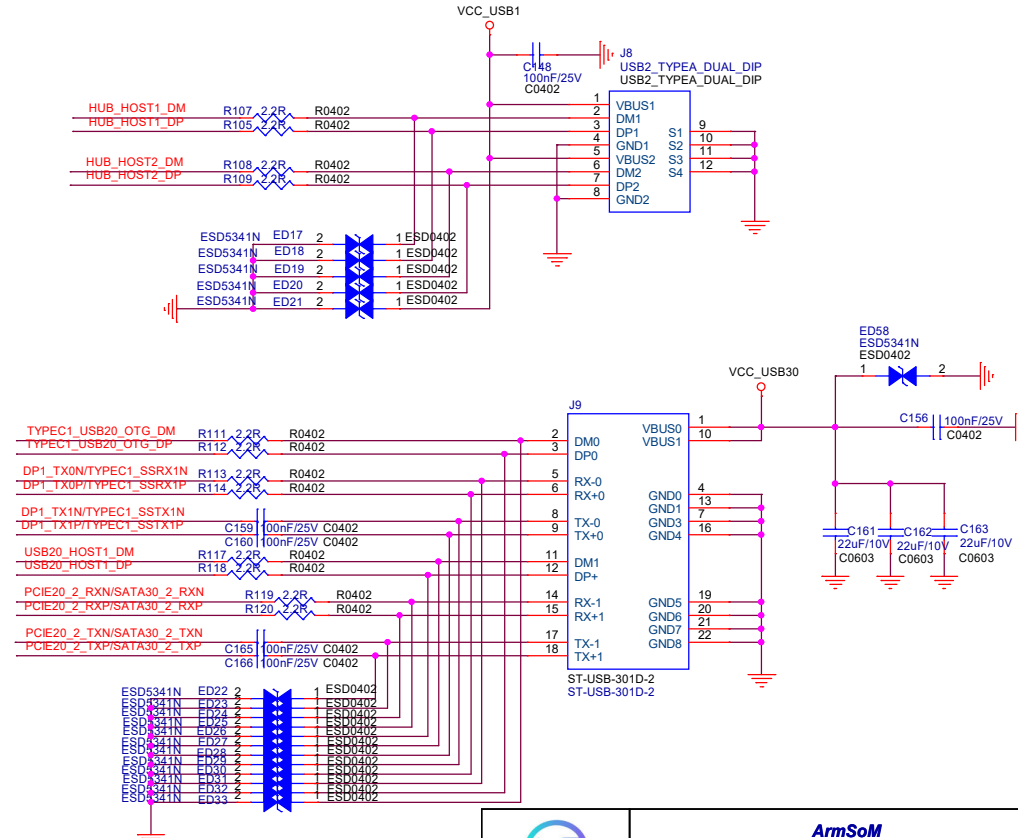
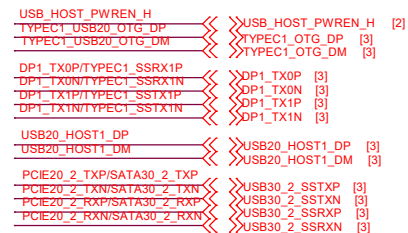
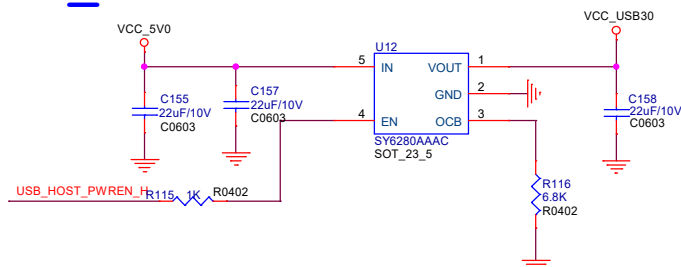
USB HUB



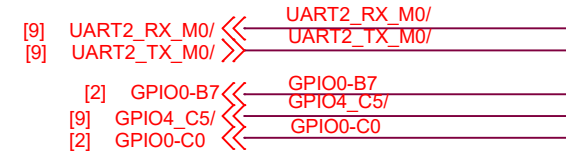
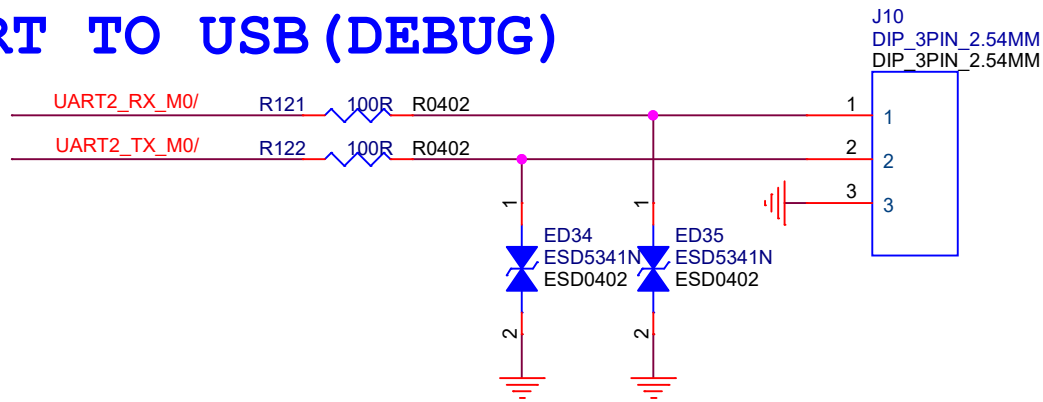
USB HOST2.0X2



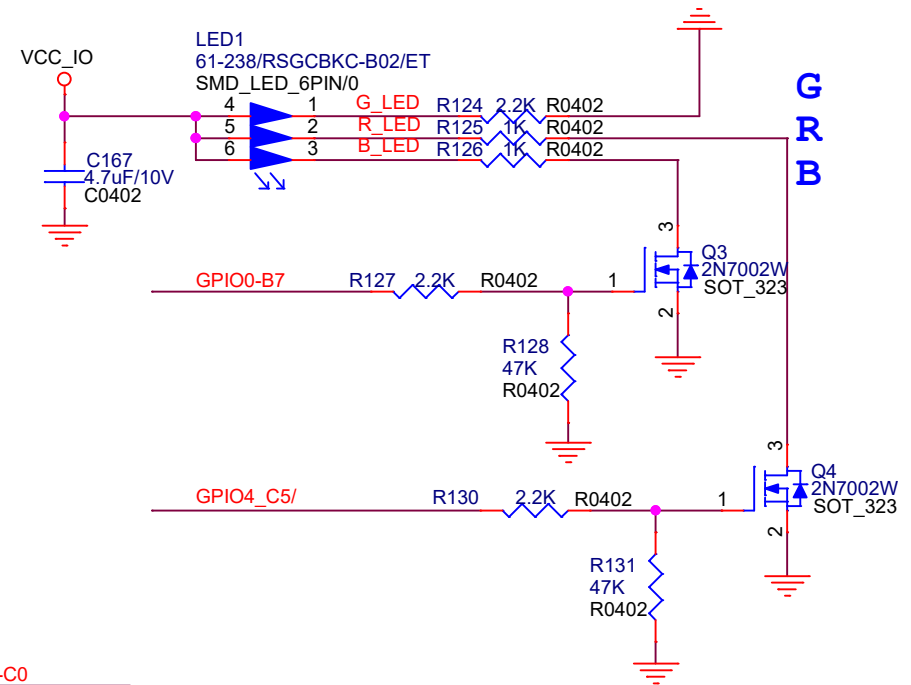
USB HOST3.0x2



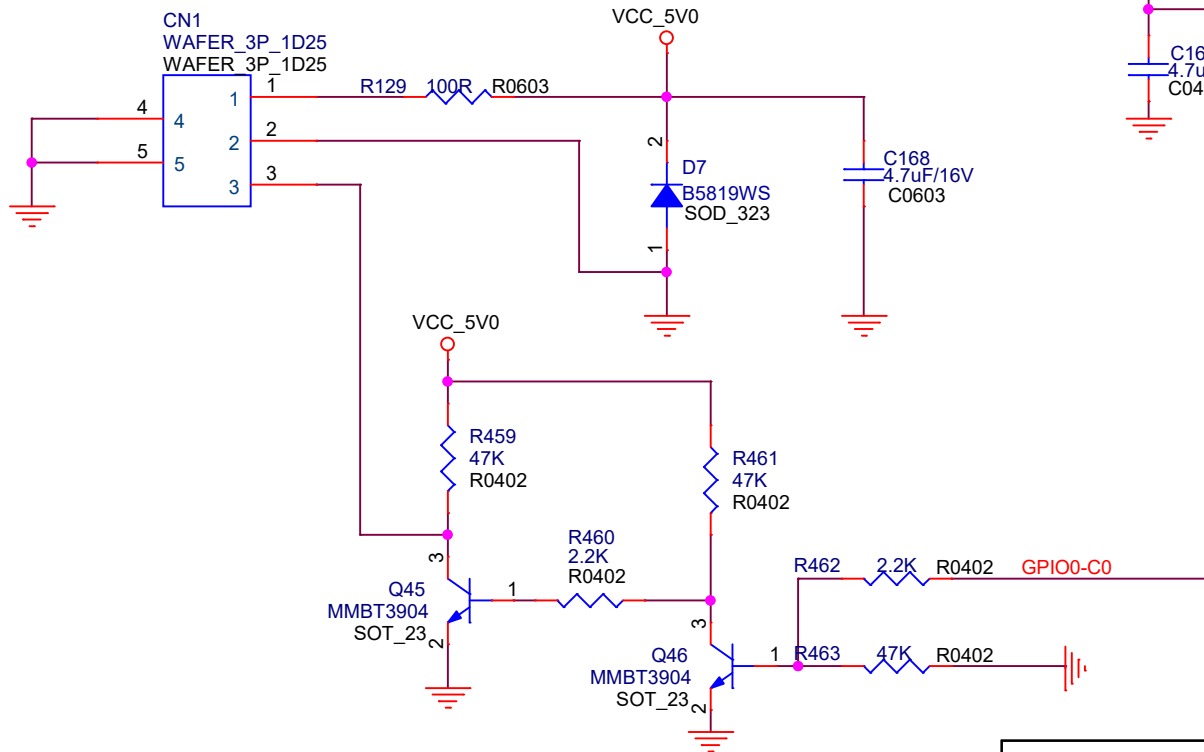
UART TO USB (DEBUG)



RGB LED



FAN



armsom

<https://armsom.org/>

ArmSom

Project :

ARMSOM_W3

Size

A

Page Name

Debug_UART/FAN/RGB_LED

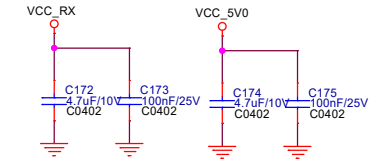
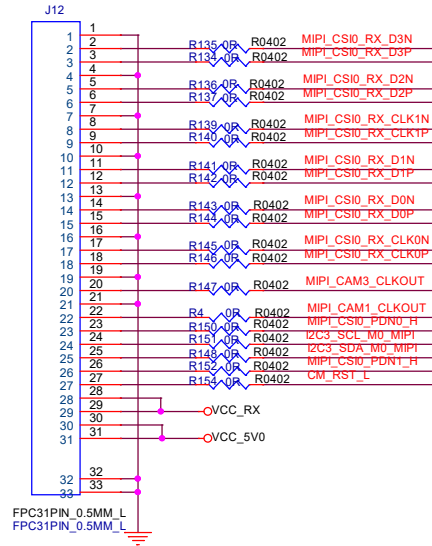
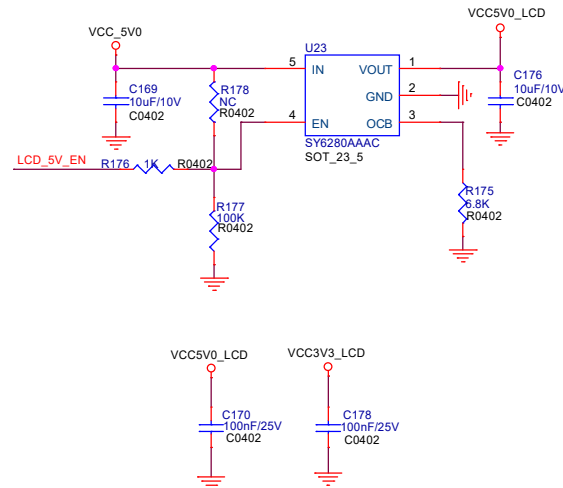
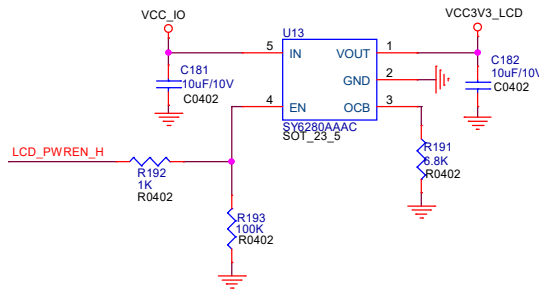
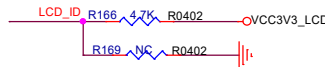
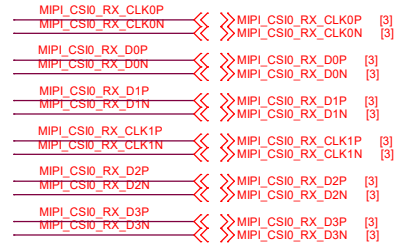
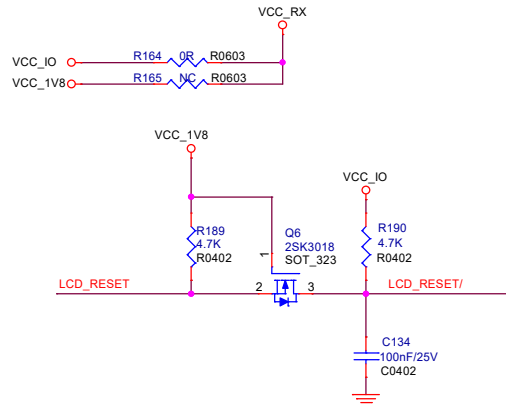
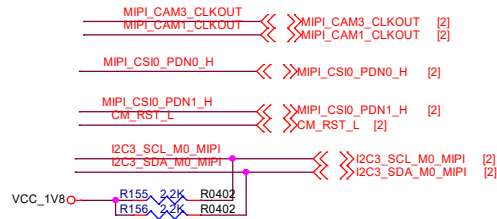
Rev

V1.2

Date: Thursday, October 24, 2024

Sheet 12 of 14

CSI_MIPI



DSI_MIPI

