

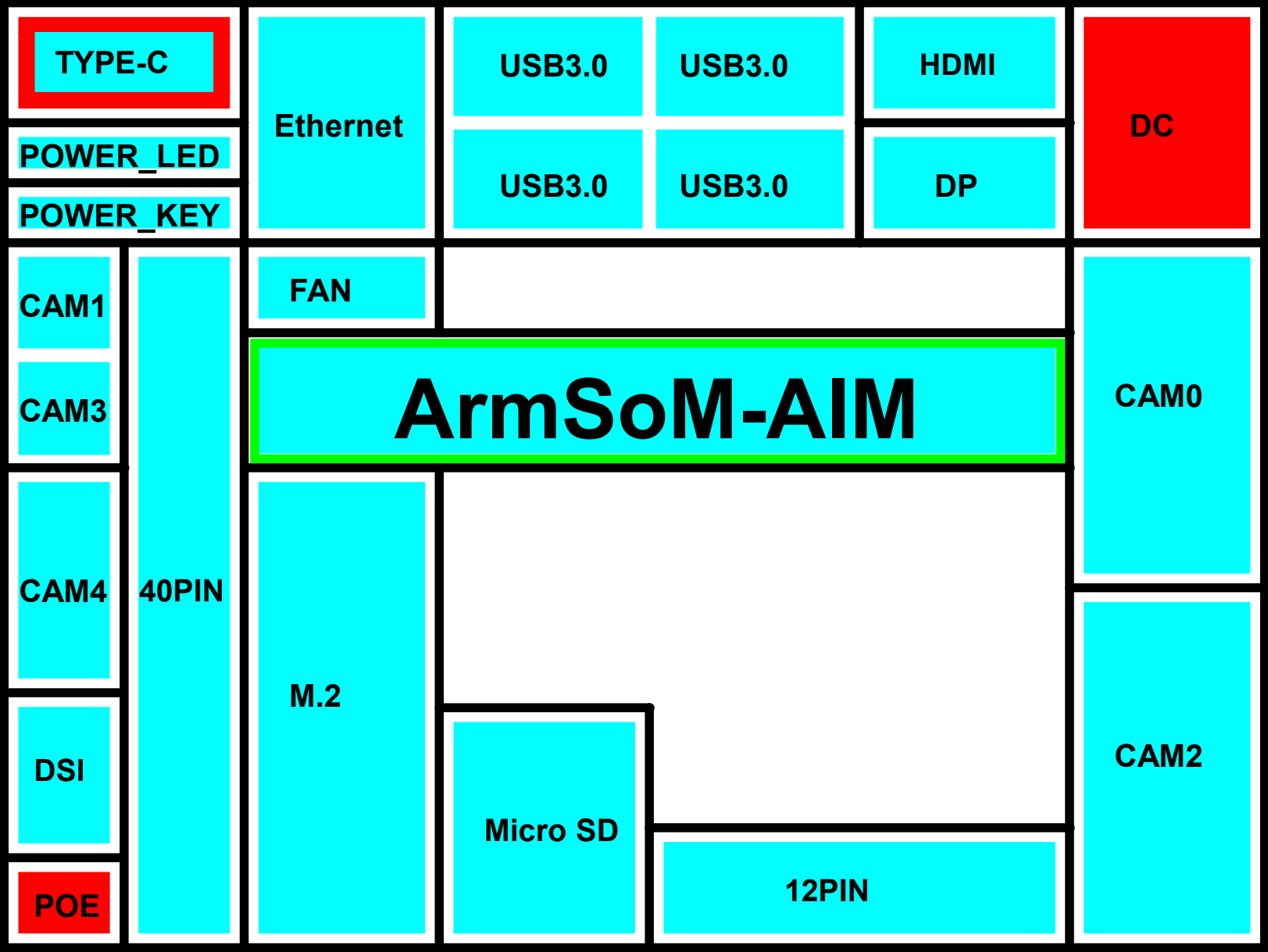
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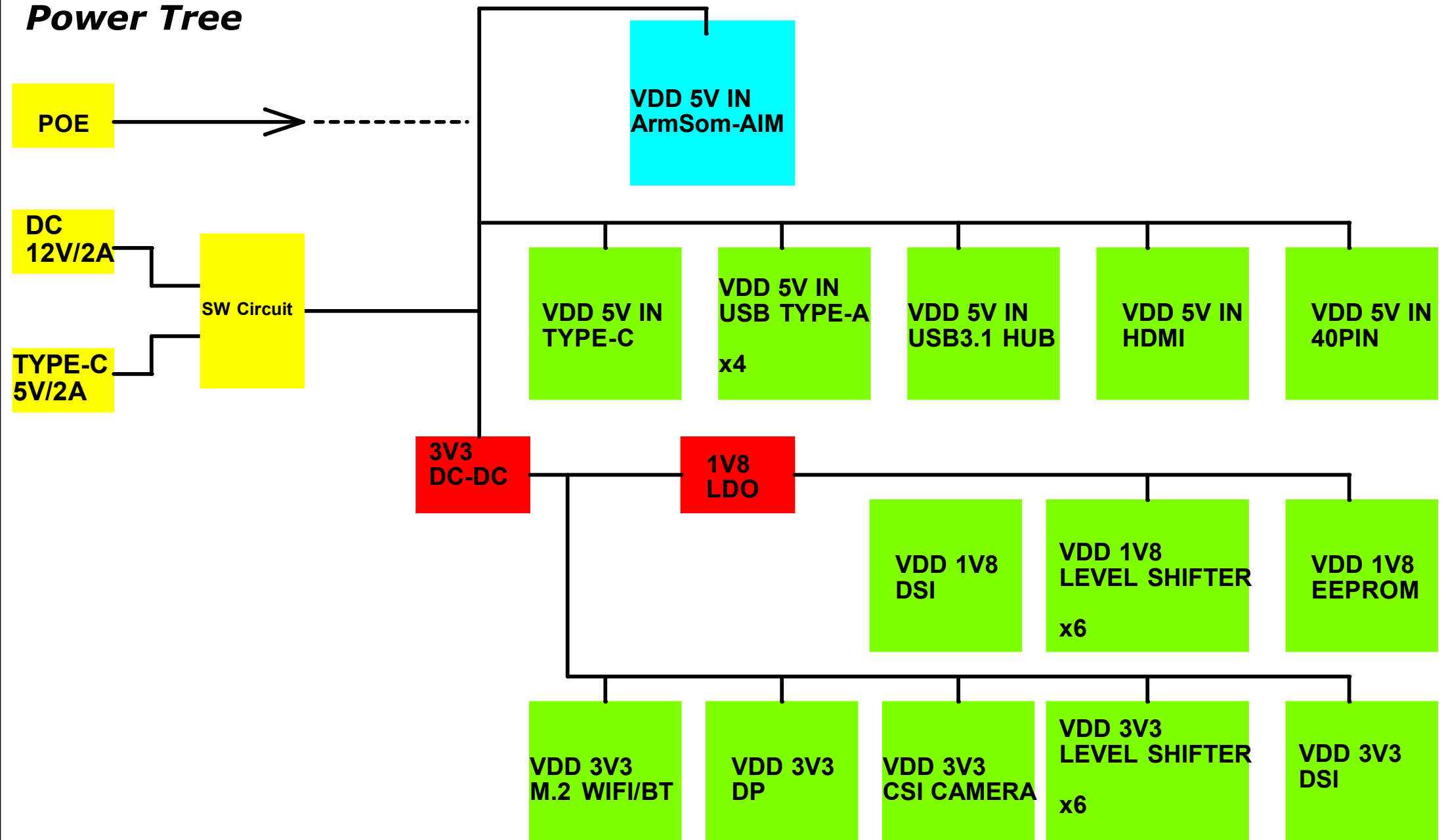
Revision History

Version	Date	By	Change Dscription	Approved
V1.0	2024-03-18	Liu Xinglin	1: First release;	
V1.1	2024-11-19	Park	1: TypeC DET: add R6370; USBHUB:add Jump testpoint VS1-VS15; 2: DSI: Lcd&tp two connectors change to one connector; DP:add R6371&R6372; 3: CAM: Cam2&Cam3&Cam4 RST singal change to uart0_rts&uart0_cts ; 4: DCIN: change DC-DC IC ; USB SW change to MOS; 5: MEC5/MEC6A/MEC6B change PCB Footprint;	

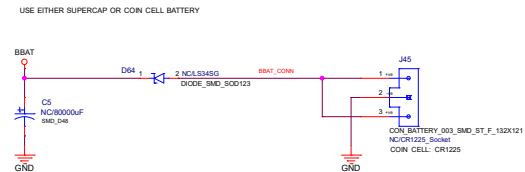
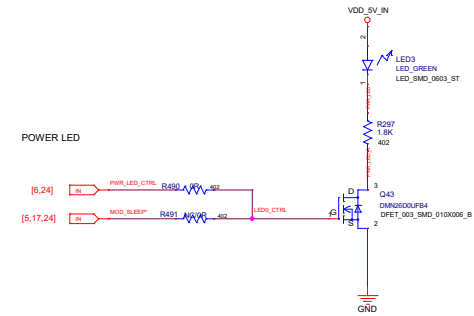
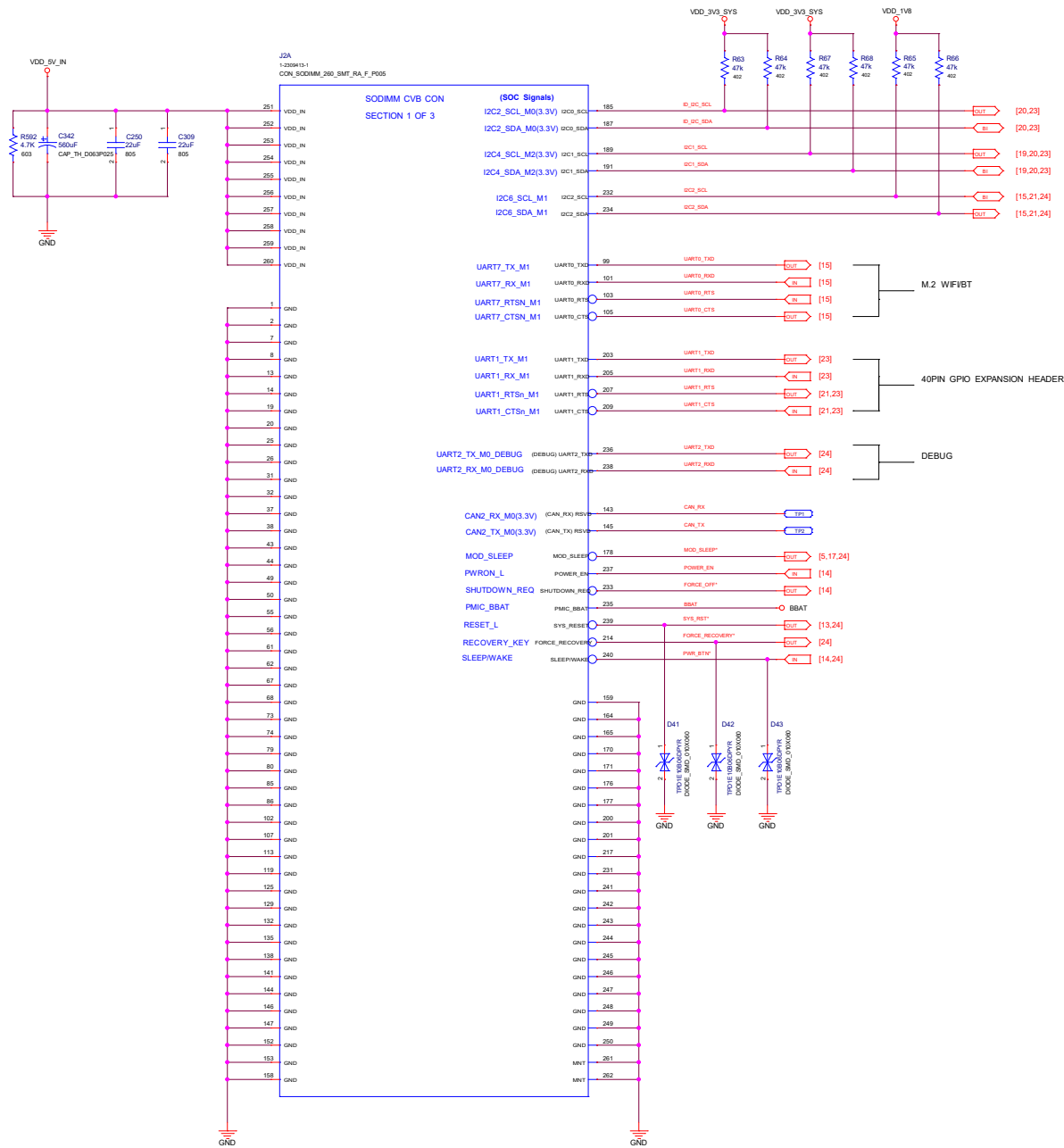
Block Diagram



Power Tree



UART/ I2C/CAN/POWER_GPIO

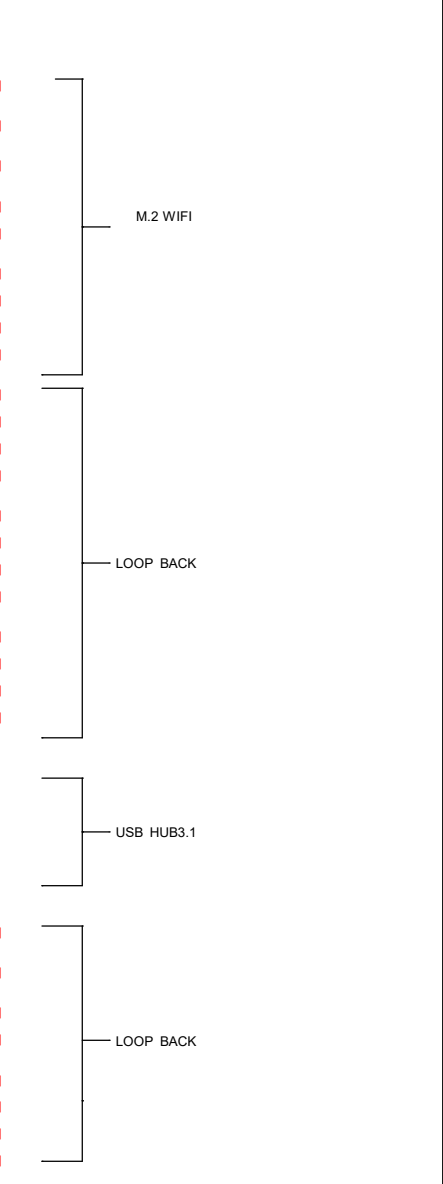
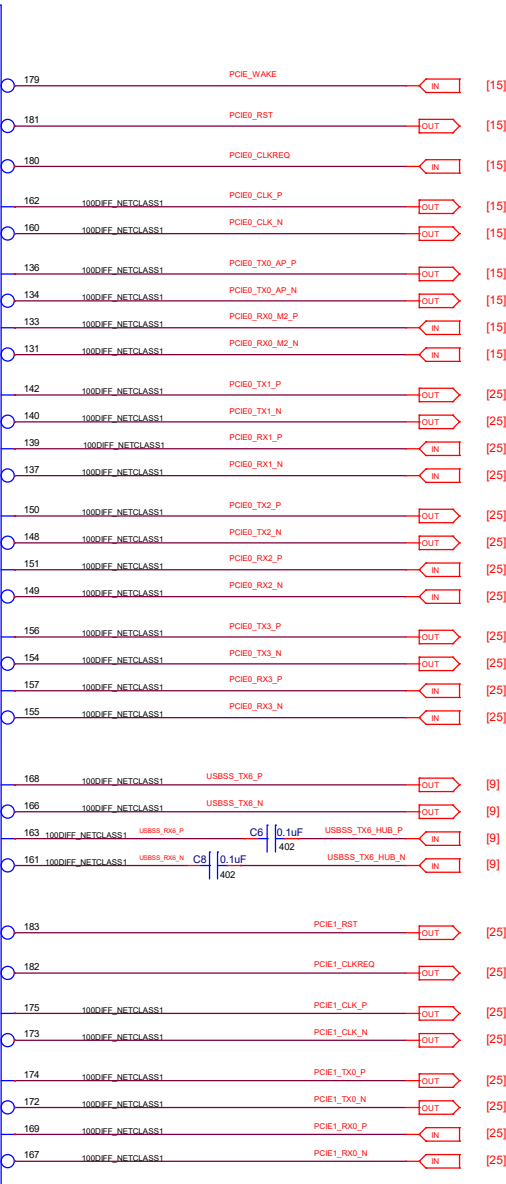


USB2.0/I2S/Ethernet/SDIO/GPIO/CSI/SPI

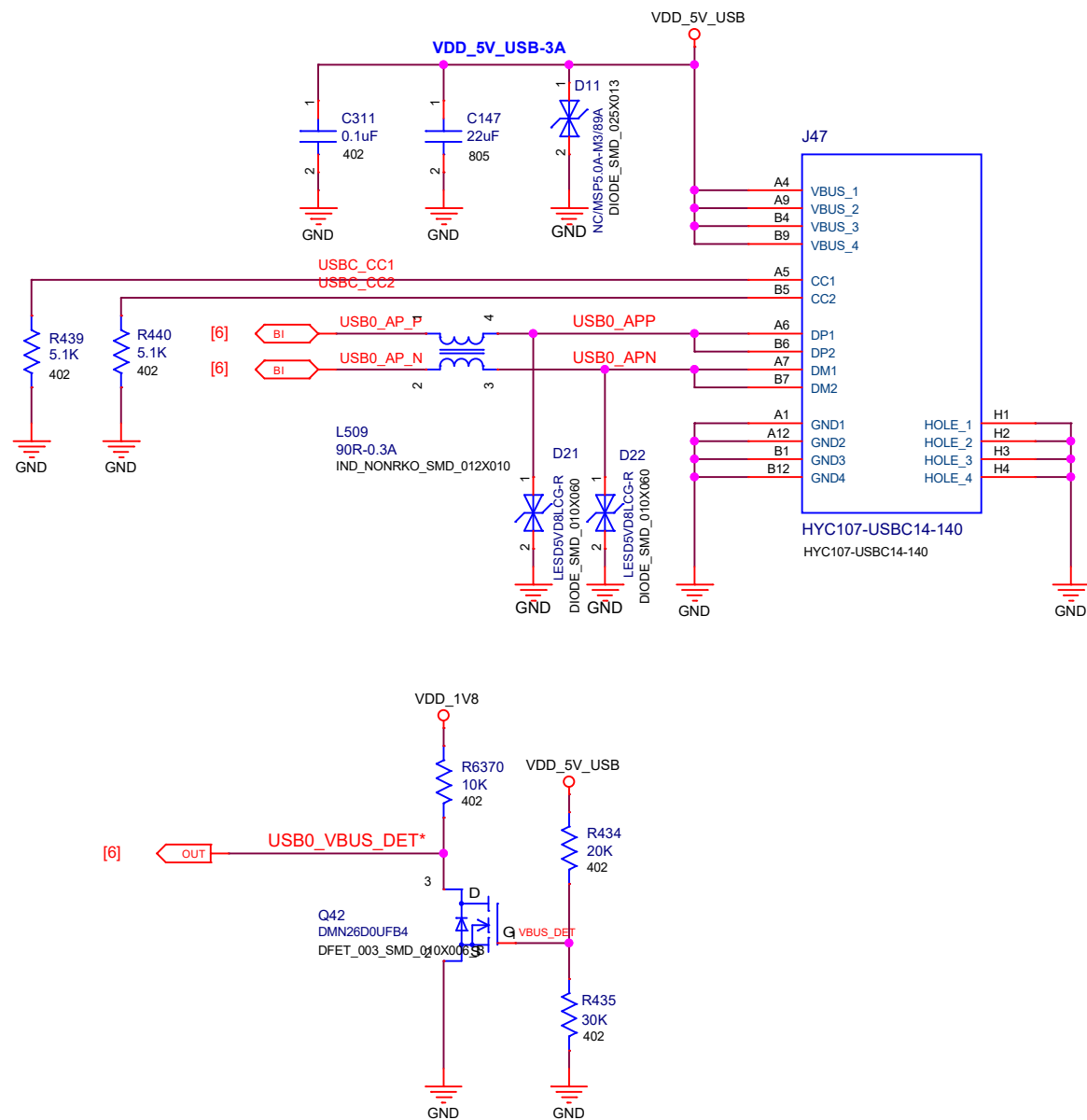


DSI/DP/HDMI/M.2/USB3.1/LOOP BACK

J2C
1-2309413-1
CON_SODIMM260_JETSON_NANO
COMMON

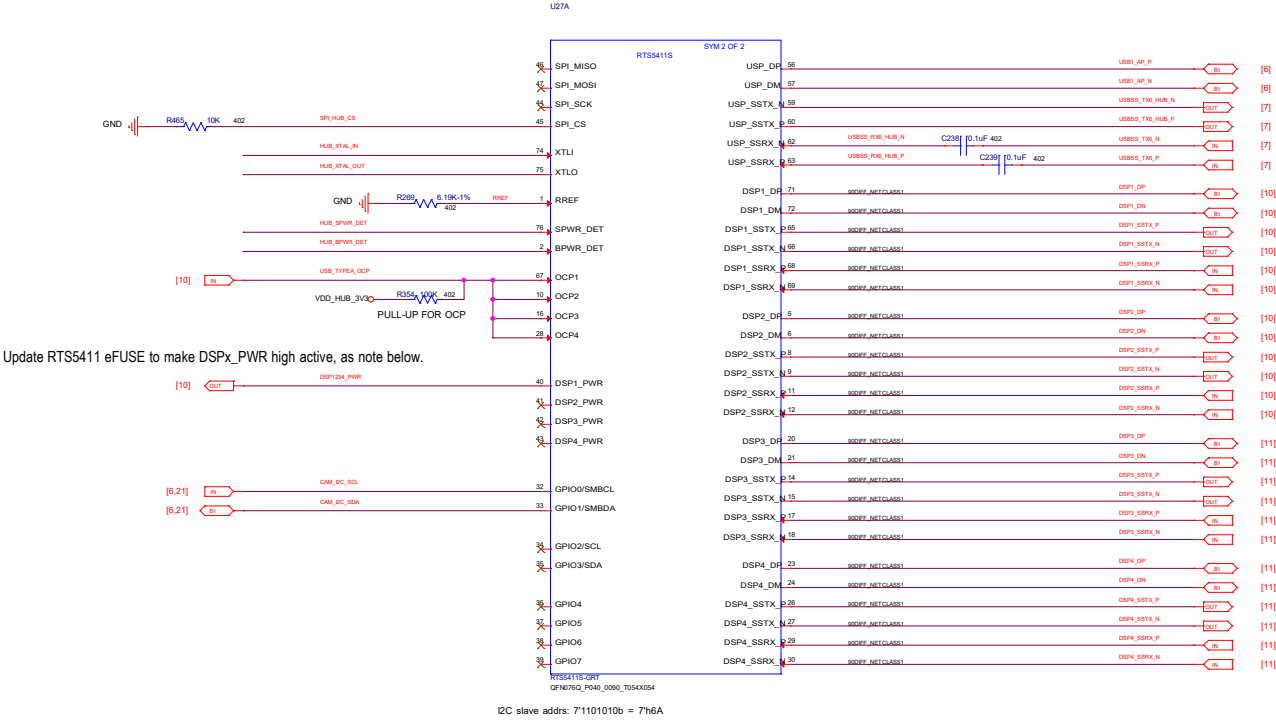
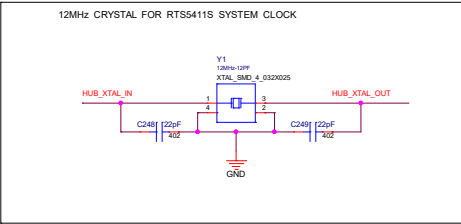
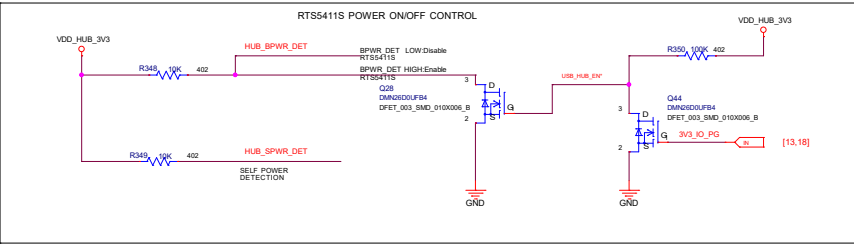
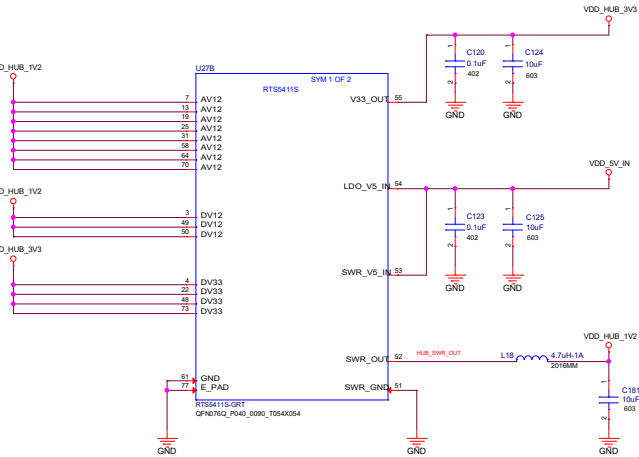
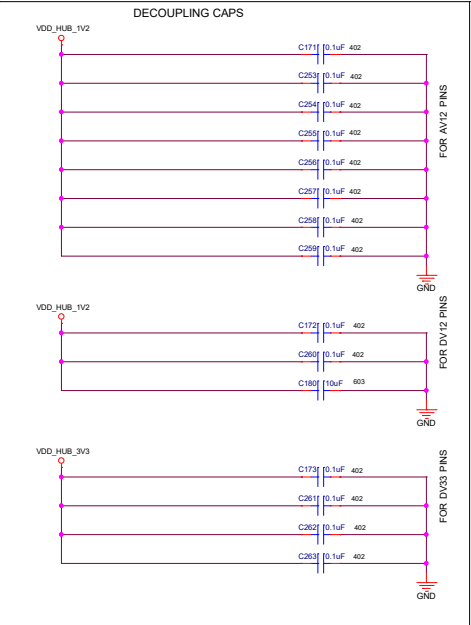


Type-c



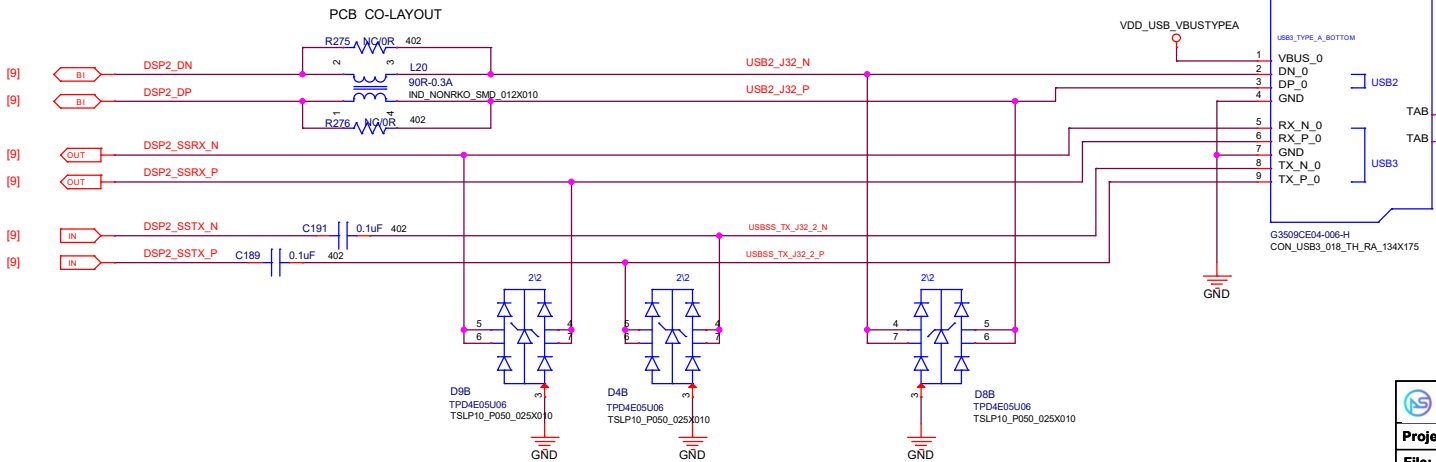
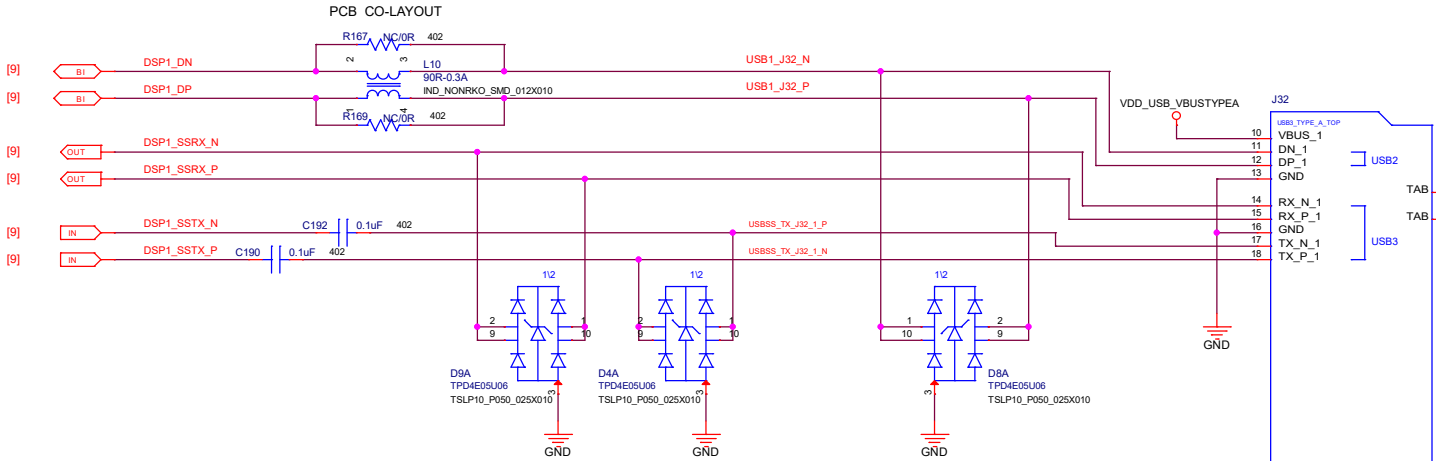
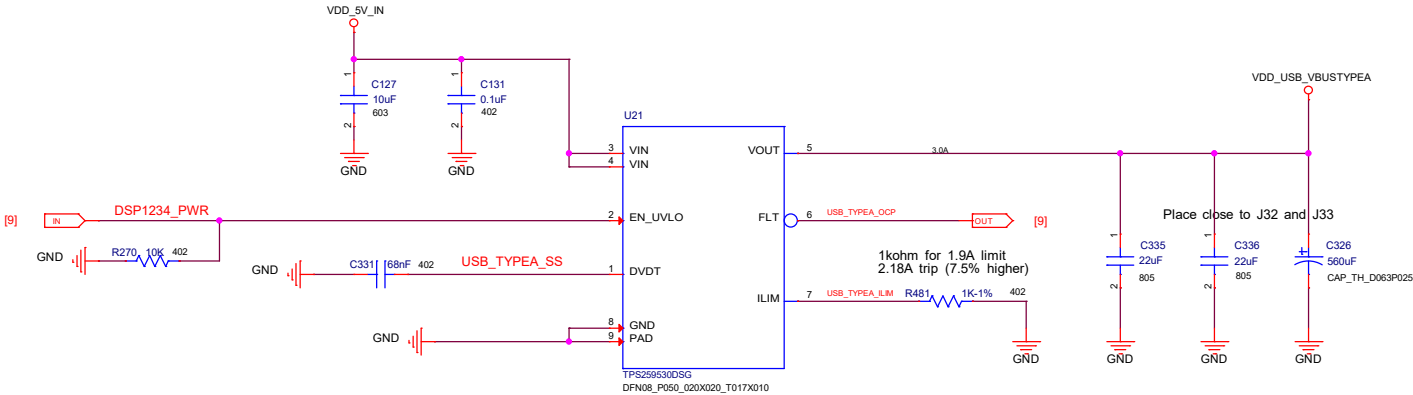
		ArmSoM	
Project:	AIM_IO_Board		
File:	USB2.0 Type-C		
Date:	Tuesday, December 10, 2024		Rev: V1.1
Designed by:	Park	Reviewed by:	<Checker>
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USB3.1 HUB

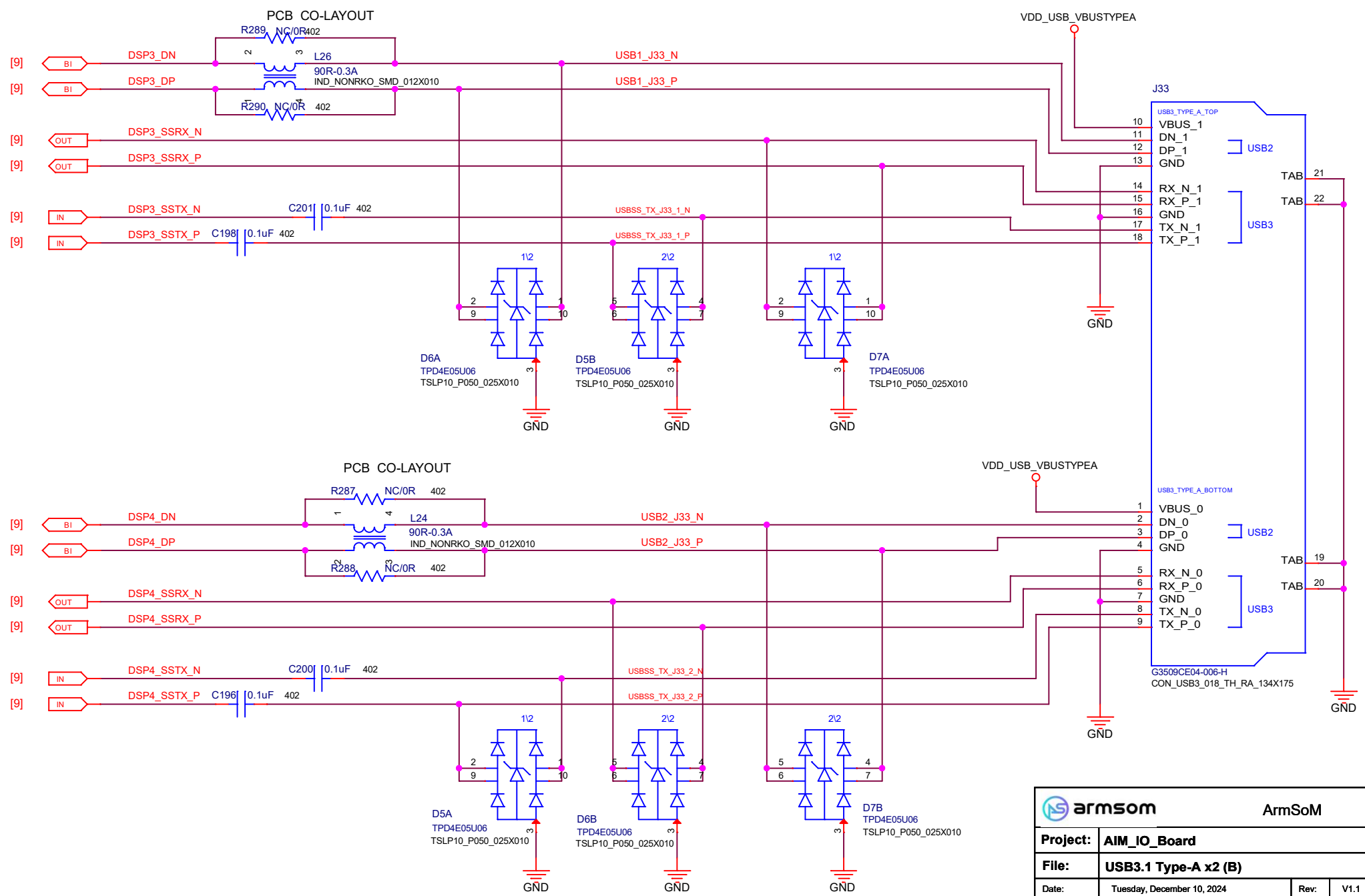


SPI Flash was removed from B01, customize RTS5411 internal eFuse (DSP1/2/3/4_PWR High active, disable charging feature, SSC valid) with Realtek tool.

USB 3.0 TYPE A x2 CONNECTOR (1/ 2)



USB 3.0 TYPE A x2 CONNECTOR (2/2)



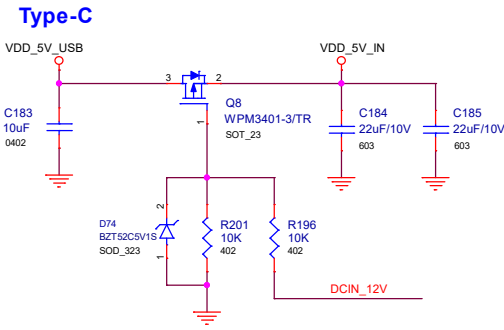
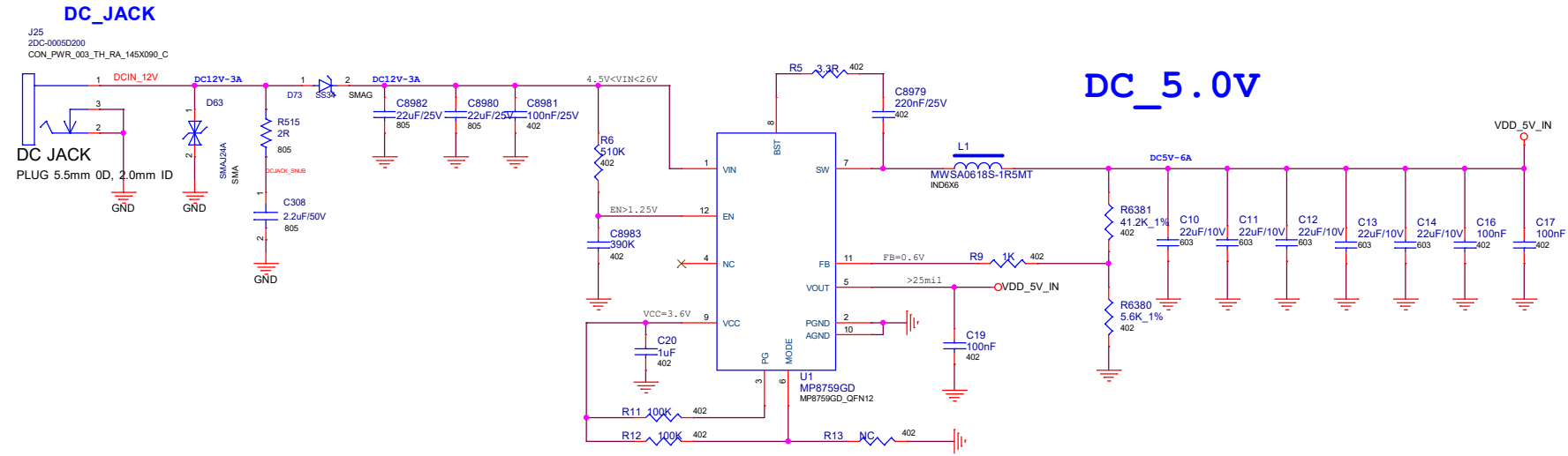


armsom

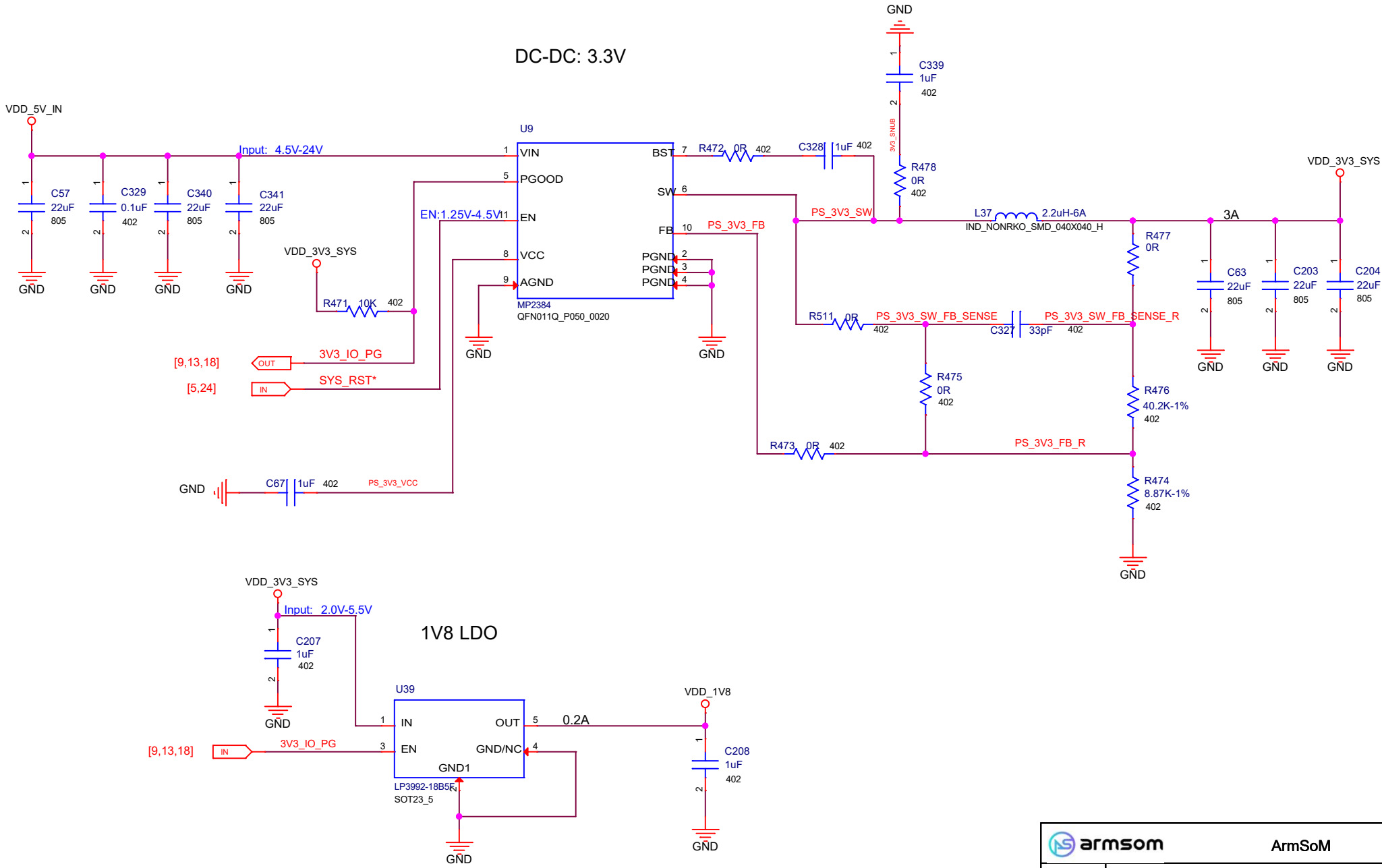
ArmSoM

Project:	AIM_IO_Board		
File:	USB3.1 Type-A x2 (B)		
Date:	Tuesday, December 10, 2024		Rev: V1.1
Designed by:	Park	Reviewed by:	<Checker>
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DC JACK & Type C POWER

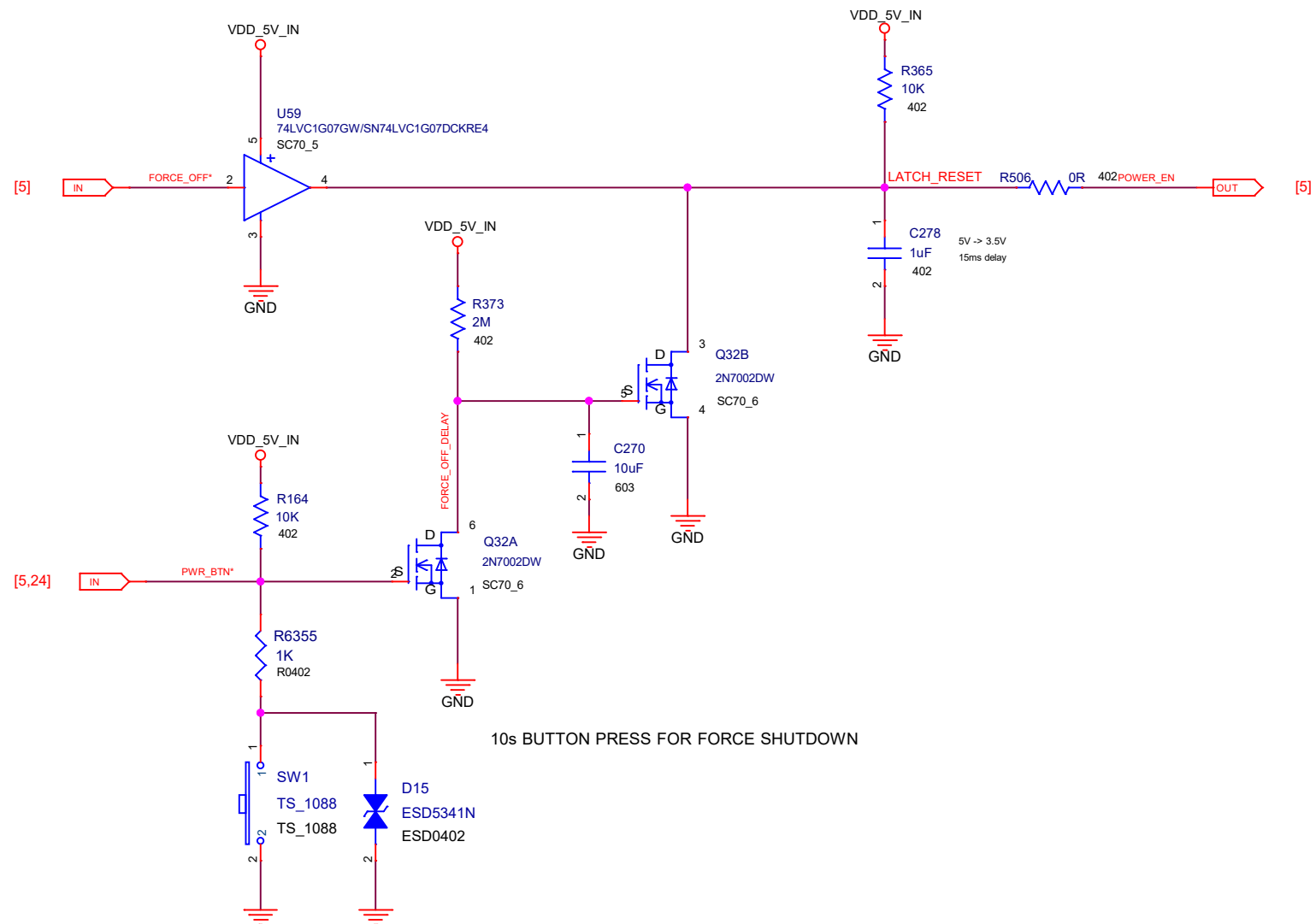


3V3/1V8



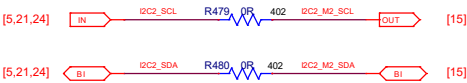
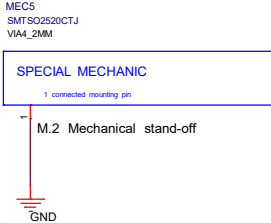
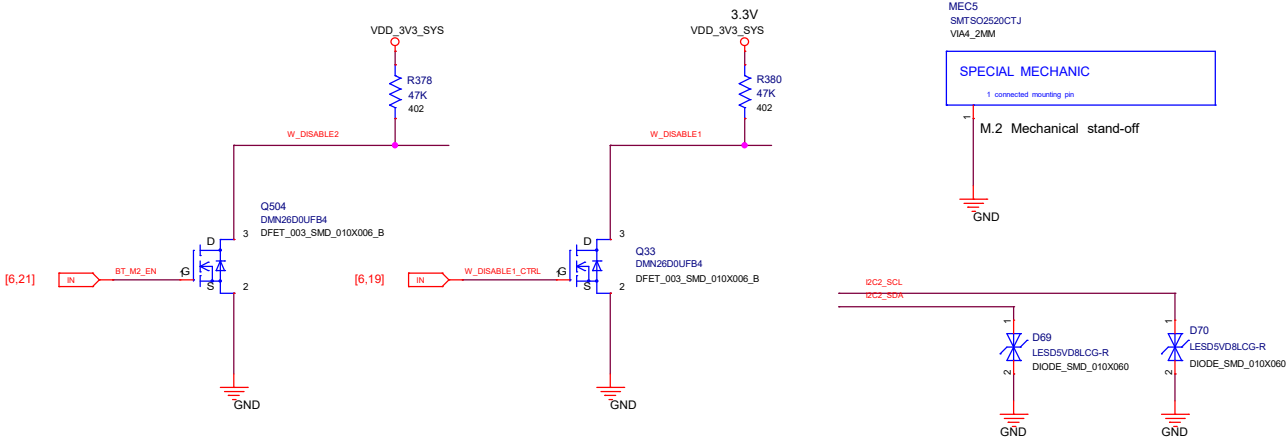
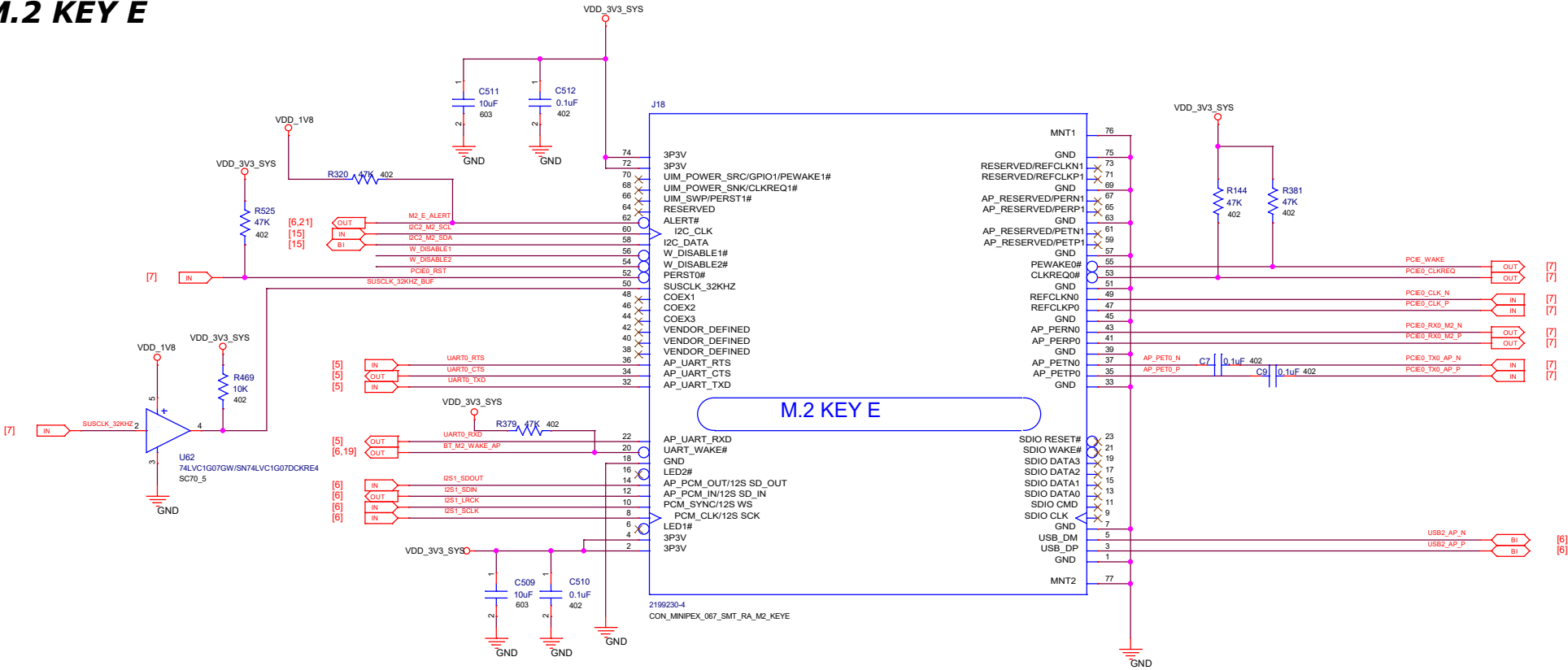
		ArmSoM	
Project:	AIM_IO_Board		
File:	3V3 DCDC and 1V8 LDO		
Date:	Tuesday, December 10, 2024		Rev: V1.1
Designed by:	Park	Reviewed by:	<Checker>
Sheet:	13 of 25		

Module Power ON/OFF logic

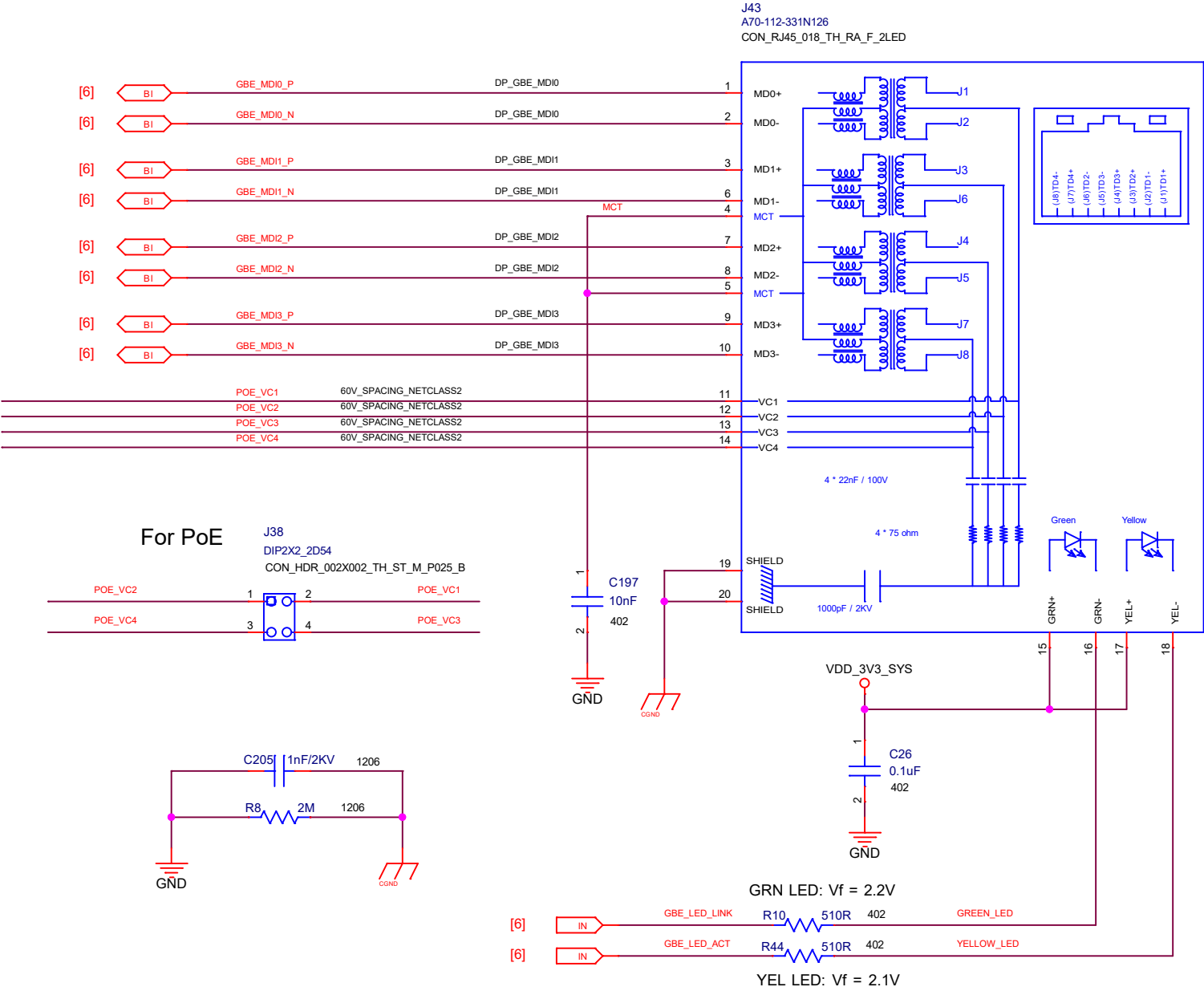


		ArmSoM	
Project:	AIM_IO_Board		
File:	Power ON_OFF Logic		
Date:	Tuesday, December 10, 2024		Rev: V1.1
Designed by:	Park	Reviewed by:	<Checker>
Sheet:	14 of 25		

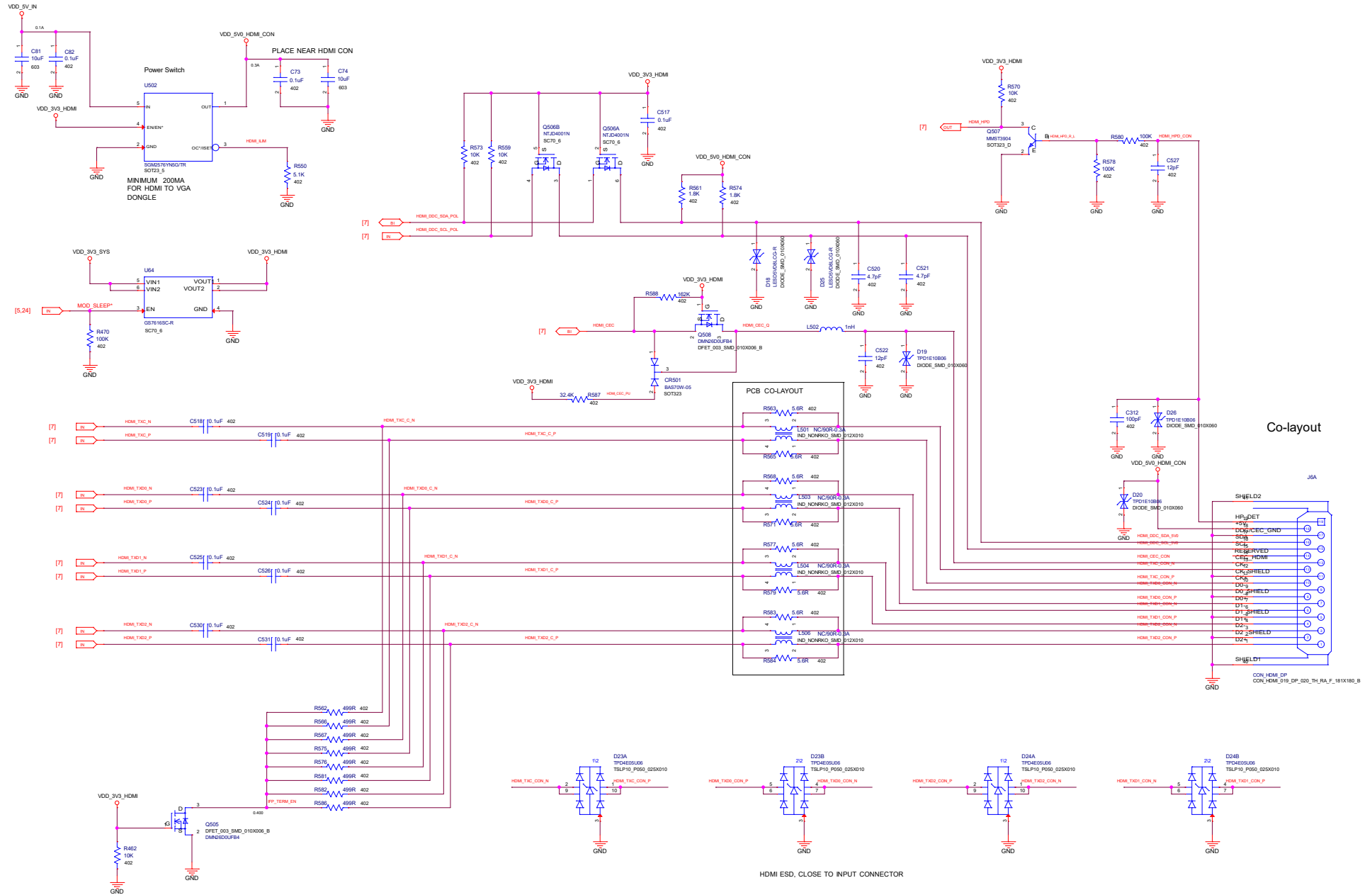
M.2 KEY E



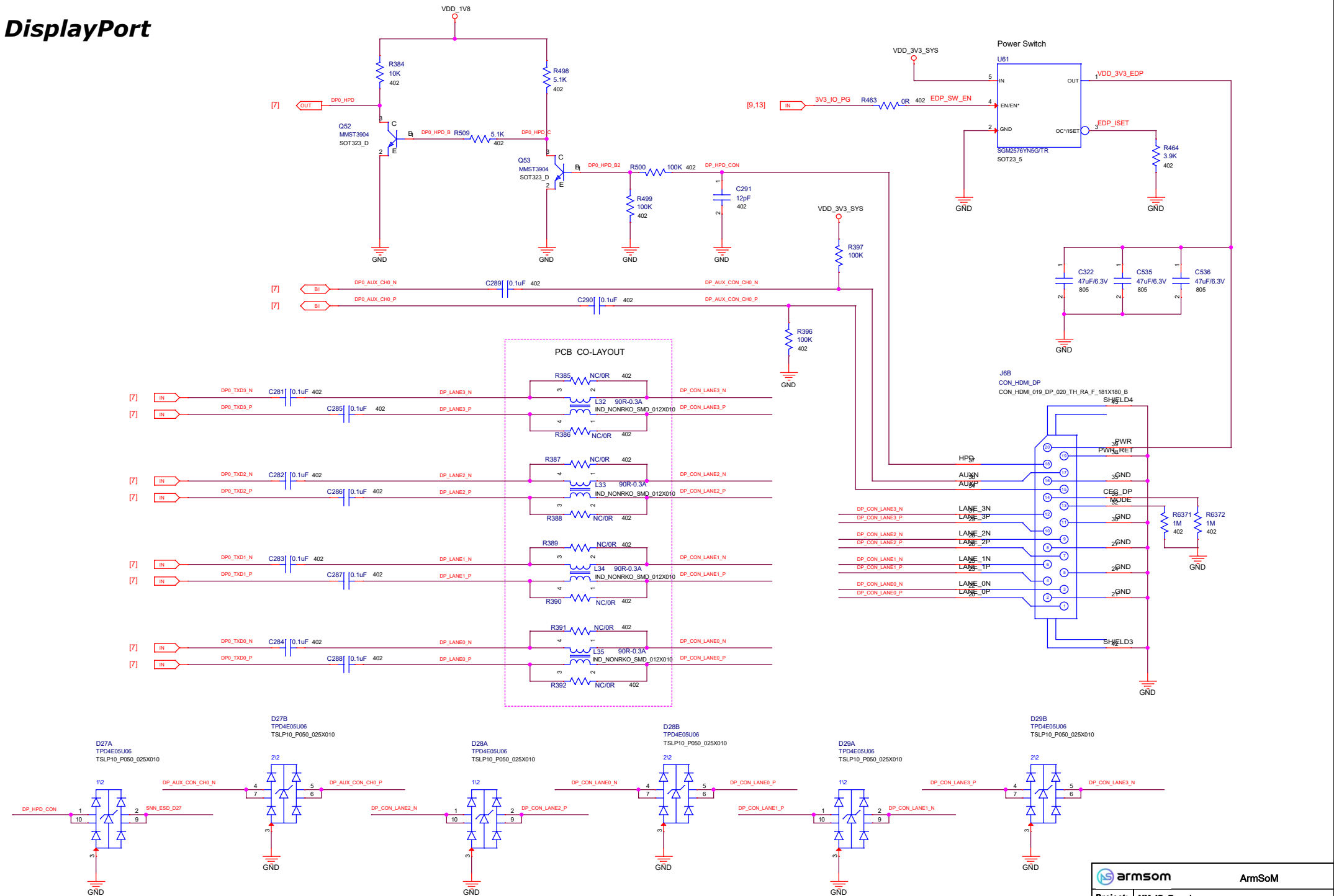
Gigabit Ethernet with PoE



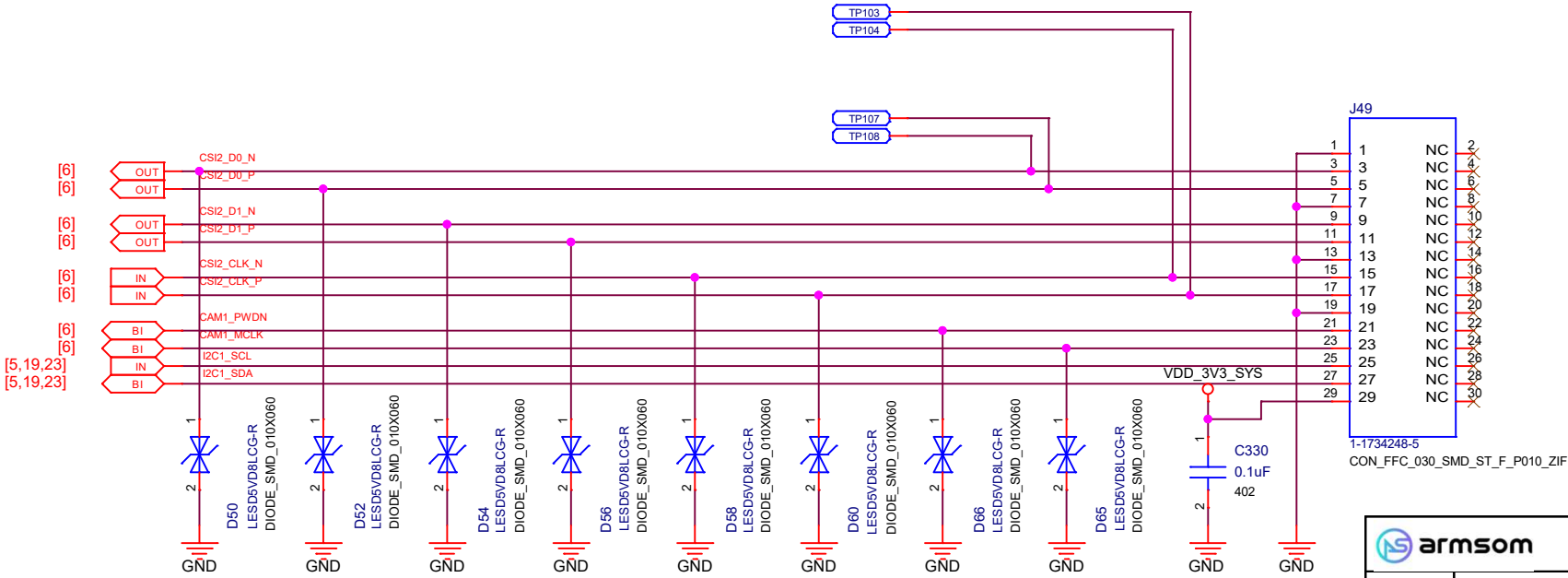
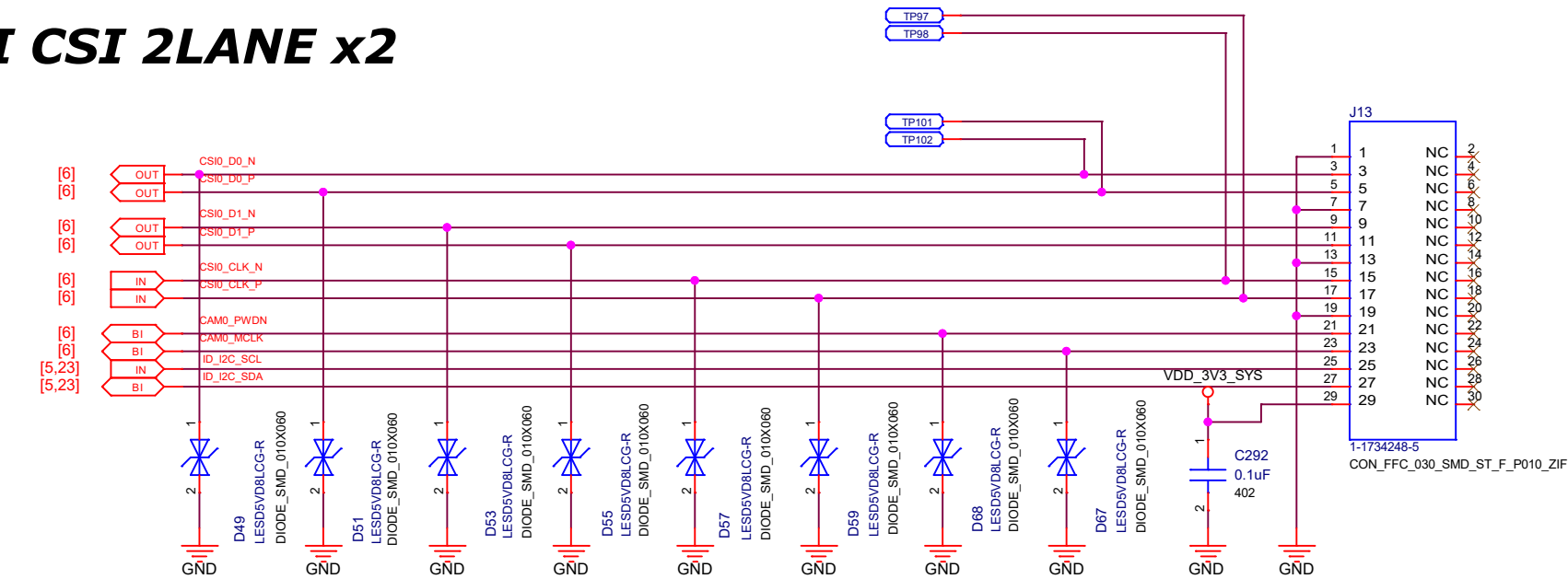
 armsom		ArmSoM				
Project:	AIM_IO_Board					
File:	Gigabit Ethernet POE					
Date:	Tuesday, December 10, 2024				Rev:	V1.1
Designed by:	Park	Reviewed by:	<Checker>	Sheet:	16 of 25	



DisplayPort



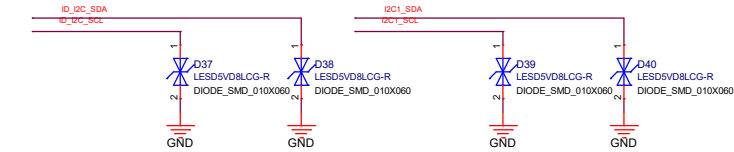
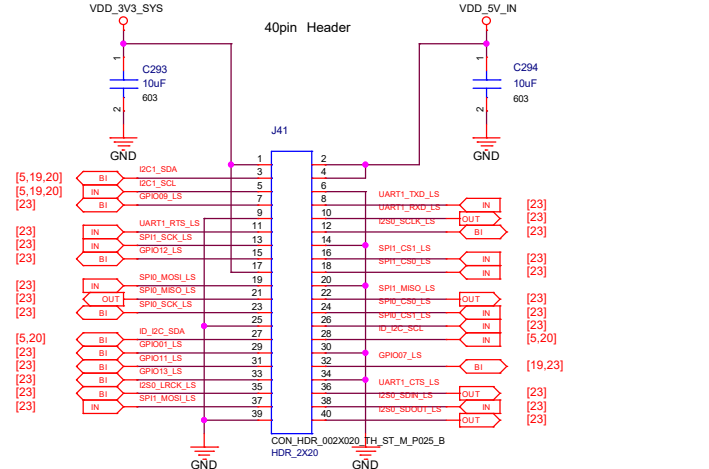
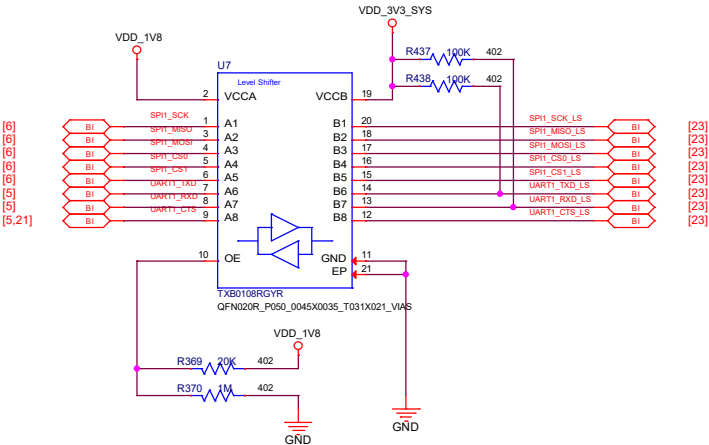
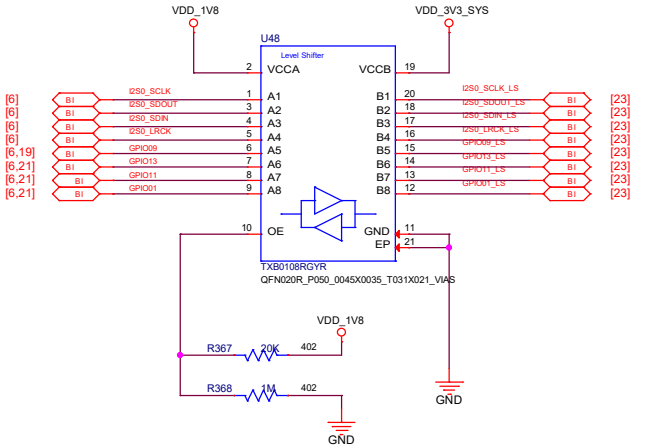
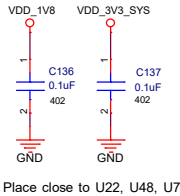
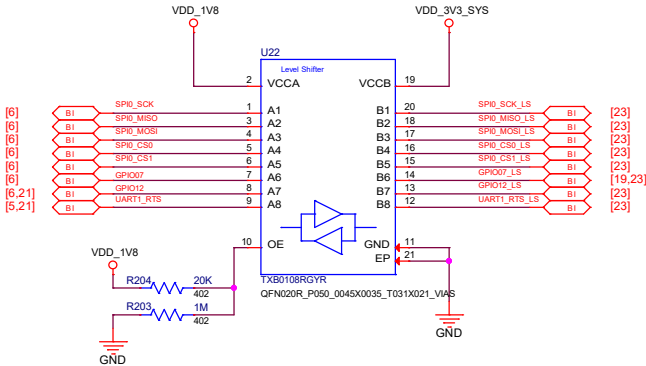
MIPI CSI 2LANE x2

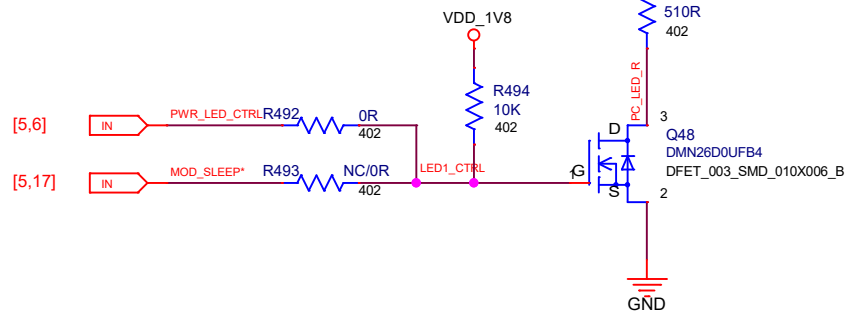
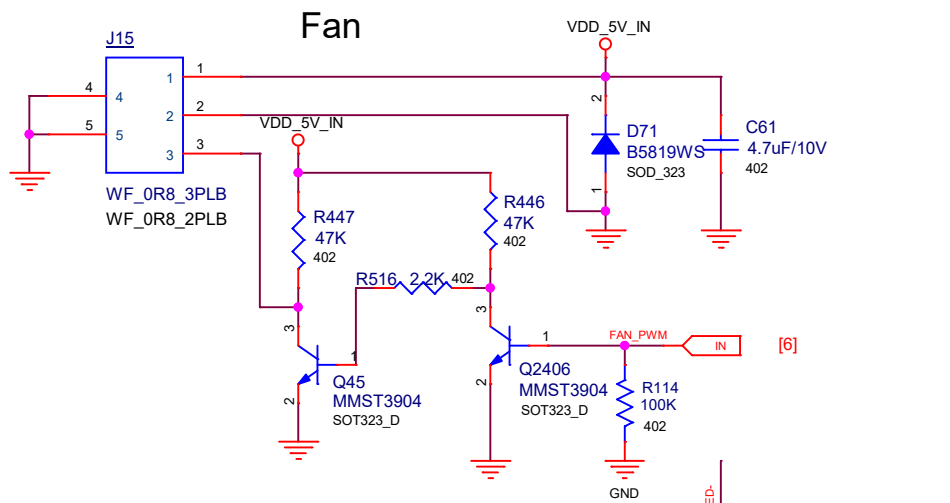


 armsom		ArmSoM	
Project:	AIM_IO_Board		
File:	CAM_0_2_Connector		
Date:	Friday, November 29, 2024	Rev:	V1.1
Designed by:	Park	Reviewed by:	<Checker>
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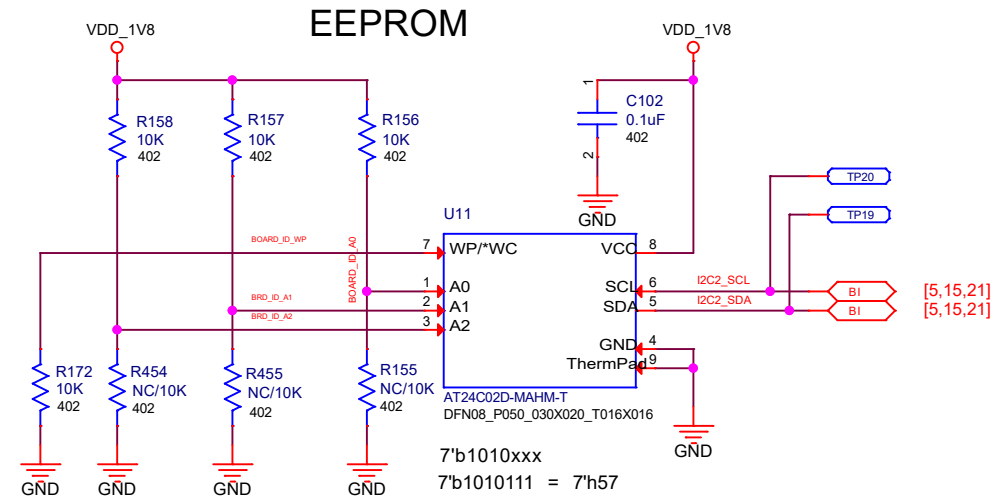
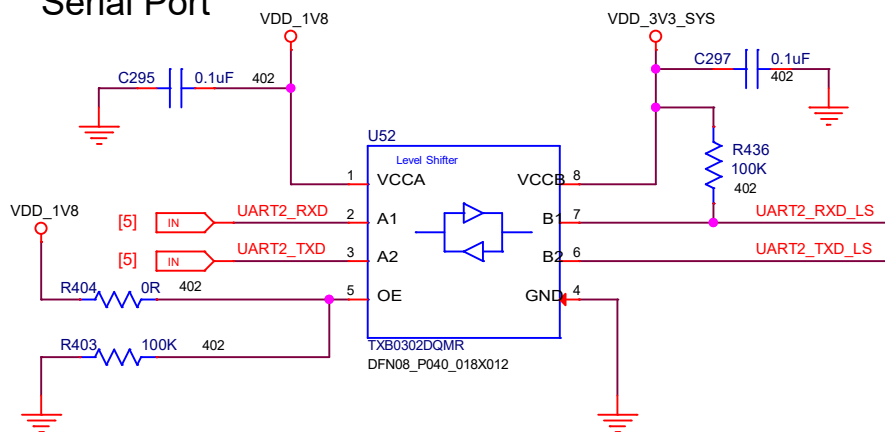
armsom		ArmSoM	
Project:	AIM_IO_Board		
File:	TF		
Date:	Tuesday, December 10, 2024	Rev:	V1.1
Designed by:	Park	Reviewed by:	<Checker>
		Sheet:	22 of 25

40PIN Header

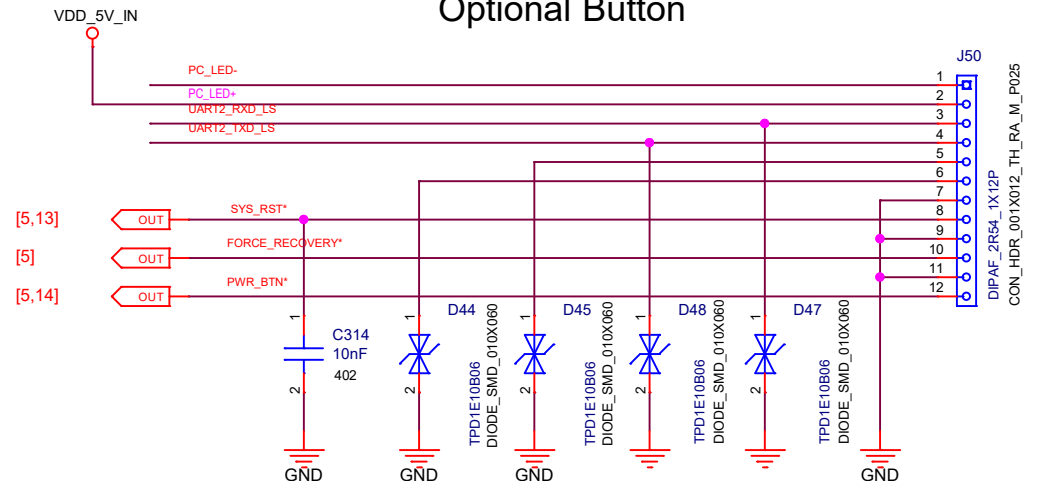




Serial Port



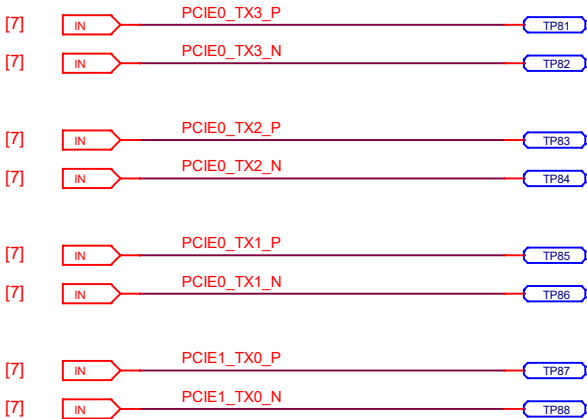
Optional Button



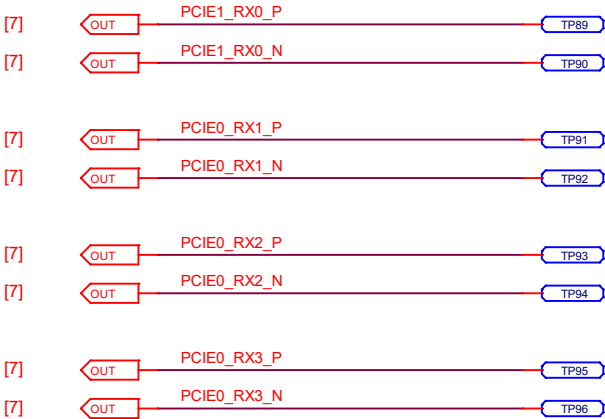
		ArmSoM	
Project:	AIM_IO_Board		
File:	Fan_Debug_EEPROM		
Date:	Tuesday, December 10, 2024		Rev: V1.1
Designed by:	Park	Reviewed by: <Checker>	Sheet: 24 of 25

LOOP BACK

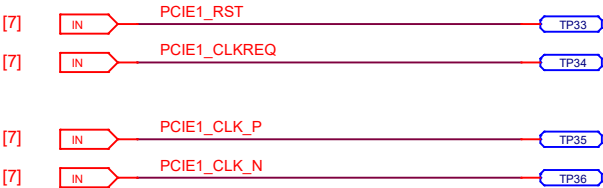
PCIe LOOP BACK TO TEST UNUSED PCIe SIGNALS



PLACE ALL TESTPOINTS ON THE BOTTOM OF THE BOARD FOR TEST ONLY!!!



PLACE TESTPOINT ON THE BOTTOM OF THE BOARD



Mounting holes for Module

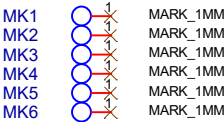
MEC7
SMTSO2565CTJ
VIA4_2MM



MEC8
SMTSO2565CTJ
VIA4_2MM



MARK



 armsom		ArmSoM			
Project:	AIM_IO_Board				
File:	LOOP BACK				
Date:	Tuesday, December 10, 2024			Rev:	V1.1
Designed by:	Park	Reviewed by:	<Checker>	Sheet:	25 of 25