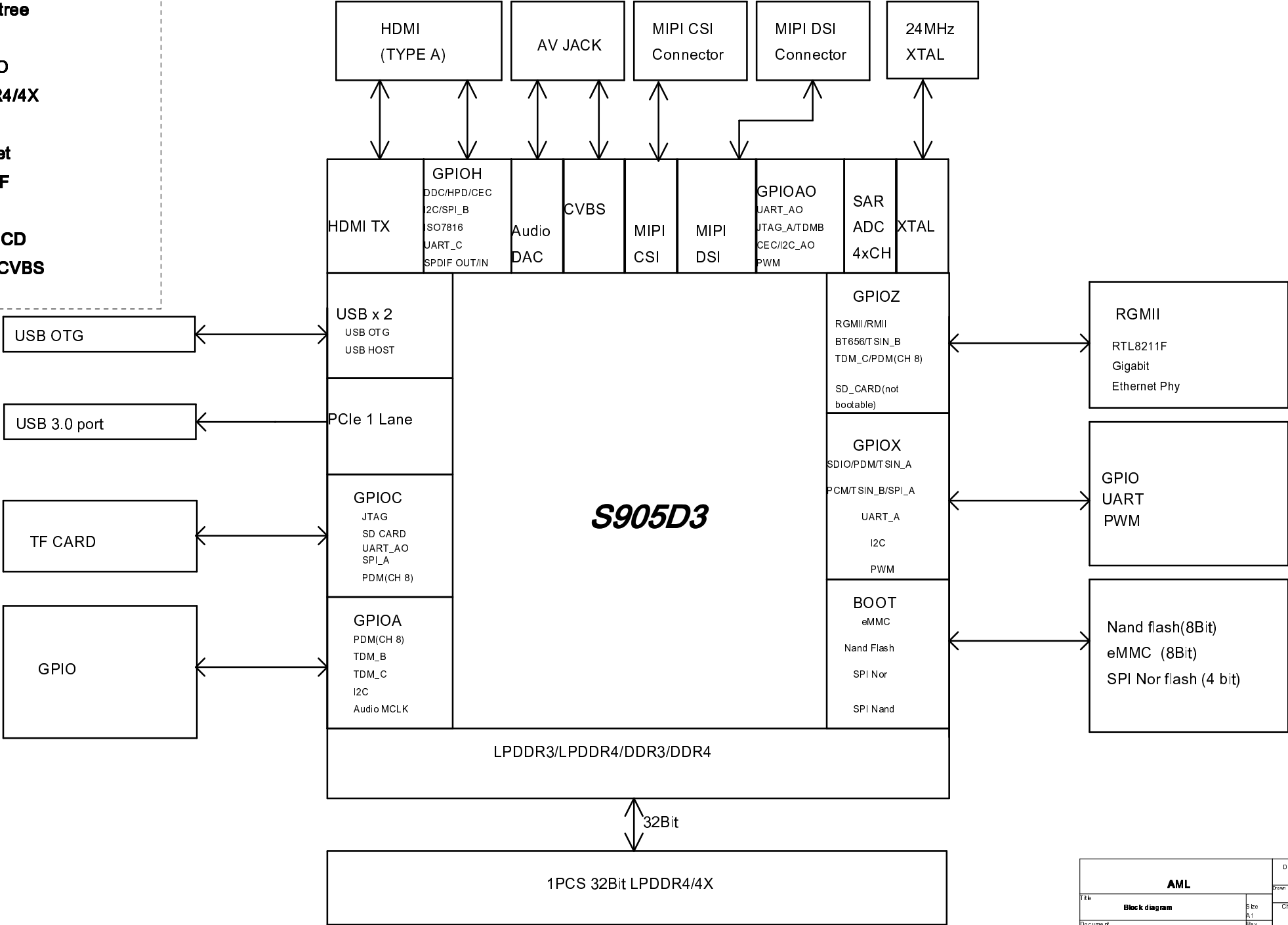
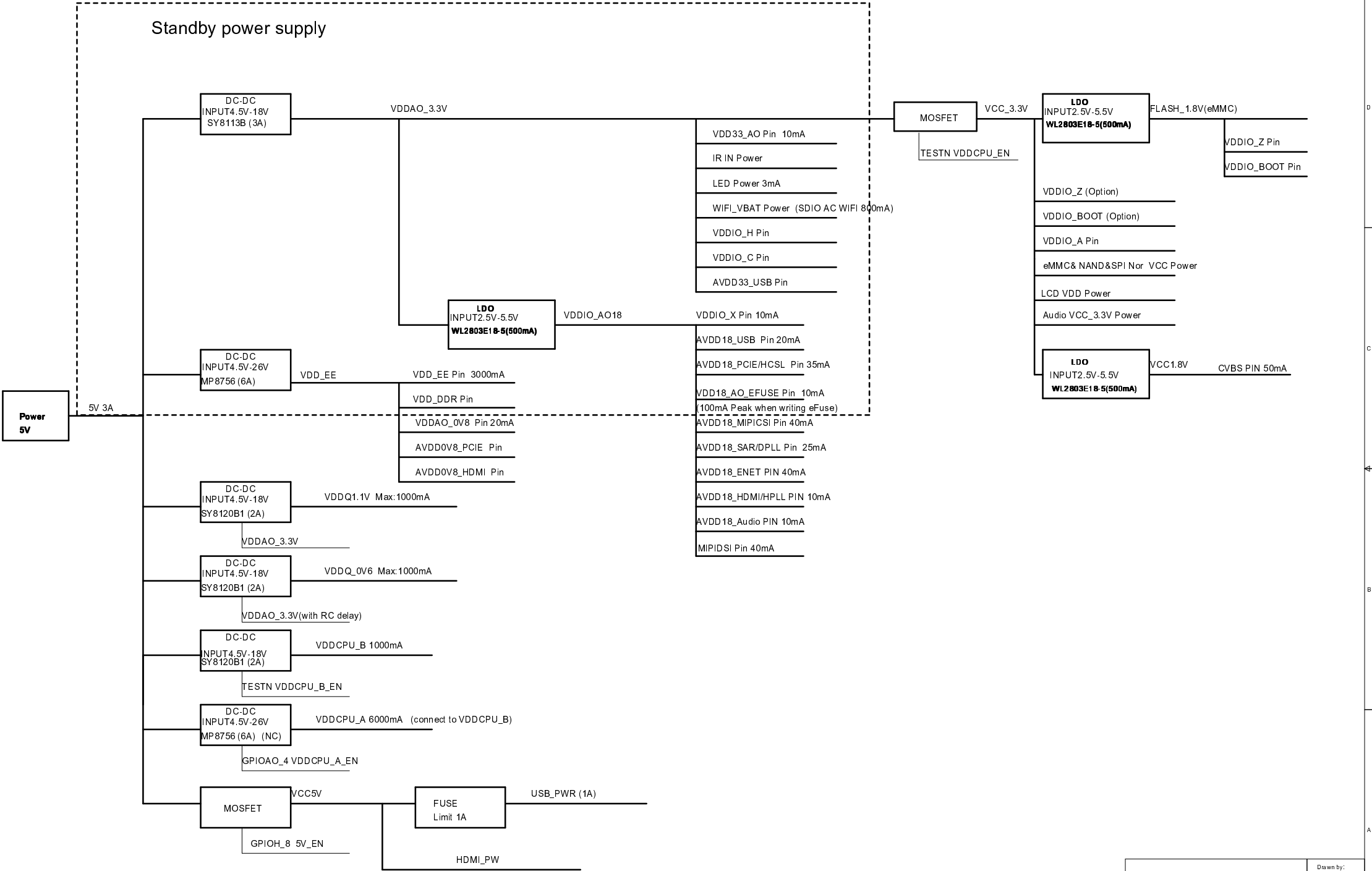


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- 00- Block diagram
- 00- Power tree
- 01- Power
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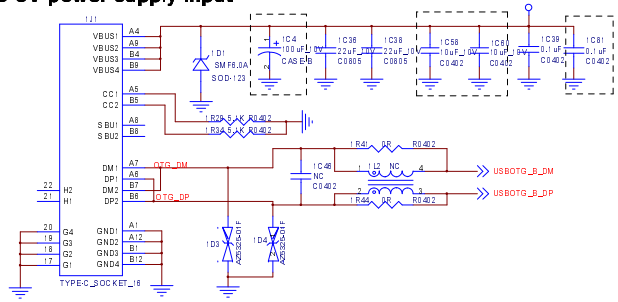
S905D3 Diagram



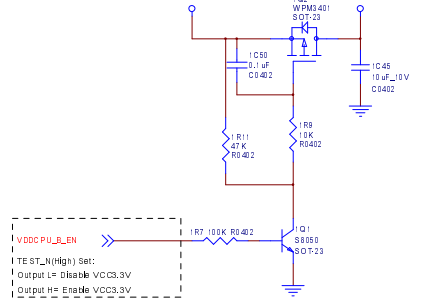


DC 5V power supply input

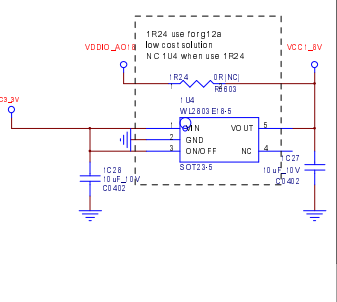
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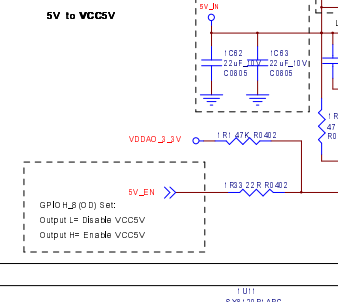
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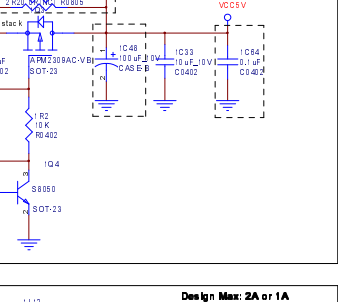
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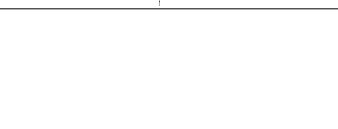
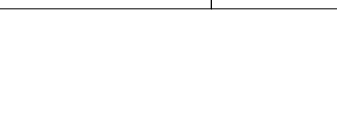
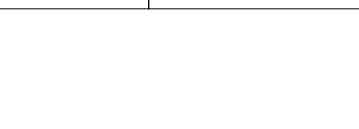
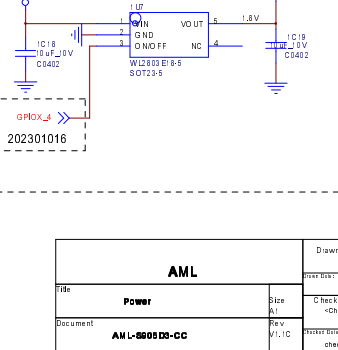
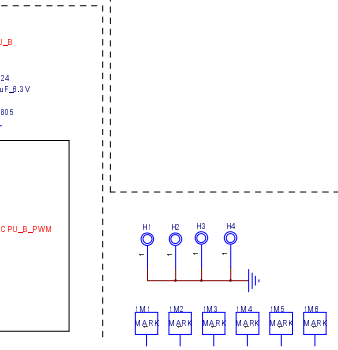
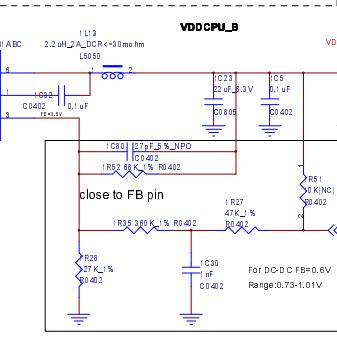
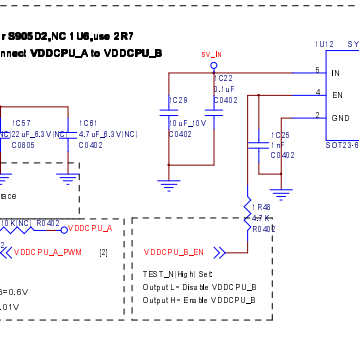
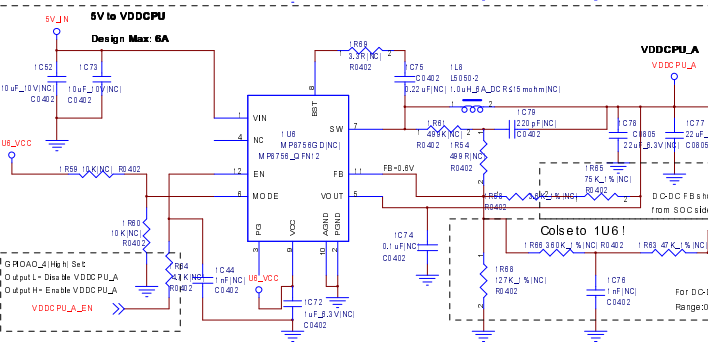
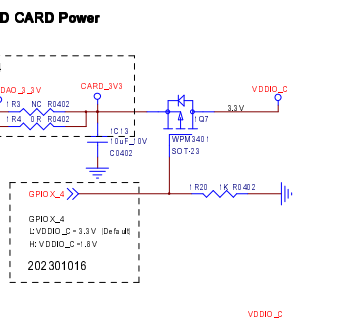
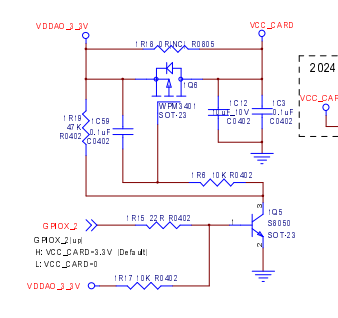
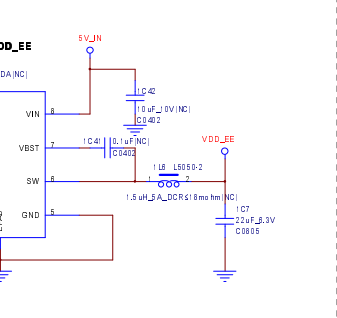
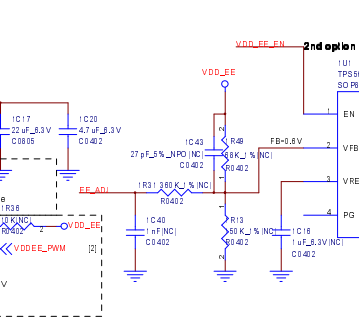
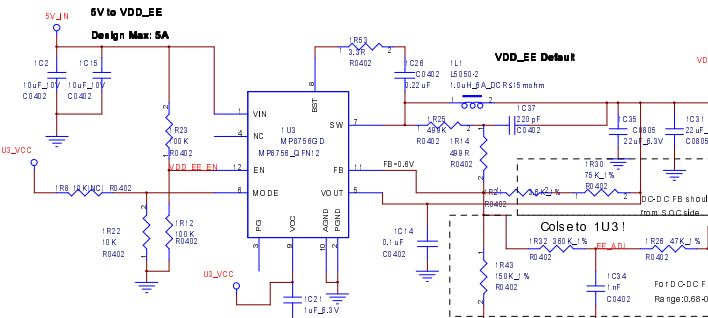
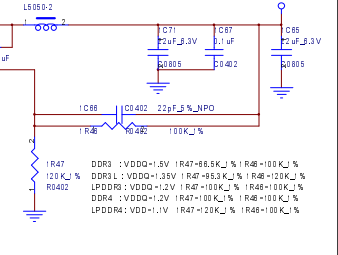
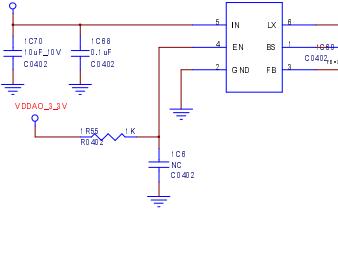
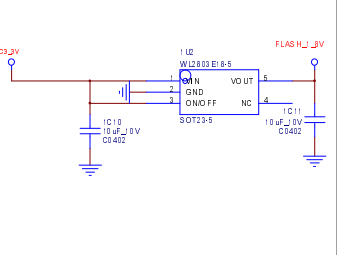
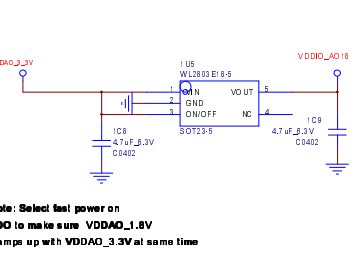
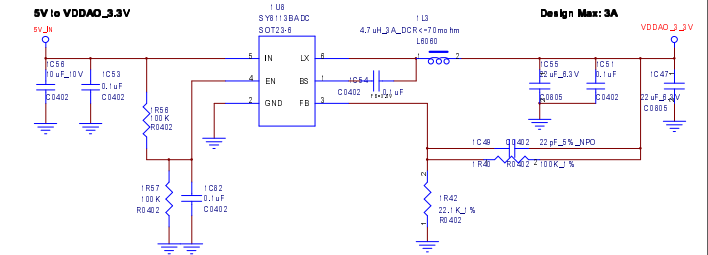
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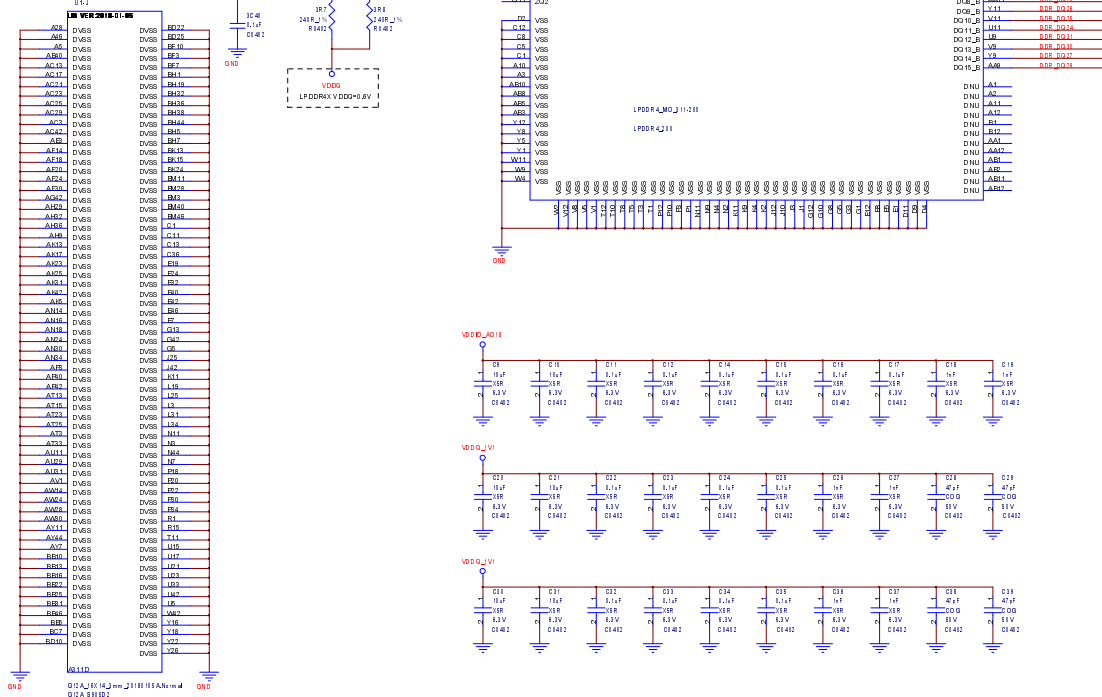
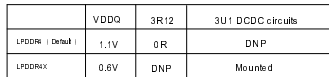
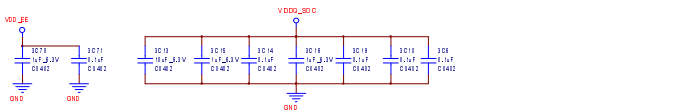
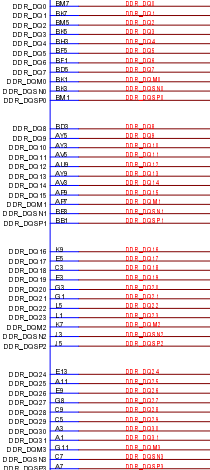
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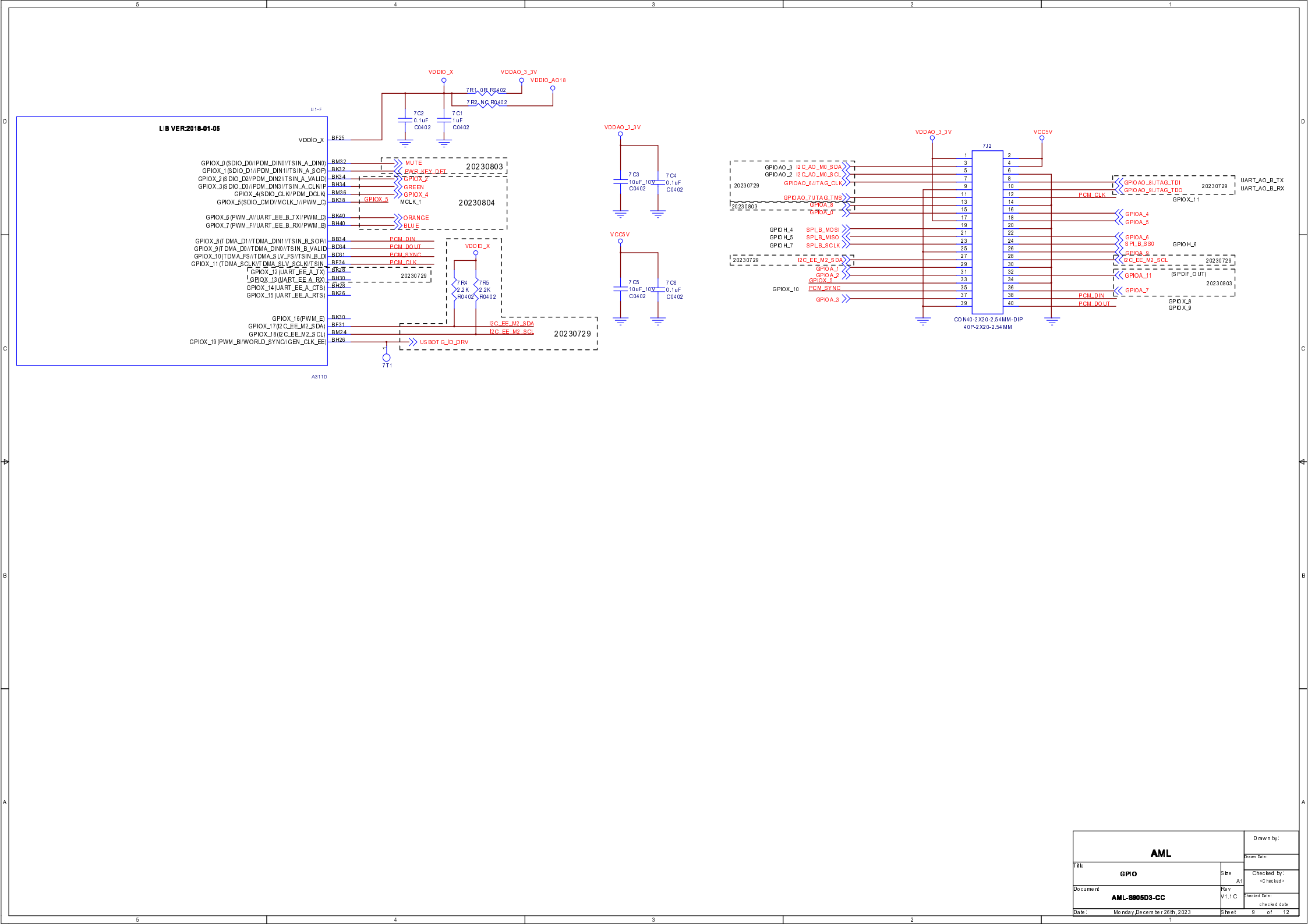


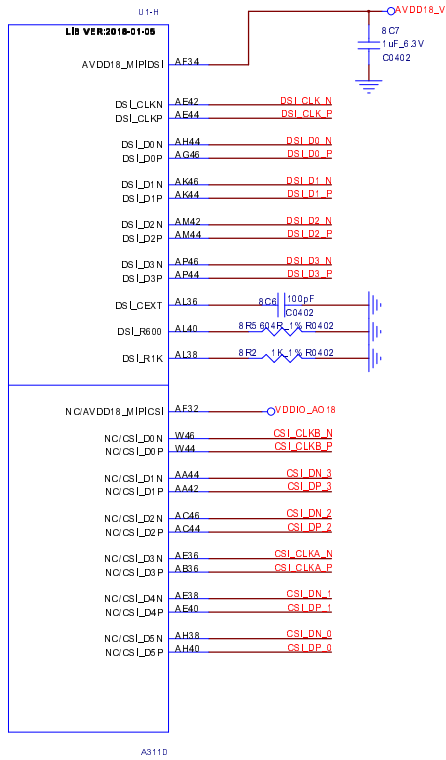
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HDMI output

LIB VER:2018-01-05

GPIOH_0(OD5V)_HDMITX_SDA/I2C_EE_M3_SD
GPIOH_1(OD5V)_HDMITX_SCL/I2C_EE_M3_SC
GPIOH_2(OD5V)_HDMITX_HPD/I2C_EE_M1
GPIOH_3(OD5V)_I2C_EE_M1_SCL/AO_CEC_A

GPIOH_4(SPDIF_OUT/IUART_EE_CRTS)/SPLB_M
GPIOH_5(SPDIF_IN/IUART_EE_CTS)/SPLB_M

GPIOH_6(IS07816_CLK/IUART_EE_CRX)/SPLB
GPIOH_7(IS07816_DATA/IUART_EE_CTX)/SPLB
GPIOH_8(OD5V)

U1-0

VDDIO_H

AW34

W40

H40

AB38

G37

AVDD0V8_HDMI

AVDD33_HDMI

AVDD18_HDMI

HDMI_CEXT

HDMI_REXT

AVSS_HPLL

HDMITX_CKN

HDMITX_CKP

HDMITX_SN

HDMITX_DP

HDMITX_1N

HDMITX_1P

HDMITX_2N

HDMITX_2P

AVDD18_CVBS

CVBS_COMP

CVBS_VREF

CVBS_RSET

CVBS_JOUT

AVSS_CVBS

AVDD18_AUDIO

LOLP

LOLN

LORP

LORN

REFP

VMID

AVSS_AUDIO

A311D

Short GND pin of REFP/VMID filter caps & AVSS_AUDIO together, then to GND (single point connection)

AVDD0V8_HDMI

AVDD33_HDMI

AVDD18_HDMI

HDMI_CEXT

HDMI_REXT

AVSS_HPLL

HDMITX_CKN

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AVDD33_HDMI

AVDD18_HDMI

HDMI_CEXT

HDMI_REXT

AVSS_HPLL

HDMITX_CKN

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HDMITX_SN

HDMITX_DP

HDMITX_1N

HDMITX_1P

HDMITX_2N

HDMITX_2P

AVDD18_CVBS

CVBS_COMP

CVBS_VREF

CVBS_RSET

CVBS_JOUT

AVSS_CVBS

AVDD18_AUDIO

LOLP

LOLN

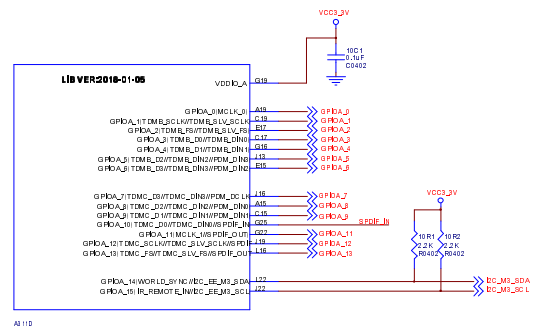
LORP

LORN

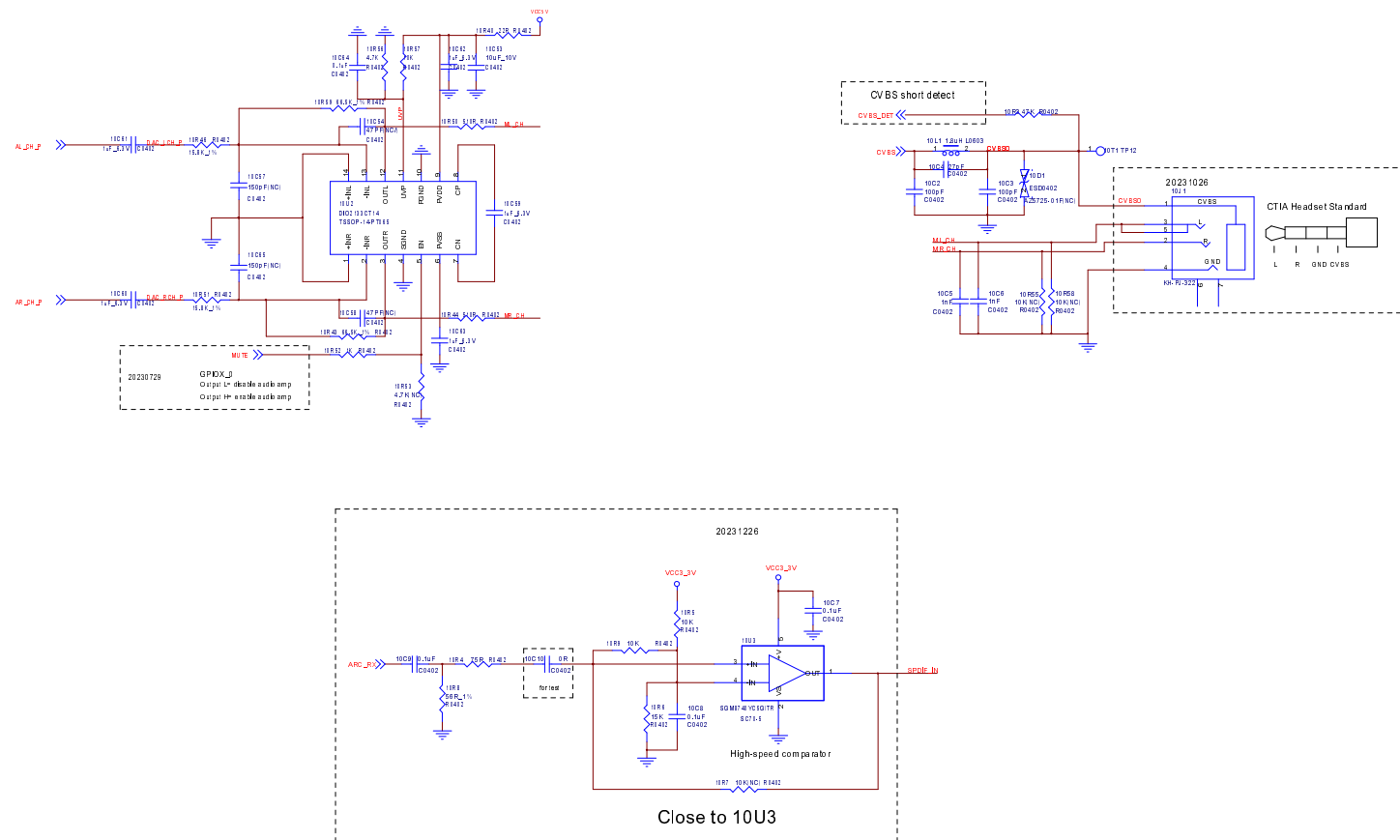
REFP

VMID

AVSS_AUDIO



CVBS out



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		(Signature)	
Title Audio	Size A1	Checked by: 	
Document: AML-8803D8-CC	Rev V1.1C	Checked: 	
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